

ENGINEERING SPECIFICATIONS

TFT COLOR LCD MODULE

TM035QV-67P06D

- 8.9cm (3.5 inch) diagonal
- Quarter VGA resolution (240 x 320 pixels)
- 6 bits x RGB interface
- With LED Backlight unit
- Glare surface type
- With touch panel

(TENTATIVE)

Ver.2

Sep. 25, 2003

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NOTICES

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- 2. This product is developed and produced for usage onto normal electronic products (office automation equipments, communication peripherals, electric appliance products, game machines, etc.) and is not suitable for applications which need extremely high reliability and extreme safety (aero- or space-use machines, control equipments for nuclear power, life keeping equipments, etc.).
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- 4. Please use this product correctly according to operating conditions and precautions for use stated in this document.

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- 5. This product is not designed to withstand against radiant rays.
- 6. It is strictly prohibited to copy or publish a part or whole of this document without our prior written approval.

REVISIO	N HISTORY					
DATE	REVISION NO.	PAGE	DESCRIF	PTIONS		
Jun.20 '03	Ver.1	-	Initial Release			
Sep.25 '03	Ver.2	2,3,4,6	VCOM DC(typ.1.6V→1.85V), Ar Touch panel surface hardne			ıre
			T	<u> </u>		
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MECHANICAL CHARACTERISTICS

Ta=25 degC

		•
ITEM	SPECIFICATION	UNIT
Module size	66.2(W) x 91.0(H) x 4.7 Max.(t)	mm
Resolution	240 x R.G.B(H) x 320(W)	pixel
Sub pixel pitch	0.0745(W) x 0.2235(H)	mm
Pixel pitch	0.2235(W) x 0.2235(H)	mm
Active viewing area	53.64(W) x 71.52(H)	mm
Bezel opening area	57.6(W) x 75.0(H)	mm
Weight	50 ± 2	g
Touch panel	Glare type (Surface Hardness : 3H)	

ELECTRICAL ABSOLUTE MAXIMUM RATINGS

Ta=25 degC

ITEM	ITEM		SYMBOL	MIN	MAX	UNIT	NOTE
		Digital	VDD	-0.3	4.0	V	
Power supply volta	200	Analog	Vcc	-0.3	6.0	V	
Fower supply void	_	Gate	VGH	-0.3	44	V	
		Gale	VGL	VGH-44	0.3	V	
Driving signal			VCOM	-1.5	5	V	
	Digital		VIN	Vss-0.3	VDD+0.3	V	
Input voltage	γ Volta	age	V0-V4	0	VCC+0.3	V	Note 1
Touc		panel		0	7.0	V	
LED current	•		ΙL	0	20	mA	

[Note 1] V0,V1,V2,V3,V4

ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS

Ta=25 degC

<u> </u>		-				
ITEM	SYMBOL	CONDITIONS	MIN	MAX	UNIT	NOTE
Ambient	TST	Storage	-20	60	degC	Note 1
temperature	TOP	Operation	-10	55		
Humidity	-	Ta=40 degC max.	-	85	%RH	No condensation
						Note 2
Vibration	-	Storage	-	TBD	G	
Shock	-	Storage	-	TBD	G	

[Note 1] Care should be taken so that the LCD module may not be subjected to the temperature beyond this specification.

[Note 2] Ta>40 degC: Absolute humidity shall be less than that of 85%RH/40 degC.

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ELECTRICAL CHARACTERISTICS for LCD

VDD=3.3V ,fV=60Hz ,fCLK=6MHz ,Ta=25 degC

ITEM		SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT	NOTE
	Digital	VDD		3.0	3.3	3.6	V	
Dower cumply yelt	Analog	VCC		4.75	5	5.25	V	Note 1
Power supply volta	_	VGH		14.3	15	15.7	V	Note 1
	Gate	VGL		-11	-10	-9	V	
Driving signal		VCOM AC		±2.4	±2.45	±2.5	V	Note 2
Driving signal		VCOM DC		1.55	1.85	2.15	V	Note 2
Input valtage	High level	VIH		0.8VDD	-	VDD	V	
Input voltage	Low level	VIL		Vss	-	0.2VDD	V	
		IDD	VDD=3.3V	-	0.2	1.0	mA	
		ICC	Vcc=5V	-	7.0	10	mA	Note 2
Power supply curr	ent	IGH	VGH=15V	-	0.03	0.1	mA	Note 3
		IGL	VGL=-10V	-	0.07	0.2	mA	
	ICOM(AC)	VCOMAC=	_	150	250	mA	Note 3	
		IOCIVI(AC)	2.45V		100	200	1117 (I 0-peak

^{*}Recommended Control IC: LC272C1B-VD6/VC9(SANYO)

- [Note 1] 1) Turn on or off the power supply with simultaneously or the following sequence. Turn on :VDD \rightarrow VGL \rightarrow VGH \rightarrow VCC \rightarrow (V0-V4) \rightarrow Logic input Turn off : Logic input \rightarrow (V0-V4) \rightarrow VCC \rightarrow VGH \rightarrow VGL \rightarrow VDD
 - 2) The input signal of "OE" Terminal(pin No.18) must be high voltage when turning on the power supply, and it is held until more than double vertical periods after VDD is turned on complete. After then it must be held low voltage.
- [Note 2] VCOM AC should be alternated on VCOM DC every 1 horizontal period and 1 vertical period. VCOM DC bias should be adjusted so as to minimize flicker or maximum contrast every each module.(Condition:VGH=15V,VGL=-10V,VCC=5V)

[Note 3] Typ. value : display pattern is 64 gray scale bar.

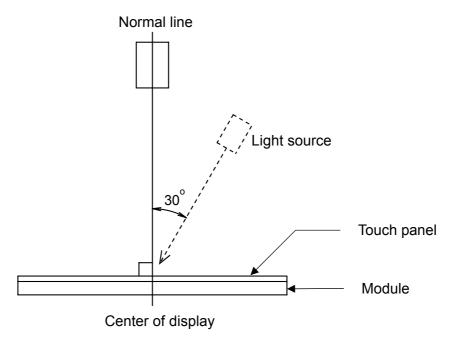
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OPTICAL CHARACTERISTICS with Touch Panel

Ta=25 degC, VDD=3.3V, fv=60Hz,IL=18mA

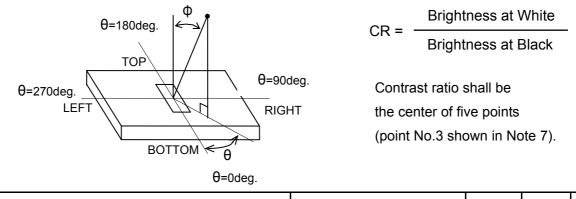
ITENA		CVMDOL	С	ONDI	ΓIONS	NAINI	TVD	MAY	LINIT	NOTE	
ITEM		SYMBOL	Back light		Other	MIN	TYP	MAX	UNIT	NOTE	
Brightness		В	On		Ф=0 deg.		(100)	ı	cd/m ²	Note 5,7,8	
Brightness unifo	rmity	δΒ	On		Ф=0 deg.	-	-	1.6	-	Note 6,7,8	
Contrast ratio		CR	On		Ф=0 deg.	60	90	•	_	Note 2,8	
Contrast ratio		CK	Off		Ф=0 deg.	-	12	•	_	Note 2,0	
					Ф= 0 deg.	-	50	•		Note 1,2,4,8	
Viewing angle		ФОг	On	CR>2	Ф= 90 deg.	ı	40	-	deg.		
range			OII	01172	Ф=180 deg.	1	50	-	ueg.		
					Φ=270 deg.	ı	40	-			
Response time	Rise	tr	On		Φ=0 deg.	-	10	-	ms.	Note 3,4,8	
response time	Fall	tf	OII		Ψ=0 deg.	-	10	-	1113.	14016 3,4,0	
		Х	Off		Ф=0 deg.	-	(0.315)	-	_	Note 9	
White Chromati	city	у	Oli		Ψ=0 ucg.	-	(0.356)	-	_	NOIC 9	
vvinte Cilionaticity		Х	On		Ф=0 deg.	-	(0.302)	-	_	Note 4,8	
		у		Ψ-0 u c g.		-	(0.310)	-		·	
Reflection Raito)	R	Off		Ф=0 deg.	-	(8)	-	%	Note 4,9,10	

The measuring method of Backlight Off is shown by the following figure.



[Note 1] Φ and θ

[Note 2] Contrast ratio "CR" is defined as :



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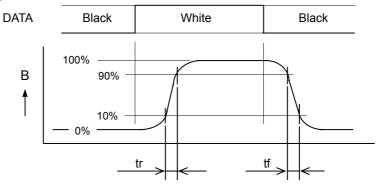
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[Note 3] Response time



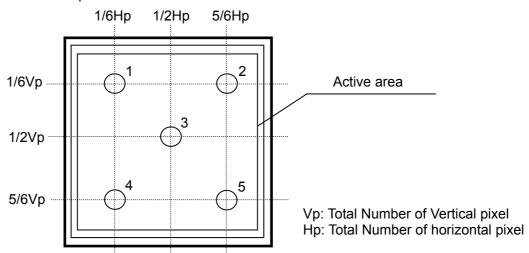
[Note 4]This shall be measured at center (point No.3 shown in Note 7).

[Note 5] The brightness shall be measured at center (point No.3 shown in Note 7).

[Note 6] The brightness uniformity " δ B" is defined as :

$$\delta \ B = \frac{\text{Maximum brightness of five points}}{\text{Minimum brightness of five points}}$$

[Note 7] Measurement points



[Note 8] Measurement condition (Back light)

- (1) Measurement equipment: BM-5A
- (2) Ambient temperature Ta: $25 \pm 2 \text{ degC}$
- (3) LCD: All pixels are WHITE, VDD=3.3V, fV=60Hz
- (4) IL=18 mA

[Note 9] The measurement shall be taken in a D65 light source.

[note10] Reflection Ratio is defined as :

R = Reflectance Factor white board x
$$\frac{\text{Lum center (on LCD surface)}}{\text{Lum white board}}$$

BACKLIGHT CHARACTERISTICS

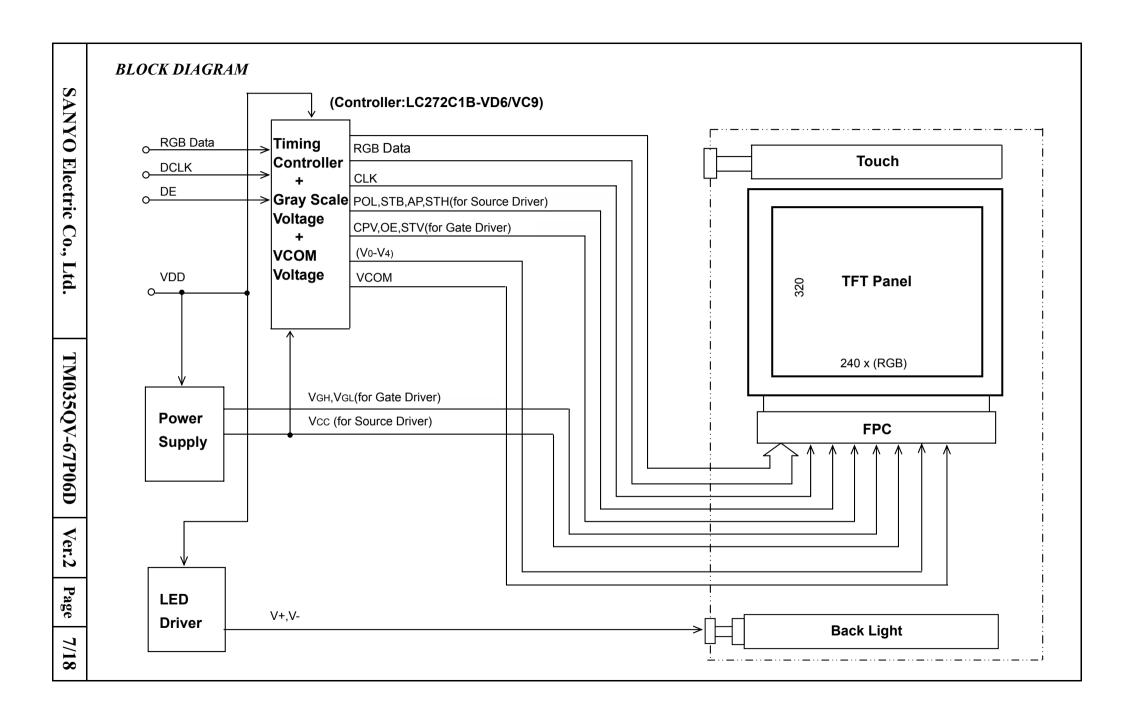
Ta=25 degC

ITEM	SYM.	CONDITION	MIN	TYP	MAX	UNIT	NOTE
LED Current	IL		-	18	20	mA	
LED Voltage	VL		-	3.6	3.95	V	for LED1p ,IL=18mA

TOUCH PANEL CHARACTERISTICS

Ta=25 degC

ITME		MIN	TYP	MAX	UNIT	NOTE
Input Voltage		4.5	5	5.5	V	
Resistance	Χ	400	540	1000	ohm	
betweenterminal	Υ	200	330	650	ohm	
Linearity		-	ı	1.5	%	
Chattering		-	ı	10	ms	
Surface hardness		3	ı	-	Н	Hardness of pencil
Operation force		-	-	80	gf	
Insulation resistance		20	-	-	Mohm	DC25V



INTERFACE PIN CONNECTIONS

LCM: CN1

PIN NO.	SYMBOL	FUNCTION
1	GND	Ground
2	VCOM	Power supply of common electrode
3	GND	Ground
4	V0	Gamma control (for use of external gamma setting)
5	V1	Keep the following relation.
6	V2	VSS+0.1V <u><</u> V4 <u><</u> V3 <u><</u> V2 <u><</u> V1 <u><</u> V0 <u><</u> VCC-0.1V
7	V3	 OR
8	V4	Non connection (for use of internal gamma setting)
9	GAM	Gamma selection switch(VDD:external, GND:internal)
10	COM	Non-connection
11	NC	Non-connection
12	INV	Data reversal signal(GND:Normal,VDD:Invert)
13	POL	Polarity reversal signal
14	STB	Data latch signal of source driver
15	AP	Power save signal of source driver
16	VDD	Power supply (3.3V)
17	CLK	Data clock
18	STH	Start signal of source driver
19	GND	Ground
20	B0	Blue data (LSB)
21	B1	Blue data
22	B2	Blue data
23	B3	Blue data
24	B4	Blue data
25	B5	Blue data(MSB)
26	G0	Green data (LSB)
27	G1	Green data
28	G2	Green data
29	G3	Green data
30	G4	Green data
31	G5	Green data (MSB)
32	R0	Red data (LSB)
33	R1	Red data
34	R2	Red data
35	R3	Red data
36	R4	Red data
37	R5	Red data (MSB)
38	GND	Ground
39	VCC	Power supply (5V)
40	CPV	Clock signal of gate driver
41	VGL	Power supply of gate driver(low level)
42	OE	Output enable of gate driver
43	VGH	Power supply of gate driver(High level)
44	STV	Start signal of gate driver
45	GND	Ground

CN1 : FPC(Pitch 0.5mm)

Suitable mating connector: FH12S-45S-0.5SH(lower terminal type) (HIROSE)

FH12A-45S-0.5SH(upper terminal type) (HIROSE)

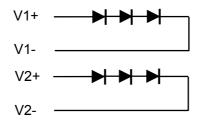
[Note 1] Valid synchronous signals are DCLK and DE. HSYNC and VSYNC are not used

Back Light: LEDCN1

PIN NO.	SYMBOL	FUNCTION
1	V1-	Ground(LED 1 cathode)
2	V1+	LED 1 voltage(anode)
3	V2-	Ground(LED 2 cathode)
4	V2+	LED 2 voltage(anode)

LEDCN1 : FPC(Pitch 0.5mm)

Suitable mating connector: 04FLH-SM1-TB (JST)



Circuits of Back Light

Touch Panel: TPCN1

PIN NO.	SYMBOL
1	RIGHT
2	BOTTOM
3	LEFT
4	TOP

TPCN1:FPC(Pitch 1.0mm)

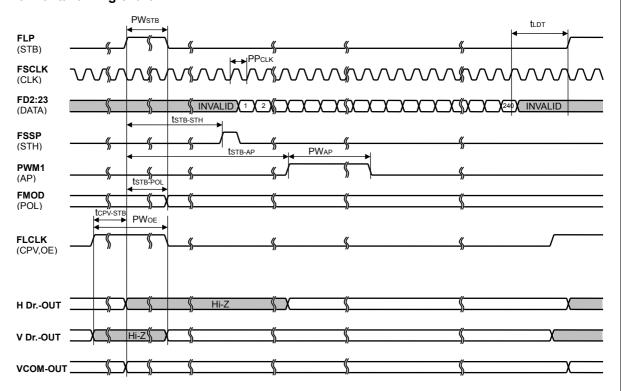
INTERFACE SIGNAL TIMING PARAMETERS

Timing characteristic

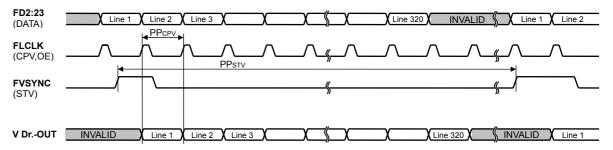
(Ta=25degC, VDD=3.3V, VCC=5.0V)

Parameter	Symbol	Min.	Тур.	Max.	Unit
Clock frequency	PPclk	5.4	-	7.2	MHz
Last data timing	tLDT	2	-	-	CLK
STB frequency	РРѕтв	16.5	-	20.0	kHz
STB pulse width	РWsтв	550	-	-	ns
STB-STH time	tstb-sth	4	-	-	CLK
STB-AP time	tstb-ap	10	-	-	us
AP pulse width	PWAP	15	-	-	us
STB-POL time	tstb-pol	40	-	-	ns
CPV-STB time	tCPV-STB	1	3	-	us
STV frequency	PPstv	50	-	65	Hz
Clock frequency	PPcpv	16.5	-	20	kHz

Horizontal timing chart



Vertical timing chart



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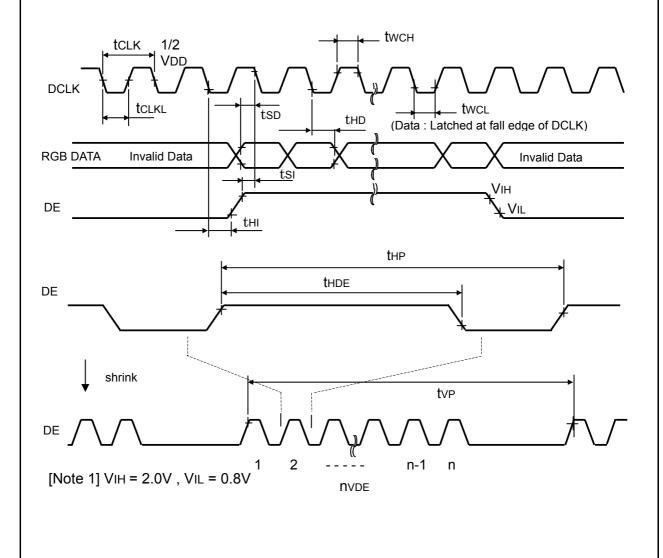
INTERFACE SIGNAL TIMING PARAMETERS (DE_MODE) FOR TIMING CONTROLLER(LC272IB-VD6/VC9)

PARAMETER		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
DCLK	Frequency	fCLK	-	ı	10	MHz	tclk=1/fclk
	Width-Low	twcL	10	ı	ı	ns	
	Width-High	twch	10	ı	1	ns	
	Duty	D	0.40	0.50	0.60	ı	D=tclkl/tclk
	Setup Time	tsı	10	ı	17	ns	for DCLK
	Hold Time	tнı	10	ı	17	ns	
DE	Horiz. Period	tHP	261	-	480	tclk	
	Horiz. DE	tHDE	-	240	ı	tclk	
	Vert. Period	t∨P	NVDE+5	ı	4095	tHP	fv=60Hz Typ.
	Vert. DE	n vde	128	320	-	n	
DATA	Setup Time	tsp	10	ı	-	ns	for DCLK
DAIA	Hold Time	thd	10	1	-	ns	

[Note 1] fH (Horizontal Frequency) = $1/T_{hp}$ fv (Vertical Frequency) = $1/T_{vp}$

INTERFACE SIGNAL TIMING DIAGRAM (DE MODE)

*Refer to Timing Controller(LC272IB-VD6/VC9)



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RELATIONSHIP BETWEEN INPUT DATA AND DISPLAY COLOR

	INPUT DATA	R DATA					G DATA						B DATA							
DISPLAY		MS	MSB LSI		.SB	MSB LSB					MSB LSB									
COLOR		R5	R4	R3	R2	R1	R0	G5	G4	G3	G2	G1	G0	В5	В4	ВЗ	В2	В1	В0	
	BLACK	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	
	RED(63)	Н	Н	Н	Н	Н	Н	L	L	L	L	L	L	L	L	L	L	L	L	
	GREEN(63)	L	L	L	L	L	L	Н	Н	Η	Η	Η	Н	L	L	L	L	L	L	
BASIC	BLUE(63)	L	L	L	L	L	L	L	L	L	L	L	L	Η	Н	Н	Н	Η	Н	
COLOR	CYAN	L	L	L	L	L	L	Н	Н	Η	Η	Η	Η	Η	Н	Н	Н	Η	Н	
	MAGENTA	Н	Н	Τ	Τ	Н	Η	L	L	L	L	L	L	Η	Н	Н	Н	Н	Н	
	YELLOW	Н	Н	Τ	Τ	Η	Η	Н	Н	Η	Η	Η	Η	L	L	L	L	L	L	
	WHITE	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	Н	
	BLACK	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	
	RED(1)	L	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	
	RED(2)	L	L	L	L	Н	L	L	L	L	L	L	L	L	L	L	L	L	L	
RED	:																			
	:																			
	RED(61)		<u>H</u>	Н	Η:	Ŀ	Н	Ŀ	L	Ŀ	Ŀ	Ŀ	L	Ŀ	L	L	Ļ	Ŀ	Ŀ	
	RED(62)		H	H:	H:	H:	L	L	L	L	L	L	L	L	L	L	L	L	L	
	RED(63)	Н	H	I.	Η.	Ŧ.	Н	Ŀ	L	L	L	L	L	L	L	L	L	L	Ŀ	
	BLACK	L	<u>L</u>	L	L	Ļ	L	L	L	L	Ŀ	L	L	Ŀ	L	L	L	L	L	
	GREEN(1)	Ŀ	<u>L</u>	L	L	L	Ŀ	Ŀ	L	L	L	_ :	H	L	L	L	L	L	Ŀ	
	GREEN(2)	L	L	L	L	L	L	L	L	L	L	Н	L	L	L	L	L	L	L	
GREEN	:							:							:					
	:																			
	GREEN(61)	L	<u>L</u>	L	L	L	L	Η	Н	Η	Н	L	Н	L	L	L	L	L	L	
	GREEN(62)	L	L	Ŀ	Ŀ	Ļ	<u>L</u>	Н	H:	Η:	Η:	Η:	L.	L	Ŀ	L	Ļ	Ŀ	L	
	GREEN(63)	L	<u>L</u>	L	L	L	L	I.	Н	H	Н	H	Н	L	L	L	L	L	L	
	BLACK	L	L	Ŀ	Ŀ	Ļ	<u>L</u>	Ļ	L	Ŀ	Ŀ	Ŀ	L	Ŀ	Ŀ	L	Ļ	Ŀ	L.	
	BLUE(1)	L	<u>L</u>	Ŀ	Ŀ	Ļ	L	Ŀ	L	Ŀ	Ŀ	Ļ	L	Ŀ	L	L	Ļ	L	H.	
	BLUE(2)	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Н	L	
BLUE	:							<u> </u>						:						
	:							:						:						
	BLUE(61)	L-	<u>L</u>	L	L	L	L	L	L	L	L	L	L	Η:	Н	Η:	Η:	L	Η-	
	BLUE(62)	H	<u>L</u>	L	L	L	L	L	L	L	L	L	L	I:	Ξ:	ΞΞ	I:	H:	L	
	BLUE(63)	L	L	L	L	L	LL	L	L	L	L	L	L	Н	Н	Н	Н	Н	Н	

[Note 1] Color(n) --- 'n' indicates gray scale step.

LEFT

RELATIONSHIP BETWEEN INPUT DATA AND DISPLAY POSITION

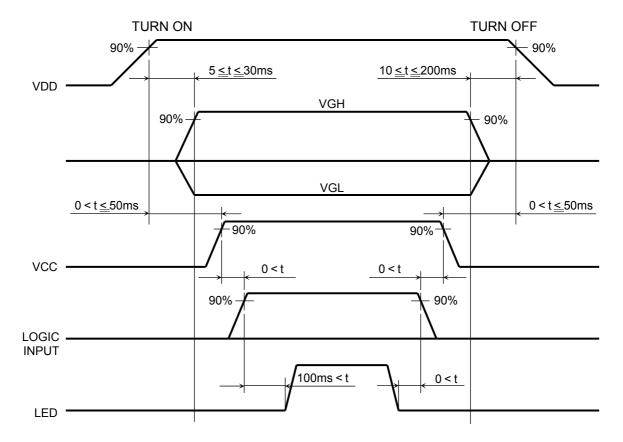
TOP

1.2 1.239 1.240 1.1 2.1 2.2 2.239 2.240 3.1 **RIGHT** Vp∙Hp R G B 319.1 319.240 320 · 1 320.2 320 • 239 | 320 • 240 • • • •

BOTTOM

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POWER ON/OFF SEQUENCE REQUIREMENT



Turn on :VDD \rightarrow VGL \rightarrow VGH \rightarrow VCC \rightarrow Logic input Turn off : Logic input \rightarrow VCC \rightarrow VGH \rightarrow VGL \rightarrow VDD

When the power is off, logic input must be kept at either low level or high impedance.

PRECAUTIONS (INSTRUCTIONS FOR SAFE AND PROPER USE)

1. Instructions for safety

- Please do not disassemble or modify LCD module to avoid the possibility of electric shock, damage of electronic components, scratch at display surface and invasion of foreign particles. In addition, such activity may result in fire accident due to burning of electronic component.
 - LCD module disassembled or modified by customer is out of warranty.
- Please be careful in handling of LCD module with broken glass. (2) When the display glass breaks, please pay attention not to injure your fingers. The display surface has the plastic film attached, which prevents dispersion of glass pieces, however touching broken edge will injure your fingers.
- (3) Please do not touch the fluid flown out of broken display glass. If the fluid should stick to hand or clothes, wipe off with soap or alcohol immediately and then wash it with water. If the fluid should get in eyes, wash eyes immediately with pure water for more than 15 minutes and then consult the doctor.
- (4) Please be careful to electric shock. Before handling LCD module, please switch off the power supply.

2. Instructions for designing

- Mounting of LCD
 - Please fix LCD module at all mounting flanges shown in this specification for installation onto system. The used screws should have proper dimensions. Furthermore, designing of mounting parts should be adequate so that LCD module is not warped or twisted, to achieve good display quality.
- (2) Mounting of LCD Please give careful consideration in designing which doesn't press down a Touch panel for installation onto system. If must be press it down that please leave it from the Bezel opening area more than 2mm.
- (3) Noise on power line Spike noise contained in power line causes abnormal operation of driving circuit and abnormal display. To avoid it, spike noise should be suppressed below VDD +/- 200mVp-p. (In any case, absolute maximum rating should be kept.)
- Power sequence Before LCD module is switched on, please make sure that power supply and input signals of system, testing equipment, etc. meet the recommended power sequence.
- Absolute maximum rating

Absolute maximum rating specified in this specification has to be kept in any case. It shows the maximum that cannot be exceeded.

Exceeding it may cause burning or non-recoverable break of electronic components in circuit. Please make system design so that absolute maximum rating is not exceeded even if ambient temperature input signal and components are varied.

- (6) Protection for power supply
 Please study to adapt protection for power supply against trouble of LCD module,
 depending on usage condition of system.
- (7) Protection cover and cut-off filter for ultraviolet rays
 When LCD module is used under severe condition like outdoor, it is
 recommended to use transparent protection cover over display surface to avoid
 scratches and invasion of dust and water. In addition, when LCD module is
 exposed to direct sun light for long time, use of cut-off filter for ultraviolet rays is
 also recommended. Please be careful not to get condensation.

3. Instructions for use and handling

(1) Protection against Static electricity

Semiconductors are easily damaged by static discharge. LCD module should be handled on conductive mat by person grounded with wrist strap etc. to avoid getting static electricity. Please be careful not to generate static electricity during operation.

- (2) Protection against dust and stain
 - LCD module should be handled in circumstance as clean as possible.
 - It is recommended to wear fingerstalls or ductless and soft gloves before handling to avoid getting dust or stain on display surface.
- (3) Protection film for display surface

It is recommended to remove protection film at nearly final process of assembling to avoid getting scratch or dust. To remove film, please pick up its edge with dull-head tweezers or cellophane tape at first and then remove film gradually taking more than 3 seconds. If film is removed quickly, static electricity may be generated and may damage semiconductors or electronic components.

- (4) Contamination of display surface
 - When display surface of Touch panel is contaminated, please wipe the surface softly with cotton swab or clean cloth.
- (5) Water drop on LCD surface
 - Please do not leave LCD module with water drop. When the display surface gets water drop, please wipe it off with cotton swab or soft cloth immediately, otherwise display surface will be deteriorated.
 - If water gets in inside of LCD module, circuit may be damaged.
- (6) Please make sure that LCD module is not warped or twisted at installation into system. Even temporary warp or twist may be the cause for failure.
- (7) Mechanical stress

Please be careful not to apply strong mechanical stress like drop or shock to LCD module. Such stress may cause break of display glass may be the cause for failure.

(8) Pressure to display surface

Please be careful not to apply strong pressure to display surface. Such pressure may cause scratches at surface or may be the cause of failure.

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(9) Protection against scratch

Please be careful not to hit, press or rub the display surface with hard material like tools. In addition, please do not put heavy or hard material on display surface, and do not stack LCD modules. Touch Panel at front surface can be easily scratched.

(10) Plugging in of connector

Please be careful not to apply strong stress to connector part of LCD module at plugging in or out, because strong stress may damage the inside connection. At plugging in connector, place LCD module on the flat surface and hold the backside of connector on LCD module. Please make sure that connector is plugged in correctly. Insecure connection may be the cause for failure during operation.

In addition, please be careful not to put the connecting cable between cabinet of system and LCD module at installing LCD module into system.

(11) Handling of FPC (Flexible Printed Circuit)

Please be careful not to pull or scratch FPC, because FPC or soldered part of FPC may be damaged consequently.

(12) Switching off before plugging in connector

Please make sure that power is switched off before plugging in connector. If power is on at plugging in or out, circuit of LCD module may be damaged. When LCD is switched on for test or inspection, please make sure that power supply and input signals of driving system meet the specified power sequence.

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(13) Temperature dependence of LCD display

Response speed (optical response) of LCD display is dependent on temperature. Under low temperature, response speed is slower.

Also brightness and chromaticity change slightly depending on temperature.

(14) Condensation

LCD module may get condensation on its display surface and inside in the circumstance where temperature changes much in short time.

Condensation can cause deterioration or failure. Therefore, please be careful not to get condensation.

(15) Remaining of image

Displaying the same pattern for long time may cause remaining of image even after changing the pattern. This is not failure but will disappear with time.

4. Instructions for storage and transportation

(1) Storage

Please store LCD module in the dark place of room temperature and low humidity in original packing condition, to avoid condensation that may cause failure. Since sudden temperature change may cause condensation, please store in circumstance of stable temperature.

(2) Stacking number

Since excessive weight causes deformation and damage of carton box, please stack only up to the number stated on carton box for storage and transportation.

(3) Handling

Since LCD module consists of glass and precise electronic components, it will be damaged by excessive shock and drop. Therefore, please handle the carton box carefully to minimize shock at loading, reloading and transportation.

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