



TFT LCD Approval Specification

Model No.:P070WQ0-C03

Customer:	
Approval by:	_
Note:	





Record of Revisions

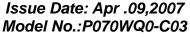
Ver.	Date	Page	Description of change
1.0	Dec.15.2006	All	Product specification was first issued





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1.0 GENERAL DESCRIPTION

1.1 Introduction

P070WQ0-A02 is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit and a back light system. This TFT LCD has a 7.0 (16:9) inch diagonally measured active display area with 1440 x 234 dot (480 horizontal by 234 vertical pixels) resolution.

1.2 Features

- 7 (16:9 diagonal) inch configuration
- Compatible with NTSC & PAL system
- Image Reversion: UP/DOWN and LEFT/RIGHT
- RoHS and UL Compliance

1.3 Applications

- Portable TV
- Portable DVD
- Multimedia applications and Others AV system

1.4 General information

Ite	em	Specification	Unit
Outline Dimension	n	164.9 x 100 x 5.5 (Typ.)	mm
Display area		154.08(H) x 86.58(V)	mm
Number of Pixel		480 RGB(H) x234(V)	pixels
Pixel pitch		0.321(H) x 0.370(V)	mm
Pixel arrangement		RGB Vertical stripe	
Display mode		Normally white	
Surface treatmen	nt	Antiglare, Hard-Coating(3H) with WV film	
Weight		165	g
Back-light		LED (Side-Light type)	
Power	Logic System	36.4	mW
Consumption	LED B/L System	2.2	W(Max.)

1.5 Mechanical Information

	Item	Min.	Тур.	Max.	Unit
N.4 a alvel a	Horizontal(H)	164.4	164.9	165.4	mm
Module Size	Vertical(V)	99.5	100.0	100.5	mm
OIZC	Depth(D)	_	5.5		mm
Weight		_	165	_	g





2.0 ABSOLUTE MAXIMUM RATINGS

2.1 Electrical Absolute Rating

2.1.1 TFT LCD Module

Item	Symbol	Min.	Max.	Unit	Note
	DV_{DD}	-0.3	6.0	V	GND=0
	V_{GH}	-0.3	40	V	GND=0
Power supply veltage	V_{GL}	-20	0.3	V	GND=0
Power supply voltage	V_{GH} - V_{GL}	-0.3	40	V	
	AV_DD	-0.3	7.0	V	AGND=0
	V_{COM}	-1.6	5.2	V	
Analog Signal Input Level	$V_{R,} V_{G,} V_{B}$	-0.2	AV _{DD} +0.2	V	
Logic Signal Input Level	V_{l}	-0.3	DV _{DD} +0.3	V	

2.1.2 Back-Light Unit

Item	Symbol	Тур	Max.	Unit	Note
LED current	I _F	0	140	mA	(1) (2)(3)
LED voltage	V_{L}	10	10.5	V	(1)(2)(3)

Note (1) Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under normal operating conditions.

(2) Ta =25±2°C

(3) Test condition :LED current 140 mA

2.2 Environment Absolute Rating

Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	T _{opa}	-10	60	$^{\circ}\mathbb{C}$	
Storage Temperature	T _{stq}	-20	70	$^{\circ}\mathbb{C}$	







3.0 OPTICAL CHARACTERISTICS

3.1 Optical specification

Item		Symbol	Condition	Min.	Тур.	Max.	Unit	Note
Contrast		CR		=	300	_		(1)(2)
Response	Rising	T _R	Θ=0 Normal	_	15	_		(4)(2)
time	Falling	T _F	viewing	-	35	_	ms	(1)(3)
White luminar (Center)			angle	_	200	_	cd/m ²	(1)(4) (I _F =140mA)
Color		W _x			0.31			(4)(4)
(CIE1931)	romaticity White		Θ=0		0.33			(1)(4)
	Llor	ΘL		_	60	_		
	Hor.	Θ_{R}	05.40	_	60	_		
Viewing angle	Ver.	Θυ	CR>10	_	40	_		
		Θ _D			60			
Brightness un	iformity	B _{UNI}	Θ=0	70	_	_	%	(5)

3.2 Measuring Condition

■ Measuring surrounding : dark room

■ LED current I_F :140mA

■ Ambient temperature : 25±2°C

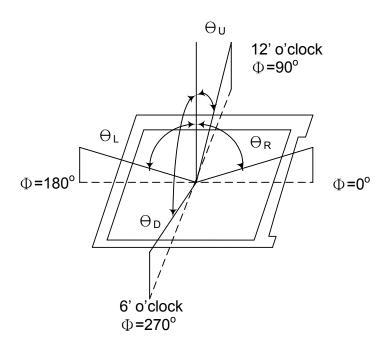
■ 30min. warm-up time.



3.3 Measuring Equipment

- TOPCON BM-7 for Chromaticity and other optical characteristics.
- Measuring spot size : 10 ~ 12 mm

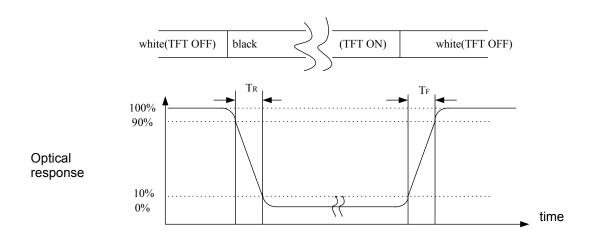
Note (1) Definition of Viewing Angle:



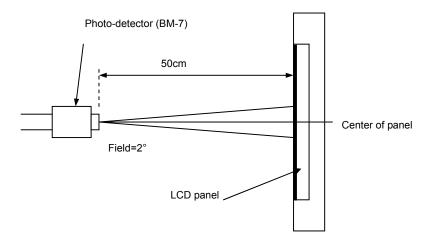
Note (2) Definition of Contrast Ratio(CR): measured at the center point of panel



Note (3) Definition of Response Time : Sum of $T_{\mbox{\scriptsize R}}$ and $T_{\mbox{\scriptsize F}}$

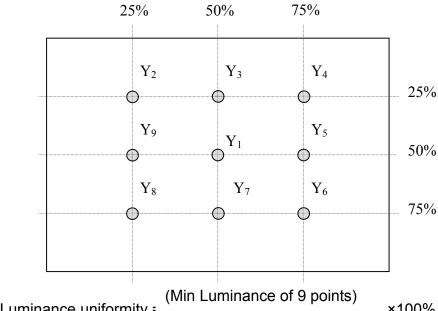


Note (4) Definition of optical measurement setup





Note (5) Definition of brightness uniformity



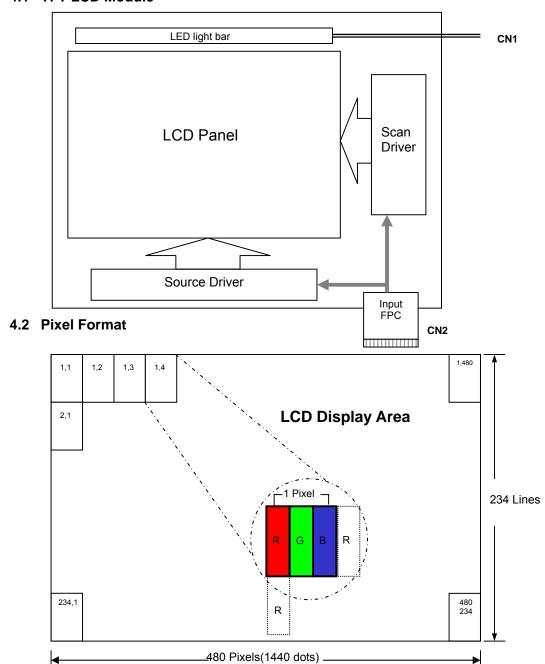
Luminance uniformity = (Min Luminance of 9 points) ×100%

(Max Luminance of 9 points)



4.0 BLOCK DIAGRAM

4.1 TFT LCD Module







5.0 INTERFACE PIN CONNECTION

5.1 TFT LCD Module

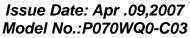
CN2 (Input signal): FPC Down Connector, 26 pins, pitch: 0.5mm

Terminal no.	Symbol	I/O	Function	
1	DGND	-	Ground for logic circuit	
2	DV_{DD}	I	Supply voltage of logic control circuit for scan (Gate) driver	
3	V_{GL}	- [Negative power for scan (Gate) driver	
4	V_{GH}	- 1	Positive power for scan (Gate) driver	
5	STVD	1/0	Vertical start pulse	(1)
6	STVU	1/0	Vertical start pulse	(1)
7	CKV	_	Shift clock input for scan (Gate) driver	
8	U/D		UP/DOWN scan control input	(1)
9	OEV	- 1	Output enable input for scan(Gate) driver	
10	Vсом	_	Common electrode driving signal	
11	Vсом	_	Common electrode driving signal	
12	L/R	_	LEFT/RIGHT scan control input	(1)
13	MOD		Sequential sampling and simultaneous sampling setting	(2)
14	OEH	- 1	Output enable input for data (Source) driver	
15	STHL	I/O	Start pulse for horizontal scan (Gate) line	(1)
16	STHR	I/O	Start pulse for horizontal scan (Gate) line	(1)
17	CPH3	- 1	Sampling and shifting clock pulse for data (Source) driver	(2)
18	CPH2	I	Sampling and shifting clock pulse for data (Source) driver	(2)
19	CPH1	I	Sampling and shifting clock pulse for data (Source) driver	
20	DV _{DD}	I	Supply voltage of logic control circuit for data(Source) driver	
21	DGND	-	Ground for logic circuit	
22	V_{R}	- 1	Alternated video signal input(Red)	
23	V _G	ı	Alternated video signal input(Green)	
24	VB	- 1	Alternated video signal input(blue)	
25	AVDD	- 1	Supply voltage for analog circuit	
26	AGND	-	Ground for analog circuit	

Note (1) Selection of scanning mode (please refer to the following table)

Setting of s	can control out	IN/OUT state for start pulse		pulse	Scanning direction	
U/D	L/R	STVD	STVU	STHR	STHL	
GND	DV _{DD}	Output	Input	Output	Input	up to down, and from left to right.
DV _{DD}	GND	Input	Output	Input	Output	down to up, and from right to left.
GND	GND	Output	Input	Input	Output	up to down, and from right to left.
DV _{DD}	DV _{DD}	Input	Output	Output	Input	down to up, and from left to right.

Note (2) MOD=H: Simultaneous sampling.(Please check CPH2 and CPH3 to GND when MOD=H) MOD=L: Sequential sampling.







6.0 ELECTRICAL CHARACTERISTICS

6.1 TFT LCD Module

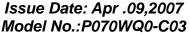
Item	Symbol	Min.	Тур.	Max.	Unit	Note
	DV_{DD}	3	3.3	5.5	V	
Supply Voltage	V_{GH}	14.3	15	15.7	V	
Supply Voltage	V _{GL}	-10.5	-10	-9.5	V	
	AV _{DD}	4.5	5	5.5	V	
Video signal	ViA	0.4	-	AV _{DD} -0.4	V	
amplitude	Viac	-	3	-	V	AC component,
(VR,VG,VB)	ViDC	-	AV _{DD} /2	-	V	DC component
VCOM	VCAC	-	5.6	-	VP-P	AC component
VCOM	Vcdc	1.6	1.8	2.0	V	DC component, (1)
Input signal	ViH	0.8DV _{DD}	-	DV _{DD}	V	(2)
voltage	ViL	0	-	0.2 DV _{DD}	V	(2)
	ldd	-	127	-	uA	DV _{DD} =3.3V
Current of power	ladd	-	7.0	-	mA	AV _{DD} =5V(Black)
supply	lgн	-	70	-	uA	V _{GH} =15V
	I GL	-	65	-	uA	V _{GL} =-10V

Note (1): The brightness of LCD panel could be changed by adjusting the AC component of VCOM.

Note (2): STHL, STHR, OEH, L/R, CPH1~CPH3, STVD, STVU, OEV, CKV, U/D

Note (3): Be sure to apply the power voltage as the power sequence spec.

Note (4): DGND=AGND=0V,)







6.2 Back-Light Unit

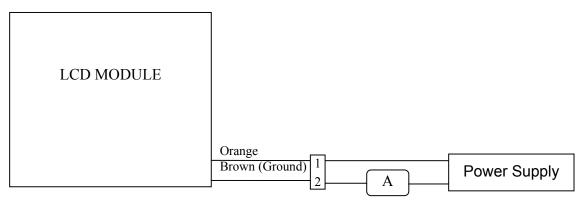
The back-light system is an edge-lighting type with 1 LED Light Bar.

The characteristics of the LED Light Bar is shown in the following tables.

Item	Symbol	Min.	Тур.	Max.	Unit	Note
LED current	I _F	_	_	140	mA	(1)
LED voltage	V _L	_	10	10.5	V	
Operating LED life time	Hr	20,000	_	_	Hour	(2)

This back light system is fixed 140 mA

Note (1) LED current is measured as shown below.



Note (2) LED life time (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25±3 °C, typical IL value indicated in the above table and fLI=50kHz until the brightness becomes less than 50%.



6.3 AC Characteristics

Item	Symbo I	Min.	Тур.	Max.	Unit	Note
Rising time	tr	-	-	10	ns	(1)
Falling time	t f	-	-	10	ns	(1)
High and low level pulse duty	tсрн	100	103	-	ns	CPH1~CPH3
CPH pulse duty	tсwн	40	50	60		CPH1~CPH3
STH setup time	tsuн	20	ı		ns	STHR,STHL
STH hold time	t HDH	10	ı		ns	STHR,STHL
STH pulse width	t sтн	-	1	1	t CPH	STHR,STHL
STH period	tн	61.5	63.5	65.5	μs	STHR,STHL
OEH pulse width	t oeh	-	1.23	-	μs	OEH
Sample and hold disable time	t DIS1	-	8.19	-	μs	
OEV pulse width	toev	-	4.77	-	μs	OEV
CKV pulse width	tckv	-	3.91		μs	CKV
Clean enable time	t _{DIS2}	-	3.90	ı	μs	
Horizontal display timing range	tон	-	1440	ı	tсрн/3	
STV setup time	tsuv	200	1	-	ns	STVD,STVU
STV hold time	t HDV	300	ı	1	ns	STVD,STVU
STV pulse width	t stv	-	1	1	tн	STVD,STVU
Horizontal line per field	t _V	256	262	268	tн	(2)
Vertical display start	t sv		3	-	tн	
Vertical display timing range	t DV		234	=	tн	
VCOM Rising time	t rCOM		-	5	μs	
VCOM Falling time	tгсом		-	5	μs	
VCOM delay time	tосом		-	3	μs	
RGB delay time	t DRGB		*	1	μs	

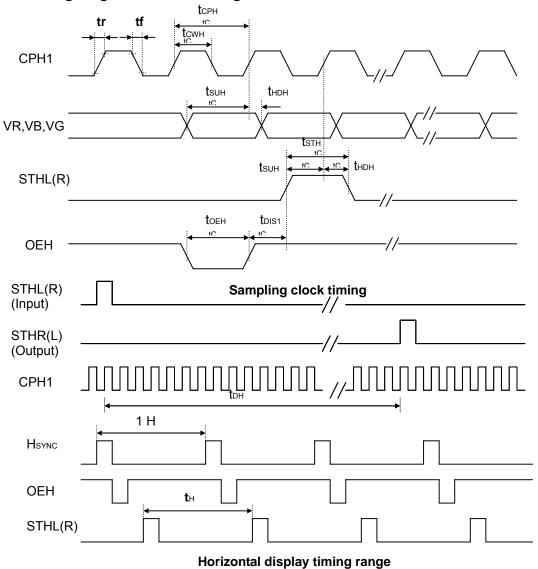
Note (1): For all of the logic signals.

Note (2): Please don't use odd horizontal lines to drive LCD panel for both odd and even filed simultaneously.

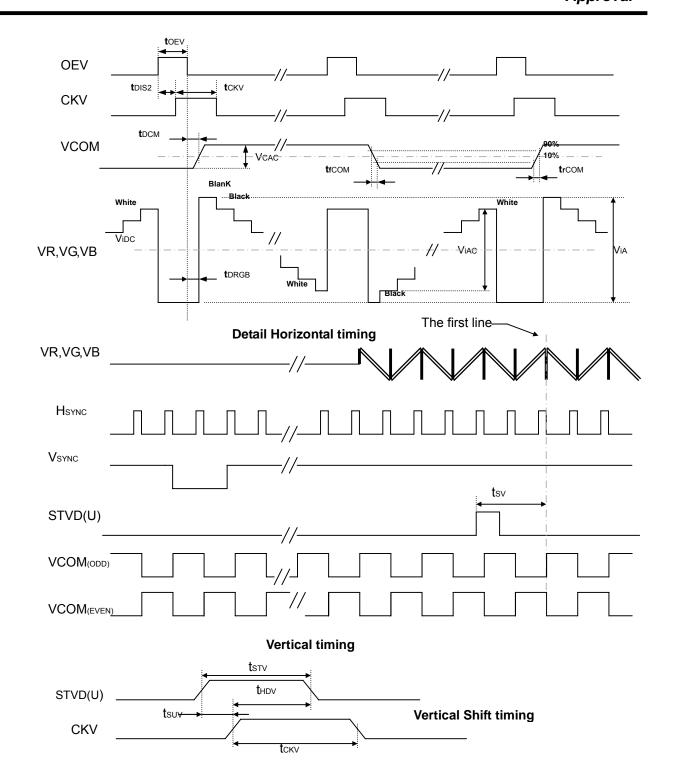




6.4 Timing Diagram of Interface Signal

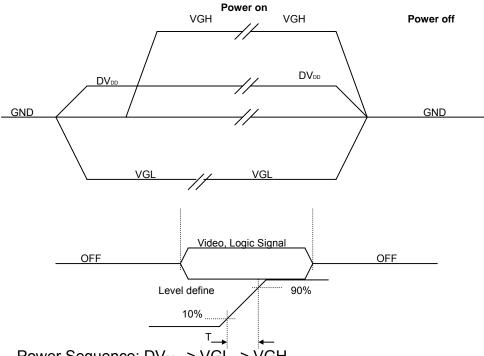








6.5 Power Sequence



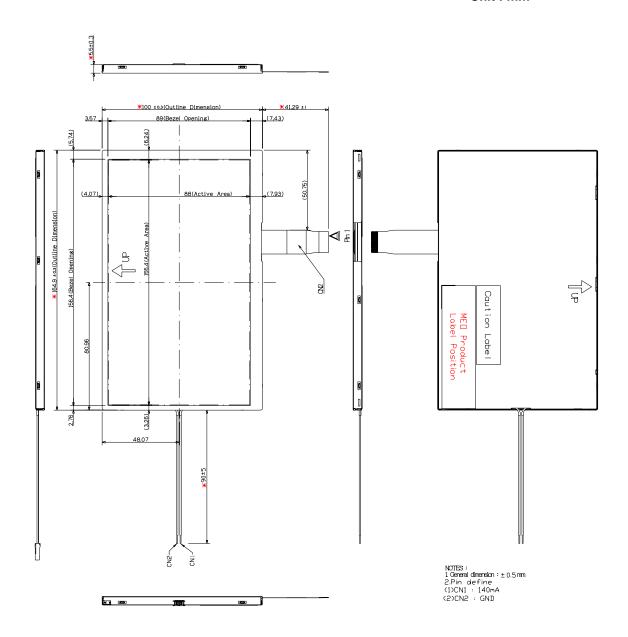
Power Sequence: $DV_{DD} \rightarrow VGL \rightarrow VGH$

Note Apply the lamp volatge within the LCD operation range. When the back-light turns on before the LCD operation or the LCD truns off before the back-light turns off. the display may momentarily become white.



7.0 OUTLINE DIMENSION

Unit: mm









8.0 GENERAL PRECAUTION

8.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

8.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. MEO does not warrant the module, if customers disassemble or modify the module.

8.3 Breakage of LCD Panel

- 8.3.1. If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 8.3.2. If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 8.3.3. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 8.3.4. Handle carefully with chips of glass that may cause injury, when the glass is broken.

8.4 Electric Shock

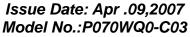
- 8.4.1. Disconnect power supply before handling LCD module.
- 8.4.2. Do not pull or fold the CCFL cable.
- 8.4.3. Do not touch the parts inside LCD modules and the fluorescent lamp's connector or cables in order to prevent electric shock.

8.5 Absolute Maximum Ratings and Power Protection Circuit

- 8.5.1. Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 8.5.2. Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 8.5.3. It's recommended to employ protection circuit for power supply.

8.6 Operation

- 8.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.
- 8.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 8.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.







- 8.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may cause deformation or color fading.
- 8.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

8.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

8.8 Static Electricity

- 8.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.
- 8.8.2 Because LCD module use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

8.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

8.10 Disposal

When disposing LCD module, obey the local environmental regulations.