# SPECIFICATION FOR APPROVAL

( ) Preliminary Specification (♦) Final Specification

Title		20.0" HD+ TFT LCD		
BUYER	FTS	SUPPLIER	LG Display Co., Ltd.	
MODEL		*MODEL	LM200WD3	
		SUFFIX	TLC7	

<sup>\*</sup>When you obtain standard approval, please use the above model name without suffix

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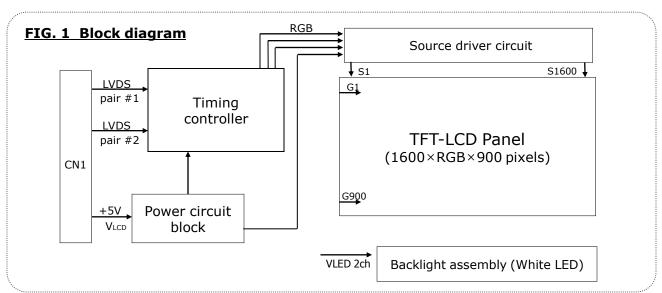
# **Record of revisions**

<b>Revision No</b>	Date	Page	Description
Ver.0.1	Jan.,11,2011		Preliminary Specifications.
Ver.1.0	Mar.,24,2011		Final Specifications.



### 1. General description

LM200WD3-TLC7 is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode(LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally white mode. It has a 20.0 inch diagonally measured active display area with HD+ resolution (900 vertical by 1600 horizontal pixel array) Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus, presenting a palette of more than 16.7M colors with Advanced-FRC(Frame Rate Control). It has been designed to apply the interface method that enables low power, high speed, low EMI. FPD Link or compatible must be used as a LVDS(Low Voltage Differential Signaling) chip. It is intended to support applications where thin thickness, wide viewing angle, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LM200WD3-TLC7 characteristics provide an excellent flat panel display for office automation products such as monitors.



#### General features

<u>General leatures</u>	
Active screen size	20.0 inches (508.05mm) diagonal
Outline Dimension	462.8(H) x 272.0(V) x 10.2(D) mm(Typ.)
Pixel Pitch	0.0922*RGB(H)mm x 0.2766(V)mm
Pixel Format	1600 horizontal By 900 vertical Pixels. RGB stripe arrangement
Interface	LVDS 2Port
Color depth	16.7M colors
Luminance, white	250 cd/m <sup>2</sup> ( Center 1Point, typ)
Viewing Angle (CR>10)	R/L 170(Typ.), U/D 160(Typ.)
Power Consumption	Total 14.2 W(Typ.), (4.3 W@ $V_{LCD}$ , 9.9 W@ $I_{BL}$ = 110 mA)
Weight	1,190 g (Typ.)
Display operating mode	Transmissive mode, normally White
Surface treatments	Hard coating (3H), Anti-glare treatment of the front polarizer



### 2. Absolute maximum ratings

The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. Absolute maximum ratings

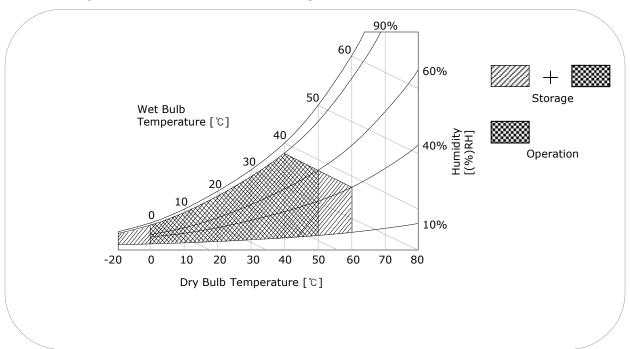
Parameter	Symbol	Values		Units	Notes	
raiailletei	Syllibol	Min	Max	UTILS	Notes	
Power Supply Input Voltage	$V_{LCD}$	-0.3	+6.0	Vdc	At 25℃	
Operating Temperature	T <sub>OP</sub>	0	50	°C		
Storage Temperature	T <sub>ST</sub>	-20	60	°C	1	
Operating Ambient Humidity	H <sub>OP</sub>	10	90	%RH	1	
Storage Humidity	H <sub>ST</sub>	10	90	%RH		

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39 °C Max, and no condensation of water.

Note: 2. Storage condition is guaranteed under packing condition.

#### FIG. 2 Temperature and relative humidity





# 3. Electrical specifications

#### 3-1. Electrical characteristics

It requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input power for the LED/Backlight, is typically generated by an LED Driver. The LED driver is an external unit to the LCDs.

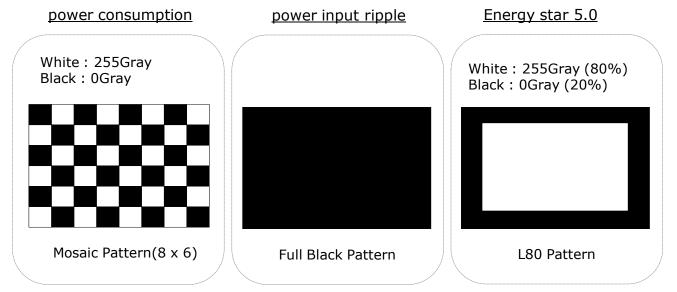
**Table 2. Electrical characteristics** 

Parameter	Symbol		Values	Unit	Notes			
i di dillecei	Symbol	Min	Тур	Max	Offic	Notes		
MODULE:								
Power Supply Input Voltage	V <sub>LCD</sub>	4.5	5.0	5.5	Vdc			
Permissive Power Input Ripple	$V_{LCD}$	_	-	0.4	V	3		
	I <sub>LCD-MOSAIC</sub>	-	860	1120	mA	1		
Power Supply Input Current	I <sub>LCD-BLACK</sub>	_	1100	1430	mA	2		
	I <sub>LCD-L80</sub>	_	800	1040	mA	2		
Power Consumption	P <sub>LCD</sub>	_	4.3	5.6	Watt	1		
Inrush current	I <sub>RUSH</sub>	_	-	3.0	А	4		

#### Note:

- 1. The specified current and power consumption are under the VLCD=5.0V, 25  $\pm$  2°C,  $f_V$ =60Hz condition whereas mosaic pattern(8 x 6) is displayed and  $f_V$  is the frame frequency.
- 2. The current is specified at the maximum current pattern.
- 3. Permissive power ripple should be measured under VCC=5.0V,  $25^{\circ}C$ ,  $f_V$  (frame frequency)=75Hz condition and At that time, we recommend the bandwidth configuration of oscilloscope is to be under 20MHz.
- 4. The duration of rush current is about 5ms and rising time of power Input is 500us  $\pm$  20%.

#### FIG.3 pattern for Electrical characteristics





#### Table 3. LED array ELECTRICAL CHARACTERISTICS

Parameter	Symbol Condition		Values			Unit	Notes
Parameter	Symbol	Symbol Condition		Тур.	Max.	Unit	Notes
LED:							1
LED String Current	Is		-	110	120	mA	2
LED String Voltage	Vs		40.6	44.8	49.0	V	3
Power Consumption	Parray		-	9.9	10.8	Watt	4,6
LED Life Time	LED_LT		30,000	-	-	Hrs	5

#### LED driver design guide

: The design of the LED driver must have specifications for the LED in LCD Assembly.

The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED driver.

So all the parameters of an LED driver should be carefully designed and output current should be Constant current control.

Please control feedback current of each string individually to compensate the current variation among the strings of LEDs.

When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the LED driver (no lighting, flicker, etc) never occurs.

When you confirm it, the LCD module should be operated in the same condition as installed in your instrument.

#### Notes:

- 1. Specified values are for a single LED bar.
- 2. The specified current is input LED chip 100% duty current.
- 3. The specified voltage is input LED string voltage at typical 110mA 100% duty current.
- 4. The specified power consumption is input LED bar power consumption at typical 110mA 100% duty current.
- 5. The life time is determined as the time at which luminance of the LED is 50% compared to that of initial value at the typical LED current on condition of continuous operating at  $25 \pm 2^{\circ}$ C.
- 6. The LED bar power consumption shown above does not include loss of external driver.

The used LED string current is the LED typical current.

Typ Power Consumption is calculated with PBar =  $Vs(Typ.) \times Is(Typ.) \times Nstring$ 

Max Power Consumption is calculated with PBar = Vs(Max.) x Is(Typ) x Nstring



### 3-2. Interface connections

### **3-2-1. LCD Module**

LCD connector(CN1): GT103-30S-HF15-E2500 (LSM), UJU(IS100-L300-C23)

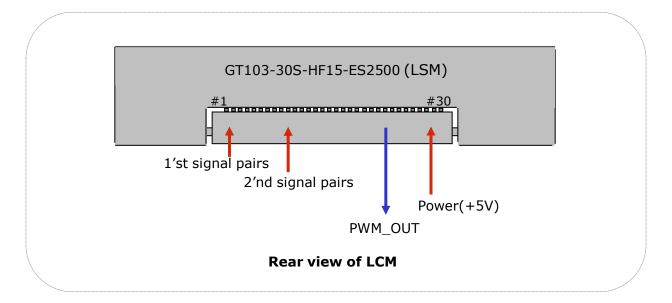
Mating connector: FI-X30H and FI-X30HL (JAE) or Equivalent

Table 4. Module connector(CN1) pin configuration

Pin No	Таріс	Table 4. Module connector(CN1) pin configuration						
2RXO0+ RXO1- RXO1-Plus signal of 1st channel 0 (LVDS) Minus signal of 1st channel 1 (LVDS)4RXO1+ Plus signal of 1st channel 1 (LVDS)5RXO2- Minus signal of 1st channel 2 (LVDS)6RXO2+ GND GRDPlus signal of 1st channel 2 (LVDS)7GND RXOC- Plus signal of 1st clock channel (LVDS)9RXOC- Plus signal of 1st clock channel (LVDS)10RXO3- Minus signal of 1st channel 3 (LVDS)11RXO3- Plus signal of 1st channel 3 (LVDS)12RXE0- Minus signal of 2nd channel 0 (LVDS)13RXE0+ Plus signal of 2nd channel 0 (LVDS)14GND Ground15RXE1- Plus signal of 2nd channel 1 (LVDS)16RXE1+ Plus signal of 2nd channel 1 (LVDS)17GND Ground18RXE2- Plus signal of 2nd channel 2 (LVDS)19RXE2+ Plus signal of 2nd clock channel (LVDS)20RXEC- Plus signal of 2nd clock channel (LVDS)21RXEC- Plus signal of 2nd clock channel (LVDS)22RXE3- Plus signal of 2nd channel 3 (LVDS)23RXE3+ Plus signal of 2nd channel 3 (LVDS)24GND35Ground46NC47No Connection (For LCD internal use only.)48VLCDPower Supply (5.0V)29VLCDPower Supply (5.0V)	Pin No	Symbol	Description					
3RXO1- RXO1+ RXO2+ RXO2- BRXO2+ RXO2+ BRXO2+ RXO2+ BRXOC- Plus signal of 1st channel 1 (LVDS) Minus signal of 1st channel 2 (LVDS) Plus signal of 1st channel 2 (LVDS) Plus signal of 1st channel 2 (LVDS)6RXO2+ Plus signal of 1st channel 2 (LVDS)7GND RXOC- PRXOC+ Plus signal of 1st clock channel (LVDS) Plus signal of 1st clock channel (LVDS) Plus signal of 1st channel 3 (LVDS)10RXO3- Plus signal of 1st channel 3 (LVDS) Plus signal of 1st channel 3 (LVDS)12RXE0- Plus signal of 2nd channel 0 (LVDS)13RXE0- Plus signal of 2nd channel 0 (LVDS)14GND Ground15RXE1- Plus signal of 2nd channel 1 (LVDS)16RXE1+ Plus signal of 2nd channel 1 (LVDS)17GND Ground18RXE2- Plus signal of 2nd channel 2 (LVDS)19RXE2+ Plus signal of 2nd clock channel (LVDS)20RXEC- Plus signal of 2nd clock channel (LVDS)21RXEC+ Plus signal of 2nd channel 3 (LVDS)22RXE3- Plus signal of 2nd channel 3 (LVDS)23RXE3+ Plus signal of 2nd channel 3 (LVDS)24GND Ground25NCNo Connection (For LCD internal use only.) No Connection (For LCD internal use only.)26NCNo Connection (For LCD internal use only.)27PWM_OUT PWM_OUT Power Supply (5.0V) Power Supply (5.0V)	1	RXO0-	Minus signal of 1st channel 0 (LVDS)					
4 RXO1+ Plus signal of 1st channel 1 (LVDS)  RXO2- Minus signal of 1st channel 2 (LVDS)  RXO2+ Plus signal of 1st channel 2 (LVDS)  RXOC- Plus signal of 1st clock channel (LVDS)  RXOC+ Plus signal of 1st clock channel (LVDS)  RXOC+ Plus signal of 1st clock channel (LVDS)  RXO3- Minus signal of 1st channel 3 (LVDS)  RXO3+ Plus signal of 1st channel 3 (LVDS)  RXE0- Minus signal of 2nd channel 0 (LVDS)  RXE0+ Plus signal of 2nd channel 0 (LVDS)  RXE1- Minus signal of 2nd channel 1 (LVDS)  RXE1+ Plus signal of 2nd channel 1 (LVDS)  RXE1+ Plus signal of 2nd channel 2 (LVDS)  RXE2+ Plus signal of 2nd channel 2 (LVDS)  RXE2+ Plus signal of 2nd channel 2 (LVDS)  RXEC- Minus signal of 2nd clock channel (LVDS)  RXEC- Plus signal of 2nd clock channel (LVDS)  RXE2- RXE3- Minus signal of 2nd channel 3 (LVDS)  RXE3+ Plus signal of 2nd channel 3 (LVDS)  RXE3+ Plus signal of 2nd channel 3 (LVDS)  RXE4- Plus signal of 2nd channel 3 (LVDS)  RXE5- Plus signal of 2nd channel 3 (LVDS)  RXE6- Plus signal of 2nd channel 3 (LVDS)  RXE7- Plus signal of 2nd channel 3 (LVDS)  RXE8- Plus signal of 2nd channel 3 (LVDS)  RXE5- Plus signal of 2nd channel 3 (LVDS)  RXE6- Plus signal of 2nd channel 3 (LVDS)  RXE7- Plus signal of 2nd channel 3 (LVDS)  RXE8- Plus signal of 2nd channel 3 (LVDS)  RXE8- Plus signal of 2nd channel 3 (LVDS)  RXE8- Plus signal of 2nd channel 3 (LVDS)  RXE9- Plus signal of 2nd channel 3	2	RXO0+	Plus signal of 1st channel 0 (LVDS)					
5RXO2- RXO2+ RXO2+ GNDMinus signal of 1st channel 2 (LVDS)7GND GNDGround8RXOC- PRXOC+ PRXOC+Minus signal of 1st clock channel (LVDS)9RXO3- RXO3- PRXO3+ Plus signal of 1st channel 3 (LVDS)11RXO3+ Plus signal of 1st channel 3 (LVDS)12RXE0- Plus signal of 2nd channel 0 (LVDS)13RXE0+ Plus signal of 2nd channel 0 (LVDS)14GND Ground15RXE1- Plus signal of 2nd channel 1 (LVDS)16RXE1+ Plus signal of 2nd channel 2 (LVDS)17GND Ground18RXE2- Plus signal of 2nd channel 2 (LVDS)19RXE2+ Plus signal of 2nd channel 2 (LVDS)20RXEC- Plus signal of 2nd clock channel (LVDS)21RXEC+ Plus signal of 2nd channel 3 (LVDS)22RXE3- Plus signal of 2nd channel 3 (LVDS)23RXE3+ Plus signal of 2nd channel 3 (LVDS)24GND Ground25NC NC NC Connection (For LCD internal use only.) No Connection (For LCD internal use only.)26NC POWM_OUTReference signal for inverter control Power Supply (5.0V)29VLCDPower Supply (5.0V)	3	RXO1-	Minus signal of 1st channel 1 (LVDS)					
6 RXO2+ GND Ground  8 RXOC- PLUS signal of 1st clock channel (LVDS)  9 RXOC+ PLUS signal of 1st clock channel (LVDS)  10 RXO3- HILL SIGNAL OF 1st clock channel (LVDS)  11 RXO3+ PLUS signal of 1st channel 3 (LVDS)  12 RXE0- HILL SIGNAL OF 1st channel 0 (LVDS)  13 RXE0+ PLUS signal of 2nd channel 0 (LVDS)  14 GND Ground 15 RXE1- HILL SIGNAL OF 1st channel 1 (LVDS)  16 RXE1+ PLUS signal of 2nd channel 1 (LVDS)  17 GND Ground  18 RXE2- HILL SIGNAL OF 1st channel 2 (LVDS)  19 RXE2+ PLUS signal of 2nd channel 2 (LVDS)  19 RXE2+ PLUS signal of 2nd channel 2 (LVDS)  20 RXEC- HILL SIGNAL OF 1st channel 3 (LVDS)  21 RXEC+ PLUS signal of 2nd channel 3 (LVDS)  22 RXE3- HILL SIGNAL OF 1st channel 3 (LVDS)  23 RXE3+ PLUS signal of 2nd clock channel 3 (LVDS)  24 GND Ground  25 NC NO Connection (For LCD internal use only.) NO Connection (For LCD internal use only.) PWM_OUT Reference signal for inverter control POWER Supply (5.0V) POWER Supply (5.0V)	4	RXO1+	Plus signal of 1st channel 1 (LVDS)					
7GNDGround8RXOC-Minus signal of 1st clock channel (LVDS)9RXOC+Plus signal of 1st clock channel (LVDS)10RXO3-Minus signal of 1st channel 3 (LVDS)11RXO3+Plus signal of 1st channel 3 (LVDS)12RXE0-Minus signal of 2nd channel 0 (LVDS)13RXE0+Plus signal of 2nd channel 0 (LVDS)14GNDGround15RXE1-Minus signal of 2nd channel 1 (LVDS)16RXE1+Plus signal of 2nd channel 1 (LVDS)17GNDGround18RXE2-Minus signal of 2nd channel 2 (LVDS)19RXE2+Plus signal of 2nd channel 2 (LVDS)20RXEC-Minus signal of 2nd clock channel (LVDS)21RXEC+Plus signal of 2nd clock channel (LVDS)22RXE3-Minus signal of 2nd channel 3 (LVDS)23RXE3+Plus signal of 2nd channel 3 (LVDS)24GNDGround25NCNo Connection (For LCD internal use only.)26NCNo Connection (For LCD internal use only.)27PWM_OUTReference signal for inverter control28VLCDPower Supply (5.0V)29VLCDPower Supply (5.0V)	5	RXO2-	Minus signal of 1st channel 2 (LVDS)					
8RXOC- RXOC+Minus signal of 1st clock channel (LVDS)9RXOC+Plus signal of 1st clock channel (LVDS)10RXO3- RXO3+Minus signal of 1st channel 3 (LVDS)11RXO3+Plus signal of 1st channel 3 (LVDS)12RXE0- Minus signal of 2nd channel 0 (LVDS)13RXE0+ GNDPlus signal of 2nd channel 0 (LVDS)14GNDGround15RXE1- Plus signal of 2nd channel 1 (LVDS)16RXE1+ Plus signal of 2nd channel 1 (LVDS)17GNDGround18RXE2- Plus signal of 2nd channel 2 (LVDS)19RXE2+ Plus signal of 2nd clock channel (LVDS)20RXEC- Plus signal of 2nd clock channel (LVDS)21RXEC+ Plus signal of 2nd clock channel 3 (LVDS)22RXE3- Plus signal of 2nd channel 3 (LVDS)23RXE3+ Plus signal of 2nd channel 3 (LVDS)24GND Ground25NCNo Connection (For LCD internal use only.)26NCNo Connection (For LCD internal use only.)27PWM_OUT PWM_OUT Reference signal for inverter control28VLCDPower Supply (5.0V)	6	RXO2+	Plus signal of 1st channel 2 (LVDS)					
9 RXOC+ RXO3- Minus signal of 1st clock channel (LVDS) 11 RXO3+ Plus signal of 1st channel 3 (LVDS) 12 RXE0- Minus signal of 2nd channel 0 (LVDS) 13 RXE0+ Plus signal of 2nd channel 0 (LVDS) 14 GND Ground 15 RXE1- Minus signal of 2nd channel 1 (LVDS) 16 RXE1+ Plus signal of 2nd channel 1 (LVDS) 17 GND Ground 18 RXE2- Minus signal of 2nd channel 2 (LVDS) 19 RXE2+ Plus signal of 2nd channel 2 (LVDS) 20 RXEC- Minus signal of 2nd channel 2 (LVDS) 21 RXEC+ Plus signal of 2nd clock channel (LVDS) 22 RXE3- Minus signal of 2nd clock channel 3 (LVDS) 23 RXE3+ Plus signal of 2nd channel 3 (LVDS) 24 GND Ground 25 NC No Connection (For LCD internal use only.) 26 NC No Connection (For LCD internal use only.) 27 PWM_OUT Reference signal for inverter control 28 VLCD Power Supply (5.0V)	7	GND	Ground					
10RXO3- RXO3+ RXE0- RXE0- RXE0+ GND RXE1+ GND RXE2- Plus signal of 2nd channel 0 (LVDS) Plus signal of 2nd channel 1 (LVDS) RXE1- Plus signal of 2nd channel 1 (LVDS) RXE1+ Plus signal of 2nd channel 1 (LVDS) Plus signal of 2nd channel 2 (LVDS) RXE1+ Plus signal of 2nd channel 2 (LVDS) Plus signal of 2nd channel 2 (LVDS) Plus signal of 2nd channel 2 (LVDS) Plus signal of 2nd clock channel (LVDS) RXE2+ Plus signal of 2nd clock channel (LVDS) RXEC- Plus signal of 2nd channel 3 (LVDS) Plus signal of 2nd channel 3 (LVDS)21RXE3- Plus signal of 2nd channel 3 (LVDS) Plus signal of 2nd channel 3 (LVDS)23RXE3+ Plus signal of 2nd channel 3 (LVDS) Plus signal of 2nd channel 3 (LVDS)24GND ONC NCGround No Connection (For LCD internal use only.) No Connection (For LCD internal use only.) Reference signal for inverter control Power Supply (5.0V)29VLCDPower Supply (5.0V)	8	RXOC-	Minus signal of 1st clock channel (LVDS)					
11 RXO3+ Plus signal of 1st channel 3 (LVDS) 12 RXE0- Minus signal of 2nd channel 0 (LVDS) 13 RXE0+ Plus signal of 2nd channel 0 (LVDS) 14 GND Ground 15 RXE1- Minus signal of 2nd channel 1 (LVDS) 16 RXE1+ Plus signal of 2nd channel 1 (LVDS) 17 GND Ground 18 RXE2- Minus signal of 2nd channel 2 (LVDS) 19 RXE2+ Plus signal of 2nd channel 2 (LVDS) 20 RXEC- Minus signal of 2nd clock channel (LVDS) 21 RXEC+ Plus signal of 2nd clock channel (LVDS) 22 RXE3- Minus signal of 2nd channel 3 (LVDS) 23 RXE3+ Plus signal of 2nd channel 3 (LVDS) 24 GND Ground 25 NC No Connection (For LCD internal use only.) No Connection (For LCD internal use only.) PWM_OUT Reference signal for inverter control Power Supply (5.0V) Power Supply (5.0V)	9	RXOC+	Plus signal of 1st clock channel (LVDS)					
12RXE0- RXE0+Minus signal of 2nd channel 0 (LVDS)13RXE0+ GNDPlus signal of 2nd channel 0 (LVDS)14GNDGround15RXE1- GNDMinus signal of 2nd channel 1 (LVDS)16RXE1+ GNDPlus signal of 2nd channel 1 (LVDS)17GNDGround18RXE2- Plus signal of 2nd channel 2 (LVDS)19RXE2+ Plus signal of 2nd clock channel (LVDS)20RXEC- Plus signal of 2nd clock channel (LVDS)21RXEC+ Plus signal of 2nd channel 3 (LVDS)22RXE3- Plus signal of 2nd channel 3 (LVDS)23RXE3+ Plus signal of 2nd channel 3 (LVDS)24GND Ground25NC NCNo Connection (For LCD internal use only.)26NC PWM_OUTNo Connection (For LCD internal use only.)27PWM_OUT PWM_OUTReference signal for inverter control Power Supply (5.0V)29VLCDPower Supply (5.0V)	10	RXO3-	Minus signal of 1st channel 3 (LVDS)					
RXE0+ GND Ground  RXE1- Minus signal of 2nd channel 1 (LVDS)  RXE1+ Plus signal of 2nd channel 1 (LVDS)  RXE1+ Plus signal of 2nd channel 1 (LVDS)  RXE2- Minus signal of 2nd channel 2 (LVDS)  RXE2+ Plus signal of 2nd channel 2 (LVDS)  RXEC- Minus signal of 2nd clock channel (LVDS)  RXEC+ Plus signal of 2nd clock channel (LVDS)  RXE3- Minus signal of 2nd clock channel (LVDS)  RXE3- Minus signal of 2nd channel 3 (LVDS)  RXE3+ Plus signal of 2nd channel 3 (LVDS)  RXE3- Plus signal of 2nd channel 2 (LVDS)  RXE4- Plus signal of 2nd channel 2 (LVDS)  RXE5- Plus signal of 2nd channel 3 (LVDS)  RXE5- Plus signal of 2nd channel	11	RXO3+	Plus signal of 1st channel 3 (LVDS)					
14GNDGround15RXE1-Minus signal of 2nd channel 1 (LVDS)16RXE1+Plus signal of 2nd channel 1 (LVDS)17GNDGround18RXE2-Minus signal of 2nd channel 2 (LVDS)19RXE2+Plus signal of 2nd channel 2 (LVDS)20RXEC-Minus signal of 2nd clock channel (LVDS)21RXEC+Plus signal of 2nd clock channel (LVDS)22RXE3-Minus signal of 2nd channel 3 (LVDS)23RXE3+Plus signal of 2nd channel 3 (LVDS)24GNDGround25NCNo Connection (For LCD internal use only.)26NCNo Connection (For LCD internal use only.)27PWM_OUTReference signal for inverter control28VLCDPower Supply (5.0V)29VLCDPower Supply (5.0V)	12	RXE0-	Minus signal of 2nd channel 0 (LVDS)					
15 RXE1- 16 RXE1+ 17 GND 18 RXE2- 19 RXE2+ 20 RXEC- 21 RXEC+ 22 RXE3- 23 RXE3+ 24 GND 25 NC 26 NC 27 PWM_OUT 28 VLCD 29 VLCD 20 RXEC- 21 RXEC+ 22 RXE3- 23 RXE3+ 24 GND 25 NC 26 NC 27 PWM_OUT 28 VLCD 28 VLCD 29 VLCD 29 VLCD 20 RXE1- 21 RXE1- 22 RXE3- 23 RXE3+ 24 GND 25 NC 26 NC 27 PWM_OUT 28 PWM_OUT 28 Plus signal of 2nd channel (LVDS) 29 VLCD 29 POWEr Supply (5.0V) 29 PWM_OUT 28 Plus signal of 2nd channel (LVDS) 29 PUM COUNT (LVD internal use only.) 29 POWER Supply (5.0V)	13	RXE0+	Plus signal of 2nd channel 0 (LVDS)					
16 RXE1+ Plus signal of 2nd channel 1 (LVDS) 17 GND Ground 18 RXE2- Minus signal of 2nd channel 2 (LVDS) 19 RXE2+ Plus signal of 2nd channel 2 (LVDS) 20 RXEC- Minus signal of 2nd clock channel (LVDS) 21 RXEC+ Plus signal of 2nd clock channel (LVDS) 22 RXE3- Minus signal of 2nd channel 3 (LVDS) 23 RXE3+ Plus signal of 2nd channel 3 (LVDS) 24 GND Ground 25 NC NC Onnection (For LCD internal use only.) 26 NC NC No Connection (For LCD internal use only.) 27 PWM_OUT Reference signal for inverter control 28 VLCD Power Supply (5.0V) 29 VLCD Power Supply (5.0V)	14	GND	Ground					
17GNDGround18RXE2-Minus signal of 2nd channel 2 (LVDS)19RXE2+Plus signal of 2nd channel 2 (LVDS)20RXEC-Minus signal of 2nd clock channel (LVDS)21RXEC+Plus signal of 2nd clock channel (LVDS)22RXE3-Minus signal of 2nd channel 3 (LVDS)23RXE3+Plus signal of 2nd channel 3 (LVDS)24GNDGround25NCNo Connection (For LCD internal use only.)26NCNo Connection (For LCD internal use only.)27PWM_OUTReference signal for inverter control28VLCDPower Supply (5.0V)29VLCDPower Supply (5.0V)	15	RXE1-	Minus signal of 2nd channel 1 (LVDS)					
18RXE2-Minus signal of 2nd channel 2 (LVDS)19RXE2+Plus signal of 2nd channel 2 (LVDS)20RXEC-Minus signal of 2nd clock channel (LVDS)21RXEC+Plus signal of 2nd clock channel (LVDS)22RXE3-Minus signal of 2nd channel 3 (LVDS)23RXE3+Plus signal of 2nd channel 3 (LVDS)24GNDGround25NCNo Connection (For LCD internal use only.)26NCNo Connection (For LCD internal use only.)27PWM_OUTReference signal for inverter control28VLCDPower Supply (5.0V)29VLCDPower Supply (5.0V)	16	RXE1+	Plus signal of 2nd channel 1 (LVDS)					
19 RXE2+ Plus signal of 2nd channel 2 (LVDS) 20 RXEC- Minus signal of 2nd clock channel (LVDS) 21 RXEC+ Plus signal of 2nd clock channel (LVDS) 22 RXE3- Minus signal of 2nd channel 3 (LVDS) 23 RXE3+ Plus signal of 2nd channel 3 (LVDS) 24 GND Ground 25 NC No Connection (For LCD internal use only.) 26 NC No Connection (For LCD internal use only.) 27 PWM_OUT Reference signal for inverter control 28 VLCD Power Supply (5.0V) 29 VLCD Power Supply (5.0V)	17	GND	Ground					
20 RXEC- 21 RXEC+ Plus signal of 2nd clock channel (LVDS)  22 RXE3- Minus signal of 2nd clock channel (LVDS)  23 RXE3+ Plus signal of 2nd channel 3 (LVDS)  24 GND  Connection (For LCD internal use only.)  No Connection (For LCD internal use only.)  No Connection (For LCD internal use only.)  Reference signal for inverter control  VLCD  Power Supply (5.0V)  Power Supply (5.0V)	18	RXE2-	Minus signal of 2nd channel 2 (LVDS)					
21 RXEC+ Plus signal of 2nd clock channel (LVDS)  22 RXE3- Minus signal of 2nd channel 3 (LVDS)  23 RXE3+ Plus signal of 2nd channel 3 (LVDS)  24 GND Ground  25 NC No Connection (For LCD internal use only.)  26 NC No Connection (For LCD internal use only.)  27 PWM_OUT Reference signal for inverter control  28 VLCD Power Supply (5.0V)  29 VLCD Power Supply (5.0V)	19	RXE2+	Plus signal of 2nd channel 2 (LVDS)					
22 RXE3- Minus signal of 2nd channel 3 (LVDS) 23 RXE3+ Plus signal of 2nd channel 3 (LVDS) 24 GND Ground 25 NC No Connection (For LCD internal use only.) 26 NC No Connection (For LCD internal use only.) 27 PWM_OUT Reference signal for inverter control 28 VLCD Power Supply (5.0V) 29 VLCD Power Supply (5.0V)	20	RXEC-	Minus signal of 2nd clock channel (LVDS)					
23 RXE3+ Plus signal of 2nd channel 3 (LVDS) 24 GND Ground 25 NC No Connection (For LCD internal use only.) 26 NC No Connection (For LCD internal use only.) 27 PWM_OUT Reference signal for inverter control 28 VLCD Power Supply (5.0V) 29 VLCD Power Supply (5.0V)	21	RXEC+	Plus signal of 2nd clock channel (LVDS)					
24 GND Ground 25 NC No Connection (For LCD internal use only.) 26 NC No Connection (For LCD internal use only.) 27 PWM_OUT Reference signal for inverter control 28 VLCD Power Supply (5.0V) 29 VLCD Power Supply (5.0V)	22	RXE3-	Minus signal of 2nd channel 3 (LVDS)					
NC No Connection (For LCD internal use only.) NO Connection (For LCD internal use only.) NO Connection (For LCD internal use only.) Reference signal for inverter control VLCD Power Supply (5.0V) Power Supply (5.0V)	23	RXE3+	Plus signal of 2nd channel 3 (LVDS)					
26 NC No Connection (For LCD internal use only.) 27 PWM_OUT Reference signal for inverter control 28 VLCD Power Supply (5.0V) 29 VLCD Power Supply (5.0V)	24	GND	Ground					
27   PWM_OUT   Reference signal for inverter control 28   VLCD   Power Supply (5.0V) 29   VLCD   Power Supply (5.0V)	25	NC	No Connection (For LCD internal use only.)					
28 VLCD Power Supply (5.0V) 29 VLCD Power Supply (5.0V)	26		No Connection (For LCD internal use only.)					
29 VLCD Power Supply (5.0V)	27							
	28	VLCD						
30 VLCD Power Supply (5.0V)	29	VLCD	Power Supply (5.0V)					
	30	VLCD	Power Supply (5.0V)					



#### FIG. 4 Connector diagram



#### Note:

- 1. NC: No Connection.
- 2. All GND(ground) pins should be connected together and to Vss which should also be connected to the LCD's metal frame.
- 3. All  $V_{LCD}$  (power input) pins should be connected together.
- 4. Input Level of LVDS signal is based on the IEA 664 Standard.
- 5. PWM\_OUT is a reference signal for inverter control.

  This PWM signal is synchronized with vertical frequency.

  Its frequency is 3 times of vertical frequency, and its duty ratio is 50%.

  If the system don't use this pin, do not connect.

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### 3-2-2. LED Interface

The LED interface connector is a model 10019HR-H06B manufactured by Yeonho Electronics.

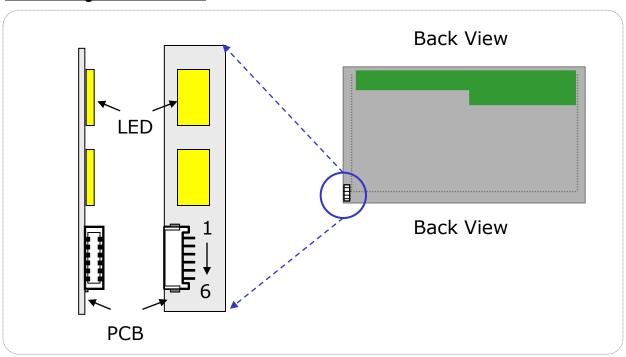
The mating connector is a FFC/FPC specified in LED interface connector specification.

The pin configuration for the connector is shown in the table below.

Table 5. LED connector pin configuration

Pin	Symbol	Description	Notes
1	FB1	Channel1 Current Feedback	
2	NC	No connection	
3	VLED	LED Power Supply	
4	VLED	LED Power Supply	
5	NC	No connection	
6	FB2	Channel2 Current Feedback	

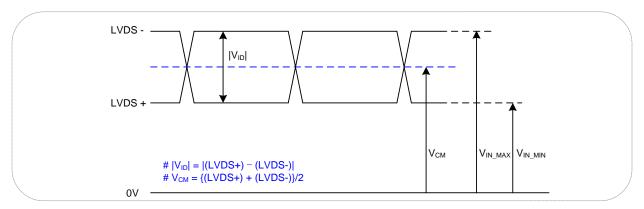
### FIG. 5 Backlight connector view





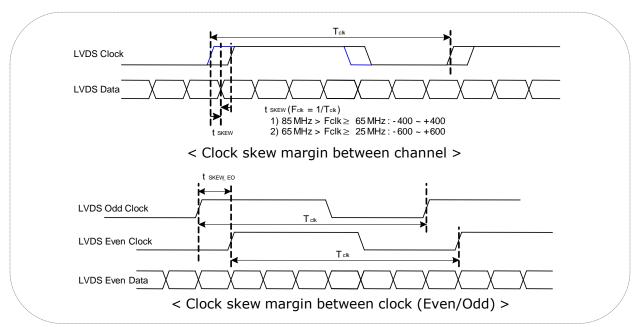
### 3-3. LVDS characteristics

### 3-3-1. DC Specification



Description	Symbol	Min	Max	Unit	Notes
LVDS Differential Voltage	$ V_{ID} $	200	600	mV	-
LVDS Common mode Voltage	V <sub>CM</sub>	0.6	1.8	V	-
LVDS Input Voltage Range	V <sub>IN</sub>	0.3	2.1	V	-

## 3-3-2. AC Specification



Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t <sub>SKEW</sub>	- 400	+ 400	ps	85MHz > Fclk ≥ 65MHz
LVDS Clock to Data Skew Margin	t <sub>SKEW</sub>	- 600	+ 600	ps	65MHz > Fclk ≥ 25MHz
LVDS Clock to Clock Skew Margin (Even to Odd)	t <sub>SKEW_EO</sub>	- 1/7	+ 1/7	T <sub>clk</sub>	-



#### 3-3-3. LVDS Data format

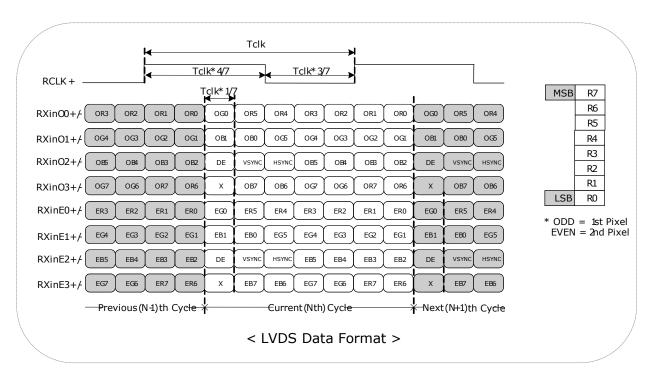




Table 6. Required signal assignment for Flat Link(NS:DS90CF383) transmitter

Pin #	Pin Name	Require Signal	Pin #	Pin Name	Require Signal
1	VCC	Power Supply for TTL Input	29	GND	Ground pin for TTL
2	D5	TTL Input (R7)	30	D26	TTL Input (DE)
3	D6	TTL Input (R5)	31	T <sub>X</sub> CLKIN	TTL Level clock Input
4	D7	TTL Input (G0)	32	PWR DWN	Power Down Input
5	GND	Ground pin for TTL	33	PLL GND	Ground pin for PLL
6	D8	TTL Input (G1)	34	PLL VCC	Power Supply for PLL
7	D9	TTL Input (G2)	35	PLL GND	Ground pin for PLL
8	D10	TTL Input (G6)	36	LVDS GND	Ground pin for LVDS
9	VCC	Power Supply for TTL Input	37	TxOUT3+	Positive LVDS differential data output 3
10	D11	TTL Input (G7)	38	TxOUT3-	Negative LVDS differential data output 3
11	D12	TTL Input (G3)	39	Tx CLKOUT+	Positive LVDS differential clock output
12	D13	TTL Input (G4)	40	T <sub>X</sub> CLKOUT –	Negative LVDS differential clock output
13	GND	Ground pin for TTL	41	T <sub>X</sub> OUT2+	Positive LVDS differential data output 2
14	D14	TTL Input (G5)	42	T <sub>X</sub> OUT2 –	Negative LVDS differential data output 2
15	D15	TTL Input (B0)	43	LVDS GND	Ground pin for LVDS
16	D16	TTL Input (B6)	44	LVDS VCC	Power Supply for LVDS
17	VCC	Power Supply for TTL Input	45	Tx OUT1+	Positive LVDS differential data output 1
18	D17	TTL Input (B7)	46	Tx OUT1 -	Negative LVDS differential data output 1
19	D18	TTL Input (B1)	47	T <sub>X</sub> OUT0+	Positive LVDS differential data output 0
20	D19	TTL Input (B2)	48	T <sub>X</sub> OUT0 –	Negative LVDS differential data output 0
21	GND	Ground pin for TTL Input	49	LVDS GND	Ground pin for LVDS
22	D20	TTL Input (B3)	50	D27	TTL Input (R6)
23	D21	TTL Input (B4)	51	D0	TTL Input (R0)
24	D22	TTL Input (B5)	52	D1	TTL Input (R1)
25	D23	TTL Input (RSVD)	53	GND	Ground pin for TTL
26	VCC	Power Supply for TTL Input	54	D2	TTL Input (R2)
27	D24	TTL Input (HSYNC)	55	D3	TTL Input (R3)
28	D25	TTL Input (VSYNC)	56	D4	TTL Input (R4)

Notes: Refer to LVDS Transmitter Data Sheet for detail descriptions.



## 3-4. Signal timing specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

### **Table 7. Timing table**

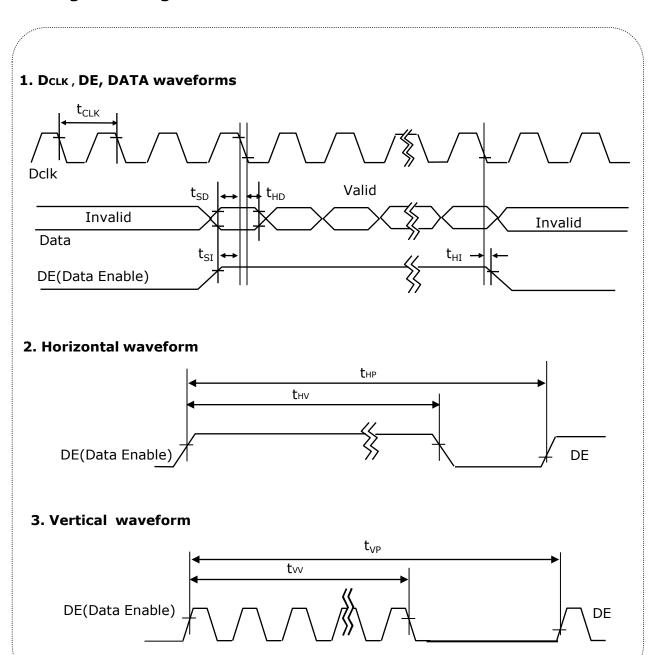
Para	ameter	Symbol	Min.	Тур.	Max.	Unit	Notes
	Period	t <sub>CLK</sub>	14.6	18.5	23.1	ns	Pixel frequency
D <sub>CLK</sub>	Frequency	f <sub>CLK</sub>	43.2	54.0	68.4	MHz	: Typ.108.0MHz
	Horizontal Valid	t <sub>HV</sub>	800	800	800	t <sub>CLK</sub>	
Horizontal	H Period Total	t <sub>HP</sub>	840	900	1200	CLK	
	Hsync Frequency	f <sub>H</sub>	48.0	60.0	76.0	kHz	
	Vertical Valid	t <sub>vv</sub>	900	900	900	_	
Vertical	V Period Total	t <sub>VP</sub>	908	1000	1300	t <sub>HP</sub>	
	Vsync Frequency	f <sub>V</sub>	48	60	76	Hz	
DE	DE Setup Time	t <sub>SI</sub>	4	_	-		
(Data Enable)	DE Hold Time	t <sub>HI</sub>	4	-	-	ns	For D <sub>CLK</sub>
Data	Data Setup Time	t <sub>SD</sub>	4	_	-	nc	For D
Data	Data Hold Time	t <sub>HD</sub>	4	-	-	ns	For D <sub>CLK</sub>

#### Note:

- 1. DE Only mode operation. The input of Hsync & Vsync signal does not have an effect on LCD normal operation.
- 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rates.
- 3. Horizontal period should be even.



# 3-5. Signal timing waveforms





# 3-6. Color input data reference

The brightness of each primary color (red,green and blue) is based on the 8bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 8. Color data reference

	ble 8. Color data	-	CIT	3110																					
											Inp	out	Co	lor	Da	ata									
	Color				Re	ed							Gre	een							Bl	ue			
	Coloi	Μ	SB					LS	SB	Μ	SB					LS	SB	Μ	SB					LS	B
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	В7	В6	В5	В4	В3	В2	В1	В0
Basic Color	Black Red (255) Green (255) Blue (255) Cyan Magenta Yellow White	0 1 0 0 1 1 1	0 1 0 0 0 1 1 1	0 1 0 0 0 1 1 1	0 1 0 0 0 1 1 1	0 1 0 0 1 1 1	0 1 0 0 1 1 1	0 1 0 0 1 1 1	0 1 0 0 1 1 1	0 0 1 0 1 0 1	0 0 1 1 0 1	0 0 1 1 0 1	0 0 1 1 1 0 1	0 0 1 1 1 0 1	0 0 1 1 1 0 1	0 0 1 1 1 0 1	0 0 1 1 1 0 1	0 0 1 1 1 0 1							
Red	Red(000) Dark Red(001) Red(002)  Red(253) Red(254) Red(255) Bright	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 0 - 1 1	0 0 - - 1 1	0 0 1 - 0 1	0 1 0 - 1 0 1	0 0 0 - 0 0	000 000	0 0 0 - 0 0	000000	000 000	0 0 0 - 0 0	000 000	000 000	000000	000000	000 000	000000	000 000	000 000	0 0 - - 0 0	0 0 0 - 0 0 0
Green	Green(000) Dark Green(001) Green(002)  Green(253) Green(254) Green(255)Bright	000000	000 000	000 000	000 000	000 000	000 000	000 000	000 000	0 0 0 - 1 1	0 0 - - 1 1	0 0 0 - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 0 - 1 1	0 0 1 - 0 1 1	0 1 0 - 1 0 1	000 000	000 000	000 000	000 000	000 000	000 000	0 0 - - 0 0	0 0 0 - 0 0 0
Blue	Blue(000) Dark Blue(001) Blue(002) Blue(253) Blue(254) Blue(255) Bright	000 000	000 000	000 000	000 000	000 000	000 000	0 0 0 0 0 0	000 000	0 0 0 - 0 0 0	000 000	0 0 0 - 0 0 0	000 000	000 000	0 0 0 - 0 0 0	000 000	000 000	0 0 - - 1 1	0 0 - - 1 1	0 0 - - 1 1	0 0 - 1 1	0 0 - 1 1	0 0 - - 1 1	0 1 - 0 1 1	0 1 0 - 1 0 1



### 3-7. Power sequence

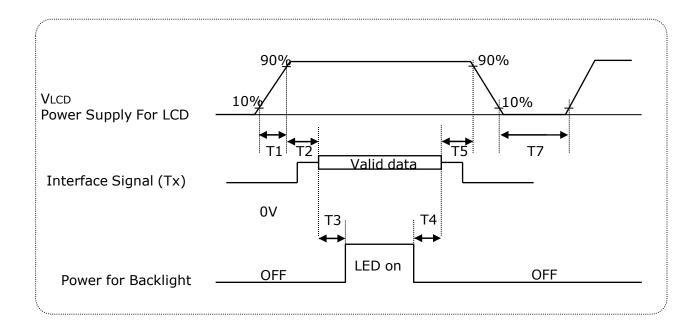


Table 9. Power sequence

Darameter		Values		Units
Parameter	Min	Тур	Max	UTILS
T1	0.5	-	10	ms
T2	0.01	-	50	ms
T3	500	-	-	ms
T4	200	-	-	ms
T5	0.01	-	50	ms
T7	1000	-	-	ms

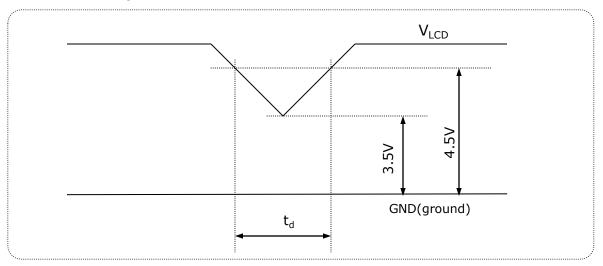
#### Notes:

- 1. Please avoid floating state of interface signal at invalid period.
- 2. When the interface signal is invalid, be sure to pull down the power supply for LCD  $V_{\text{LCD}}$  to 0V.
- 3. LED power must be turn on after power supply for LCD an interface signal are valid.



# 3-8. V<sub>LCD</sub> Power dip condition

### FIG. 6 Power dip condition



1) Dip condition

$$3.5V \le V_{LCD} < 4.5V$$
 ,  $t_d \le 20ms$ 

2)  $V_{LCD}$ < 3.5V

 $V_{\text{LCD}}$ -dip conditions should also follow the Power On/Off conditions for supply voltage.



# 4. Optical specification

Optical characteristics are determined after the unit has been 'ON' for 30 minutes in a dark environment at  $25^{\circ}$ C.

**Table 10. Optical characteristics** 

Ta= 25°C,  $V_{LCD}$ =5.0V,  $f_{V}$ =60Hz  $f_{CLK}$ =54.0MHz,  $I_{BL}$ =110mA

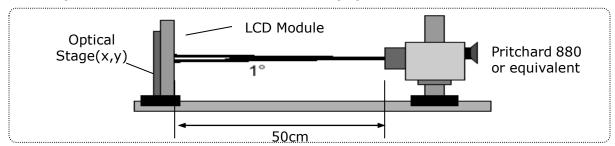
i dbic 1	ог орск	<u>cai ciiai acte</u>	1136165		1a – 23 C	, V <sub>LCD</sub> =5.0V,	1V-00112 1 <sub>Cl</sub>	_K=34.0M11	Z, IBL—110111/			
	Dayama	<b>+</b> ~~	Cymol	201		Values		Linita	Notes			
	Parame	ter	Syml	001	Min	Тур	Max	Units	Notes			
Contrast F	Ratio		CR		700	1000	-		1 (PR-880)			
Surface	Luminanc	ce, white	L <sub>W</sub>	1	200	250	-	cd/m <sup>2</sup>	2 (PR-880)			
Surface	Luminanc	ce, Black	L <sub>BL</sub>		-	-	0.6	cd/m <sup>2</sup>	2 (PR-880)			
Luminanc	e Variatio	n	δ <sub>WHITE</sub> 9P		75	-	-	%	3 (PR-880)			
Docnonco	nse Time Rise Time		Tr	ł.	-	1.1	2.6	ms	4			
Response	Decay Time		Tr	)	-	3.9	7.4	ms	(PR-880)			
Color Gam	Gamut				67.5	72	-	%	(PR-650)			
		RED	Rx			0.636						
		KED	Ry			0.330						
		CDEEN	Gx	,		0.304						
Color Coordinat [CIE1931]	rdinates	GREEN	Gy	,	Тур	0.629	Тур		(DD (E0)			
		DILLE	Bx		-0.03	0.154	+0.03		(PR-650)			
		BLUE	Ву			0.070						
		\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	W>	(		0.313						
		WHITE	Wy	,		0.329						
Viewing A	Angle (CR	>5)										
	x axis, rig	ght(φ=0°)	θr		75	88		Degree				
	x axis, le	ft (φ=180°)	θΙ		75	88						
	y axis, up	ο (φ=90°)	θu		70	85			5			
	y axis, d	own (φ=270°)	θd		70	85			(PR-880)			
Viewing A	Angle (CR	>10)										
	x axis, rig	ght(φ=0°)	θr		70	85		Degree				
	x axis, le	ft (φ=180°)	θl		70	85						
	y axis, up	ο (φ=90°)	θu		60	75						
	y axis, down (φ=270°)		θd		70	85						
Crosstall	Crosstalk						1.5	%	6 (PR-880)			
Luminance uniformity - Angular dependence (TCO5.0)			LR		-	-	1.73		7 (PR-880)			
Color gra	Color grayscale linearity			Color grayscale linearity		Δu'n	·′		0.018			8 (PR-650)



The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to 0 °.

FIG. 7 presents additional information concerning the measurement equipment and method.

#### FIG. 7 Optical characteristic measurement equipment and method



#### Notes:

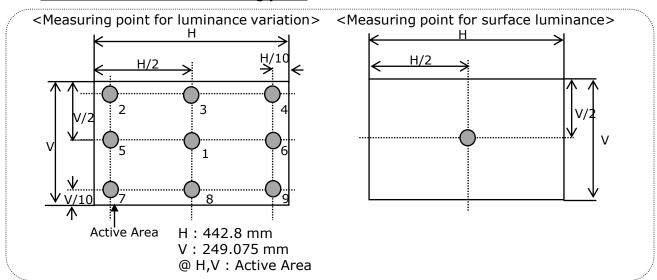
Contrast ratio(CR) is defined mathematically as :It is measured at center point(1)

- 2. Surface luminance is the luminance value at center 1 point(1) across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 8.
- 3. The variation in surface luminance ,  $\delta$   $_{\text{WHITE}}$  is defined as

$$\delta_{\text{WHITE}} = \frac{\text{Minimum (P1,P2 .....P9)}}{\text{Maximum (P1,P2 .....P9)}}$$

For more information see Figure 8.

#### FIG. 8 Luminance measuring point





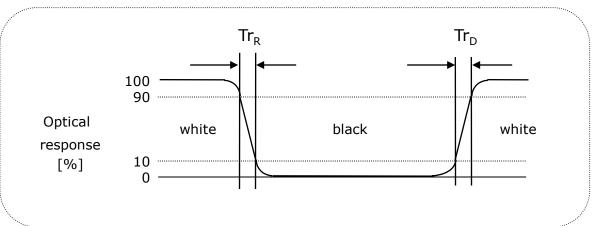
#### Notes:

4. Response time is the time required for the display to transition from black to white (Decay Time, Tr<sub>D</sub>) and from white to black (Rise Time, Tr<sub>R</sub>)

The sampling rate is 2,500 sample/sec. For additional information see FIG. 9.

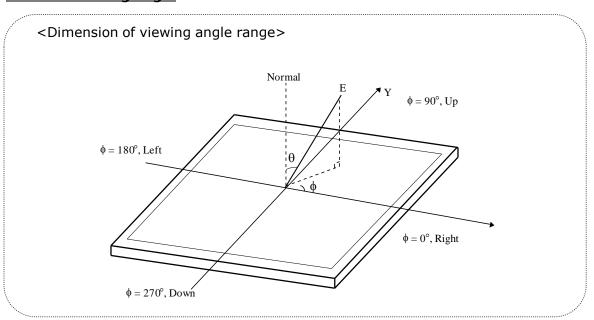
The response time is defined as the following figure and shall be measured by switching the input signal for each gray to gray.

FIG. 9 Response time (measurement equipment : RD-80S)



5. Viewing angle is the angle at which the contrast ratio is greater than 10 or 5. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG. 10.

FIG. 10 Viewing angle





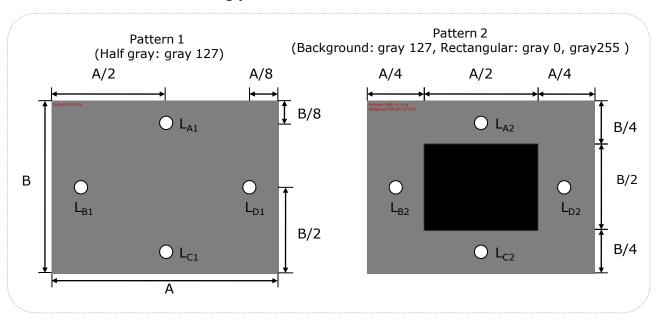
#### Notes:

6. Crosstalk is defined as

The equation of crosstalk : 
$$(|L_{A[or\ C]2}-L_{A[or\ C]1}|/L_{A[or\ C]1})\times 100(\%)$$
 [Vertical],  $(|L_{B[or\ D]2}-L_{B[or\ D]1}|/L_{B[or\ D]1})\times 100(\%)$  [Horizontal]

For more information see Figure 11.

### FIG. 11 Crosstalk measuring point



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#### Notes:

7. Luminance Uniformity - angular - dependence (LR& TB)

TCO 5.0 Luminance uniformity – angular dependence, is the capacity of the VDU to present the same Luminance level independently of the viewing direction. The angular-dependent luminance uniformity is calculated as the ratio of maximum luminance to minimum luminance in the specified measurement areas.

- Test pattern  $\,$  : Full white 4  $^{\circ}$  × 4  $^{\circ}$  square size, back ground shall be set to 80%

image loading, RGB 204, 204, 204

- Test luminance : ≥150cd/ m²

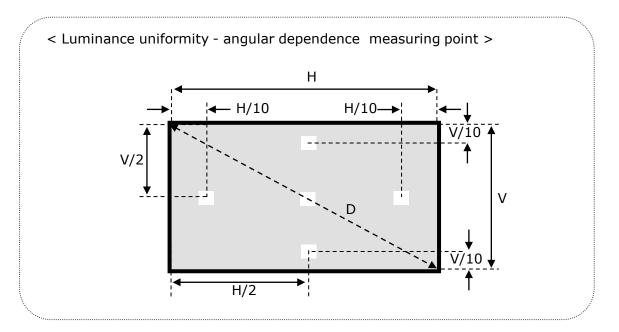
- Test point : 5-point

- Test distance : D \* 1.5 = 76.22cm

- Test method :  $L_R = ((L_{\text{max.}+30\text{deg.}} / L_{\text{min.}+30\text{deg.}}) + (L_{\text{max.}-30\text{deg.}} / L_{\text{min.}-30\text{deg.}})) / 2$ 

 $T_{B} = ((L_{\text{max},+15\text{deg}} / L_{\text{min},+15\text{deg}})$ 

#### FIG. 12 Luminance Uniformity angular dependence





Notes:

8. Color grayscale linearity,  $\Delta u'v'$  is defined as

$$\sqrt{(u'_A - u'_B)^2 + (v'_A - v'_B)^2}$$

Where indices A and B are the two gray levels found to have the largest color differences between them.

i.e. get the largest  $\Delta u'$  and  $\Delta v'$  of each 6pairs of u' and v' and calculate  $\Delta u'v'$ .

-Test pattern: 100% full white pattern with a test pattern as shown FIG.12

Squares of 40mm by 40mm in size, filled with 255, 225, 195, 165, 135 and 105

grayscale steps should be arranged in the center of the screen.

-Test method

First gray step: move a square of 255 gray level should be moved into the center of the screen

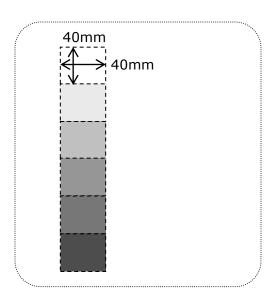
and measure luminance and u' and v' coordinates.

Next gray step: move a 255 gray square into the center and measure both luminance and

u' and v' coordinates.

The same procedure shall then be repeated for gray steps 195, 165, 135 and 105.

### FIG. 13 Color grayscale linearity



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### 5. Mechanical characteristics

The contents provide general mechanical characteristics. In addition the figures in the next page are detailed mechanical drawing of the LCD.

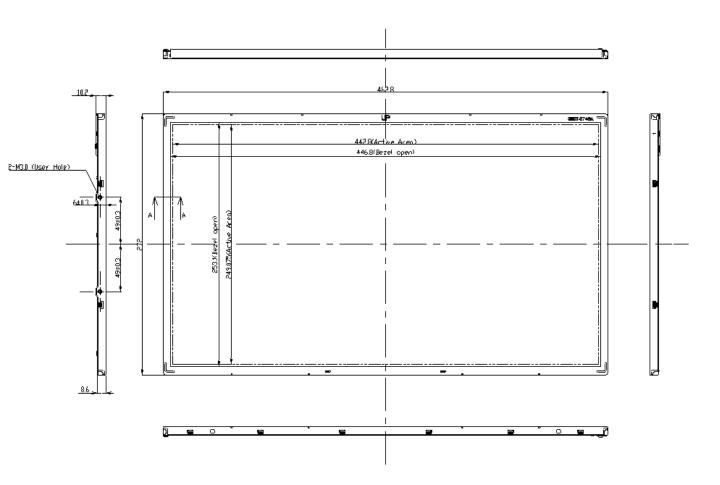
**Table 11. Mechanical characteristics** 

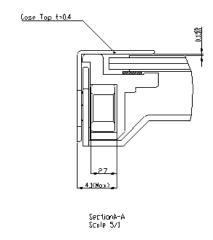
	Horizontal	462.8 mm
Outline dimension	Vertical	272.0 mm
	Depth	10.2 mm
Bezel area	Horizontal	446.8 mm
Dezei area	Vertical	253.1 mm
Active display area	Horizontal	442.8 mm
Active display area	Vertical	249.075 mm
Weight	1,190g (Typ.), 1,250g (Max)	
Surface treatment	Hard coating(3H) Anti-glare treatment of the front	polarizer

Notes: Please refer to a mechanic drawing in terms of tolerance at the next page.



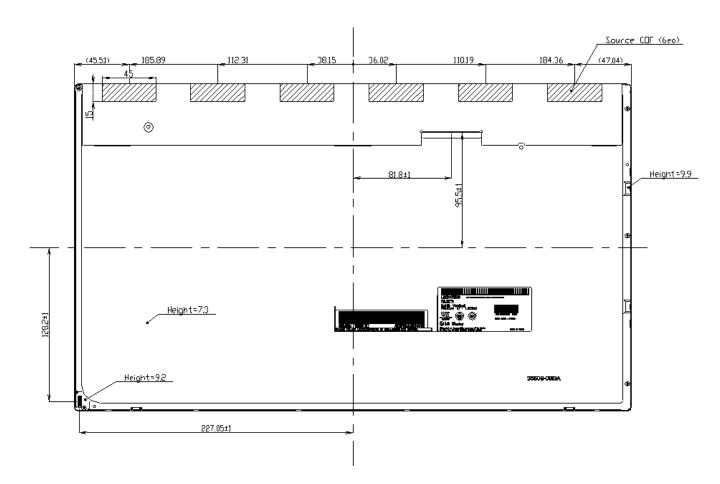
# <FRONT VIEW>





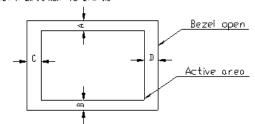


### <REAR VIEW>



Notes

- Notes
  1. Bocklight has 1 LED Array Ass'y
  2. 1/F Connector Specification : GT103-30S-H23-D (LGM) or Equivalent,
  3. LED Connector Specification : 10019HR-6PIN (YEDNHD)
  4. Torque of user hale : 30^4.0kgf-cm
  5. Tilt and partial disposition talerance of display area as following
  (1) Y-Direction : IA-BI ≤ 1.0
  (2) X-Direction : IC-DI ≤ 1.0



6. Unspecified toleronces to be  $\pm 0.5$ mm 7. The CDF area is weak \$ sensitive, So, please don't press the CDF area.



# 6. Reliability

**Table 12. Environment test conditions** 

No	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240hrs
2	Low temperature storage test	Ta= -20°C 240hrs
3	High temperature operation test	Ta= 50°C 50%RH 240hrs
4	Low temperature operation test	Ta= 0°C 240hrs
5	Vibration test (non-operating)	Wave form: random Vibration level: 1.0GRMS Bandwidth: 10-300Hz Duration: X,Y,Z, 20 min One time each direction
6	Shock test (non-operating)	Shock level : 120G Waveform : half sine wave, 2msec Direction : $\pm X$ , $\pm Y$ , $\pm Z$ One time each direction
7	Altitude operating storage / shipment	0 - 16,400 feet(5,000m) 0 - 40,000 feet(12,192m)

 $<sup>\{ \ \</sup>mathsf{Result} \ \mathsf{evaluation} \ \mathsf{criteria} \ \}$ 

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.



#### 7. International standards

### 7-1. Safety

- a) UL 60950-1, Second Edition, Underwriters Laboratories Inc.
  Information Technology Equipment Safety Part 1: General Requirements.
- b) CAN/CSA C22.2 No.60950-1-07, Second Edition, Canadian Standards Association. Information Technology Equipment Safety Part 1: General Requirements.
- c) EN 60950-1:2006 + A11:2009, European Committee for Electrotechnical Standardization(CENELEC). Information Technology Equipment Safety Part 1 : General Requirements.
- d) IEC 60950-1:2005, Second Edition, The International Electrotechnical Commission (IEC). Information Technology Equipment Safety Part 1 : General Requirements. (Including report of IEC60825-1:2001 clause 8 and clause 9)

#### **Notes**

1. Laser (LED Backlight) Information

Class 1M LED Product IEC60825-1 : 2001 Embedded LED Power (Class1M)

#### 2. Caution

: LED inside.

Class 1M laser (LEDs) radiation when open.

Do not open while operating.

#### 7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

#### 7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003



# 8. Packing

## 8-1. Designation of lot mark

a) Lot mark

Α	В	С	D	Е	F	G	Н	I	J	K	L	М	

A,B,C : Size (Inch) D : Year

E: Month  $F \sim M$ : Serial No.

#### Note:

#### 1. Year

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	С	D	Е	F	G	Н	J	K

#### 2. Month

	Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
I	Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of lot mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

# 8-2. Packing form

a) Package quantity in one box: 12 pcs

b) Box size : 366mm x 315mm X 578mm



#### 9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

### 9-1. Mounting Precautions

- (1) You must mount a module using holes arranged in left sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the Module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

# 9-2. Operating precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :  $V=\pm200mV(Over\ and\ under\ shoot\ voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes higher.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can not be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw (if not, it causes metal foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.



### 9-3. Electrostatic discharge control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

### 9-4. Precautions for strong light exposure

Strong light exposure causes degradation of polarizer and color filter.

### 9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

  It is recommended that they be stored in the container in which they were shipped.

### 9-6. Handling precautions for protection film

- (1) The protection film is attached to the bezel with a small masking tape.

  When the protection film is peeled off, static electricity is generated between
  the film and polarizer. This should be peeled off slowly and carefully by people who are
  electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.