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## **SPECIFICATION**

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**FOR** 

#### TOSHIBA TFT-LCD MODULE

## LTM12C278E

DATE OF ISSUE: 1999-12-13

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H. Kimura
Senior Manager
Liquid Crystal Display
Quality Assurance Dept.
Liquid Crystal Display Div.

<Engineering Department to contact as per SPECIFICATION>
Liquid Crystal Display Device Marketing & Engineering Dept.

Liquid Crystal Display Div.

Display Devices & Components Company

TOSHIBA Corporation

8, Shinsugita-cho, Isogo-ku, Yokohama, 235-8522, JAPAN

## **Revision History**

Date	Sheet (New)	ltem	Old	New	Reason
		٠			

TOSHIBA CORPORATION LIQUID CRYSTAL DISPLAY DIVISION Date:1999-12-13

Date:

New No.NR-LTM12C278E-13 Old No.

#### **Caution and Handling Precaution**

For your end user's safety, it is strongly advised that the items with"\* should be included in the instruction manual of the system which may be issued by your organization.

# For Safety Warning

- (1) Toshiba's Standard LCD modules have not been customized for operation in extreme environments or for use in applications where performance failures could be life-threatening or otherwise catastrophic.
  - Since they must never be installed in aircraft navigation control systems (such as, but not limited to Traffic Collision System and Air traffic Indicator), in military defense or weapons systems, in critical industrial process-control systems (e.g., those involved in the production of nuclear energy), or in critical medical device or patient life-support systems.
- (2) DISCONNECT POWER SUPPLY before handling LCD module.
  - DO NOT TOUCH the parts inside LCD module and the fluorescent lamp's (hereinafter called "FL") connector or cable in order to prevent electric shock, because high voltage is supplied to these parts from the inverter unit while power supply is turned on.
- (3) Make sure to insert the module FL connector to the inverter connector in correct position.
  - Do not insert in irregular position.
  - If incorrect, this may cause smoke or burn of electrical parts by high voltage of FL circuit.
  - If there is a possibility that the connector has been inserted incorrectly, please re-insert the connector only after you confirm the module and FL power is completely off.
  - DO NOT USE the mating FL connector which Toshiba does not specify.
  - Otherwise, Toshiba shall not be liable for any damages caused by the connector.



#### Caution

- (1) DO NOT DISASSEMBLE OR MODIFY the module.
  - Sensitive parts inside LCD module may be damaged, and dusts or scratches may mar the displays.
  - Toshiba does not warrant the modules, if customer disassembled or modified them.
- \*(2) DO NOT INGEST liquid crystal material, DO NOT INHALE this material, and DO NOT PERMIT this material to contact the skin, if LCD panel is broken and liquid crystal material spills out.
  - In the event of inadvertent contact, immediately rinse the mouth or eyes with adequate water. If this material should inadvertently contact the skin or clothing, wash immediately with alcohol and then rinse thoroughly with water.
- \*(3) BE CAREFUL WITH CHIPS OF GLASS that may cause injuring fingers or skin, when the glass is broken.
  - (4) DO NOT EXCEED the absolute maximum rating values under the worst probable conditions caused by the supply voltage variation, input voltage variation, variation in parts' constants, ambient temperature, etc., otherwise LCD module may be damaged.
  - (5) Suitable protection circuit should be applied for each system design.
    - DO NOT MODIFY the fuse used in the module. It may cause overheat and/or burning if dusts or metal particles are on the PCBs in the LCD module.

(6) Be sure that power supply output from the system should be limited to smaller values than listed shown below. (For example Quick Arcing Fuse with listed ratings can be used.)

It is because this LCD module explained in this specification has a current limiter, with such function at power input line(s). But it may be some possibility of overheat and/or burning of LCD module and its peripheral devices before current limiter of the module when open-short test of the module is performed by using power supply higher than following recommended value.

Power supply	Recommended maximum output current of power supply	Recommended Fuse Rating (in case of using fuse for current limiter)	Built-in Fuse Rating (for reference)
V <sub>DD</sub>	4.0 A	1.0 A	1.0 A

(7) Always comply with all applicable environmental regulations, when disposing of LCD.

#### For Designing the System

- (1) LCD module should be assembled to the system by using all mounting holes specified in this specification and with the specified screws.
- (2) Power supply lines should be designed as follows.
  - Power supplies should always be turned on before the input signals are supplied to LCD module, and the input signals should be disconnected before power supplies are turned off.
  - If the sequence does not satisfy specified conditions, it may cause miss-operation of the panel.
  - Refer to "2.4.2 Sequence of Power Supplies and Signals" for the detailed specification.
- (3) DO NOT GIVE high voltage to "Low Voltage" side of the FL.
  - For example, DO NOT USE a floating inverter which gives high voltage to "Low Voltage" side. That's because it has a possibility to burn or smoke around the FL.
- (4) Make sure to connect correctly high-voltage wire and low-voltage wire between FL tube and inverter unit.
- (5) Input FL starting voltage( $V_{\rm SFL}$ ) should not be less than one second.
  - If it were less than one second, it may cause unstable operation of FL.
  - Please adjust inverter circuit parameters, such as capacitor, resistor, to assure the display quality is maintained.
  - There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency).
- (6) In case of severe environmental condition like outdoor usage, a proper transparent protective cover(lens) over LCD module is recommended to apply in order to prevent scratches, and invasion of dust, water, etc., from the system's window onto LCD module.
  - Ultra-violet ray cut filter is recommended to apply onto LCD module for outdoor operation. Strong ultra-violet ray may cause damage the panel.
- (7) Design the system not to display same pattern for a long time in order to prevent image sticking on the panel. Note that incorrect sequence of power supplies and input signals may cause the sticking on the panel, too.

#### For Installation in Assembly

- The C-MOS LSIs used in LCD module are very sensitive to ESD (Electro-static Discharge).
  - Ambient humidity of working area is recommended to be higher than 50%(RH).
  - Person handling LCD modules should be grounded with wrist band. Tools like soldering iron and screw driver, and working benches should be grounded.
  - The grounding should be done through a resistor of 0.5-1M $\Omega$  in order to prevent spark of ESD.
- (2) When remove protection film from LCD panel, peer off the film slowly (more than three seconds) from the edge of the panel, using a soft-pointed tweezers covered by teflon or adherent tape.
- (3) Reduce dust level in working area. Especially the level of metal particle should be decreased.
  - Use finger stalls or soft and dust-free gloves in order to keep clean appearance of LCD module when handled for incoming inspection and assembly.
- \*(4) When LCD panel becomes dirty, wipe off the panel surface softly with absorbent cotton or another soft cloth.
  - If necessary, breathe upon the panel surface and then wipe off immediately and softly again.
  - If the dirt can not be wiped off, absorbent cotton wetted a little with normal-hexane or petroleum benzine can be used for wiping the panel.
  - Be careful not to spill this solvent into the inside of LCD module. Driver ICs and PCB area used inside LCD module may be damaged by the solvent.
- \*(5) AVOID THE CONDENSATION OF WATER
  - Wipe off a spot or spots of water of mist and chemicals of mist on LCD panel softly with absorbent cotton or another cloth as soon as possible if happened, otherwise discoloration or stain may be caused. If water invade into LCD module, it may cause LCD module damages.
- \*(6) Do not expose LCD module to the gas (which is not normally contained in the atmosphere), it may cause mis-operation or defects.
- \*(7) DO NOT APPLY MECHANICAL FORCES.
  - Do not bend or twist LCD module even momentary when LCD module is installed an enclosure of the system. Bending or twisting LCD module may cause its damages.
  - Make sure to design the enclosure that bending/twisting forces are not applied to LCD module when it is installed in the system.
  - Refrain from strong mechanical shock like dropping from the working bench or knocking against hard object.
  - These may cause glass of the panel crack, damage of FL or other mis-operation.
- \*(8) Refrain from excessive force like pushing the surface of LCD panel. This may cause damage of the panel or electrical parts on PCB.
- \*(9) Do not put heavy object such as tools, books, etc., and do not pile up LCD modules.
  - Be careful not to touch surface of the polarizer laminated to the panel with any hard and sharp object. The polarizer is so soft that it can easily scratched, even the protect film covers it.
- (10) When inserting or disconnecting the connectors to LCD module, be sure not to apply force against PCB, nor connecting cables, otherwise internal connection of PCB and TAB drivers may be damaged.
  - Do not fasten screws while putting cables like those for interface or FL between LCD module and the enclosure.
  - Make sure to insert the module FL connector to the inverter connector in correct position.
  - If incorrect, this may cause smoke or burn of electrical parts by high voltage of FL circuit.

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- (11) Be careful not to pull the FL cables of the backlight in order to avoid mechanical damage in FL lamp and soldering area. Be careful not to pull or not to hurt the FPC (Flexible Printed Circuit) cables.
- (12) Power supplies should always be turned off in assembling process.
  - Do not connect or disconnect the power cables and connectors with power applied to LCD module. This may cause damage of module circuit.
  - The signal should be applied after power are turned on. And the signal should be removed before power supplies are turned off. (Refer to "For Designing The System"(2).)

## For Transportation and Storage

- (1) Do not store LCD module in high temperature, especially in high humidity for a long time (approximately more than one
  - It is recommended to store LCD module where the temperature is in the range of 0 to 35 °C and the relative humidity is lower than 70%.
- Store LCD module without exposure to direct sunlight or fluorescent lamps in order to prevent the module from strong ultra violet ray.
- Avoid condensation of water on LCD module, otherwise it may cause mis-operation or defects. Keep away LCD module from such ambient.
  - (4) In case of transportation of storage after opening the original packing. LCD module are recommended to be repacked into the original packaging with the same method, especially with same kind of desiccant.

TOSHIBA

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#### 1. Scope

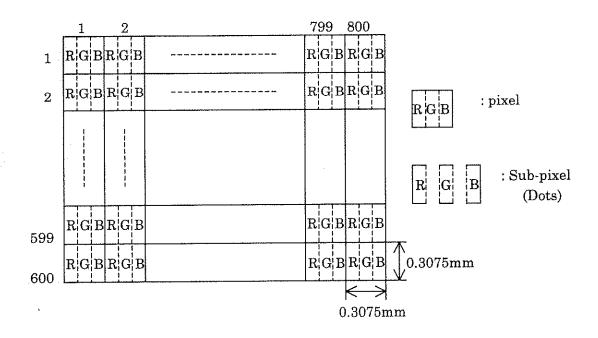
This specification is applicable to Toshiba's 31cm diagonal size TFT-LCD module "LTM12C278E" designed for Personal Computer.

## 2. Product Specifications

### 2.1 General Specifications

Item	Specifications
Display Mode	TN color(64 gray scales, 262,144 colors)
•	Transmissive type, Normally white
Viewing Direction	6 o'clock (in direction of maximum contrast)
Driving Method	TFT active matrix
Input Signals	LVDS interface
•	CLK+,CLK-
	INO+,INO-
	IN1+,IN1-
	IN2+,IN2-
Active Area	246.0 (W) × 184.5 (H) (mm)
Viewing Area	249.0 (W) × 187.5 (H) (mm)
Number of Pixels	$800  (W) \times 600  (H)  ^{1)}$
Pixel Pitch	$0.3075 (W) \times 0.3075 (H) (mm)^{-1}$
Pixel Arrangement	RGB vertical stripes 1)
Surface Treatment	Anti-glare and hard coat 3H on LCD surface
Backlight	Single cold-cathode fluorescent lamp for sidelighting
Dimensional Outline	275.3 (W) $\times$ 199.5 (H) $\times$ 7.5max. (D) (mm)

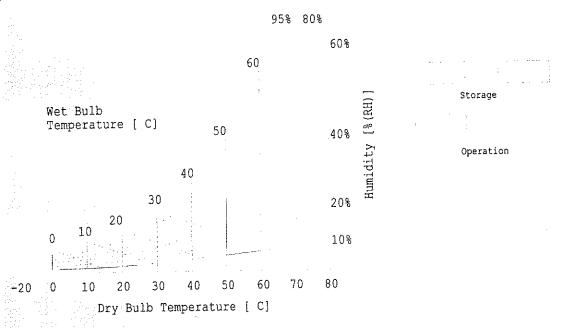
Note 1)



## 2.2 Absolute Maximum Ratings 1)

11	Symbol	Min.	Max.	Unit	Checked Terminal 4)
Item		-0.3	+4.0	V	V <sub>pp</sub> - GND
Supply Voltage	$V_{DD}$	-0.3	V <sub>DD</sub> +0.3	V	LVDS interface
Input Voltage of Signals		-0.3	2.0	kV(rms)	V <sub>FLH</sub> - V <sub>FLL</sub>
FL Driving Voltage	V <sub>FL</sub>	<del>-</del>	100	kHz	FLH
FL Driving Frequency		<u> </u>			<u> </u>
Operating Ambient Temperature 2)	$T_{OP}$	0	50	°C	
Operating Ambient Humidity	H <sub>OP</sub>	10	90	%(RH)	
Storage Temperature 2		-20	+60	°C	
	2) H <sub>STG</sub>	10	90	%(RH)	
Operating Temperature for Panel	3)	0	+60	°C	

- Note 1) Do not exceed the maximum rating values under the worst probable conditions taking into account the supply voltage variation, input voltage variation, variation in part constants, and ambient temperature and so on. Otherwise the module may be damaged.
  - 2) Wet bulb temperature should be 39°C Max, and no condensation of water. See figure below.
  - 3) The surface temperature caused by self heat radiation of cell itself is specified on this item.
  - 4) Refer to 2.4.5

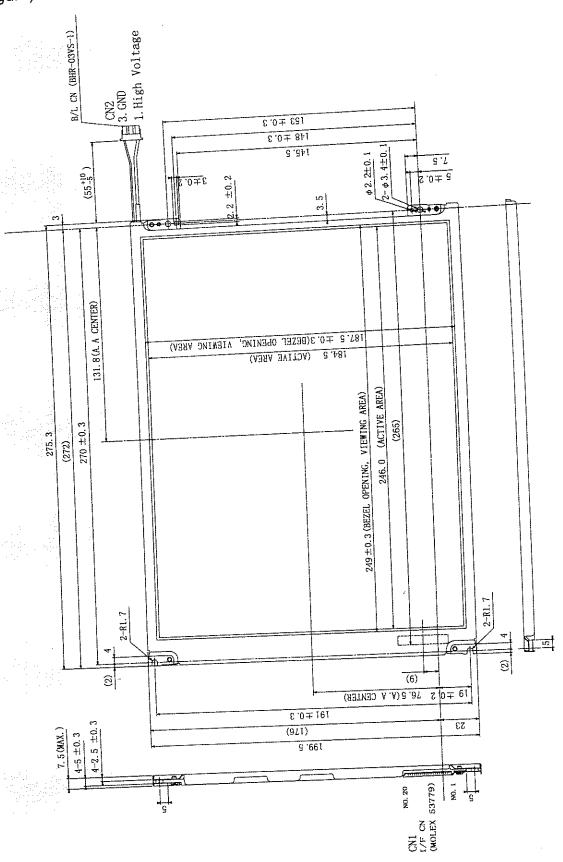


## 2.3 Mechanical Specifications 2.3.1 Weight

 $490g \pm 20g$ 

2.3.2 Dimensional Outline (front figure)

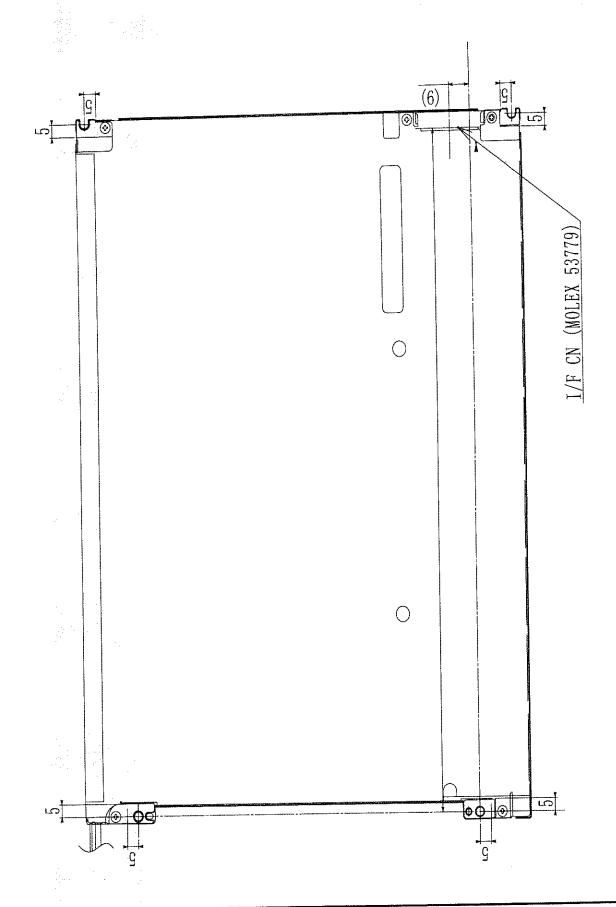
Unit : mm Standard Tolerance; ±0.5



(back figure)

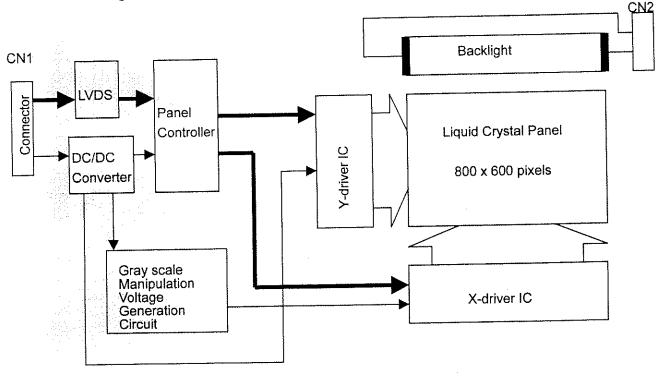
Unit: mm

Standard Tolerance: ±0.5

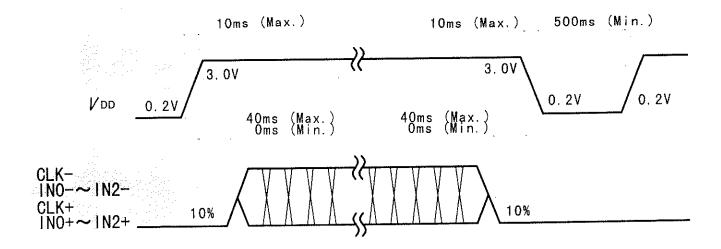


### 2.4 Electrical Specifications

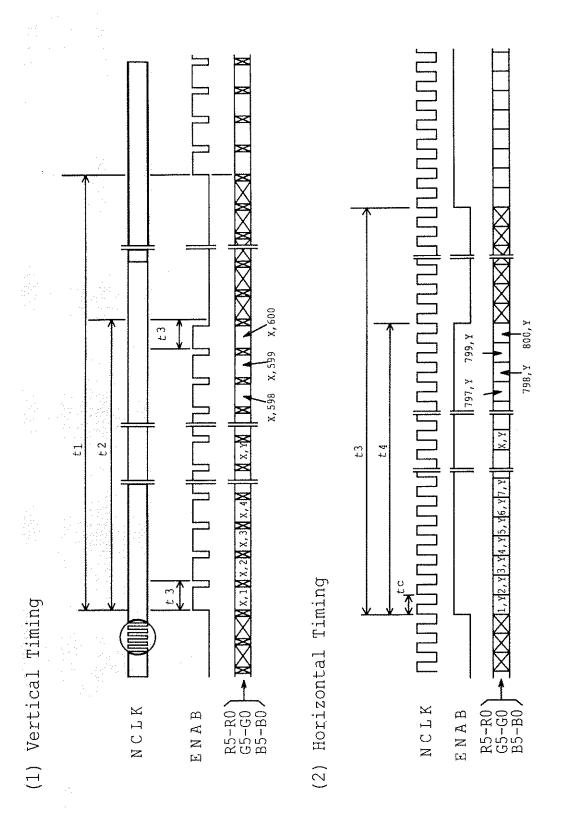
#### 2.4.1 Circuit Diagram



## 2.4.2 Sequence of Power Supplies and Signals



## 2.4.3 Timing Chart



## 2.4.4 Timing Specifications 1) 2) 3) 4) 5) 6) 7)

Item	Symbol	Min.	Тур.		<del></del>	···
Frame Period 1) 4)	t1	604 40		Max.	Unit	Remarks
	''	604 × <i>t</i> 3	628 × t3	677 × t3	_	
Vertical Display Time 1)	t2		16.67	_	ms	
Horizontal Scanning		600 × t3	600 × <i>t</i> 3	600 × t3	1	
Time 1)4)	t3	844 × tc	1056 × tc	1064 × tc	<del>                                     </del>	
lorizontal Display Time 1)	t4	26.3	26.4	-	μs	
Clock Period 4)		800 × <i>t</i> c	800 × tc	800 × tc	<del> </del>	
SIOCK 1, GIIOG 1/	tc	24.0	25.0		<del>                                     </del>	
		27.0	25.0		ns	

Note 1) Refer to "2.4.3 Timing Chart" and LVDS (SN75LVDS86) specifications by TEXAS INSTRUMENTS.

Note 2) If ENAB is fixed to "H" or "L" level for certain period while NCLK is supplied, the panel displays black with some flicker.

Note 3) If NCLK is fixed to "H" or "L" level for certain period while ENAB is supplied, the panel may be damaged.

Note 4) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality. There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency), even if the condition satisfies above timing specifications and recommended operating

Note5 ) Do not make t1, t2 and t3 fluctuate. If t1, t2 and t3 are fluctuate, the panel displays black.

Note6) In case of using the long frame period, the deterioration of display quality, noise etc. may be occurred.

Note7) NCLK count of each Horizontal Scanning Time should be always the same. V-Blanking period should be "n" X "Horizontal Scanning Time". (n: integer) Frame period should be always the same.

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# 2.4.5 Interface Connector

CN1 INPUT SIGNAL (53779-2010/ JAPAN MOLEX CO., LTD.)

[ Mating Connector : 51146-2000(housing) / 50641-8041(Contact Pin) ]

CN1 INPUT SIC	-4446.20	100(housing) / 5064 1-504 1(5
[ Mating Cor	nector: 51146-20	000(housing) / 50641-6041(05
Terminal No.	Symbol	POWER SUPPLY:+3.3V
1	$V_{DD}$	POWER SUPPLY :+3.3V
2	$V_{\rm DD}$	POWERS
3	GND	
	GND	Transmission data of pixels 0 (negative : -)
4	INO-	Transmission data of pixels 0 (negative : +)  Transmission data of pixels 0 (negative : +)
5	IN0+	Transmission data
6	GND	Transmission data of pixels 1 (negative : -)
7	IN1	Transmission data of pixels 1 (negative : +)  Transmission data of pixels 1 (negative : +)
8	IN1+	Transmission data of P
9	GND	Transmission data of pixels 2 (negative : -)
10	IN2-	Transmission data of pixels 2 (negative : +)  Transmission data of pixels 2 (negative : +)
11	IN2+	Transmission data or p.s.
12	GND	1. It (negative : - )
13	CLK-	Sampling clock (negative : - )
14	CLK+_	Sampling clock (positive : + )
15	GND_	(ODEN)
16	NC1) _	Non Connection (OPEN)
17	NC1)	Non Connection (OPEN)
18	GND	
19	GND_	
20	GND	

CN2 CCFL POWER SOURCE (BHR-03VS-1/JAPAN SOLDERLESS TERMINAL MFG CO., LTD.)

[ Mating Connector : SM02(8.0)B-BHS/ JAPAN SOLDERLESS TERMINAL MFG CO.,LTD. ]

CN2 CCFL I OWE	OMP · reter · SMO	2(8,0)B-BHS/ JAPAN SOLDLINGES
[ Mating	Connector . Sino	2(8.0)B-BHS/ JAPAN SOLDLINEES
Terminal No.	Symbol	Function CCFL POWER SUPPLY (HIGH VOLTAGE)
terminal ivo.	VEH	CCFL POWER SUFF ET (III
11	NC <sup>1)</sup>	Non Connection (OPEN)
2	NG -	CCFL POWER SUPPLY (LOW VOLTAGE)
3	VELL	T COLUMN TO THE TOTAL TO
L		

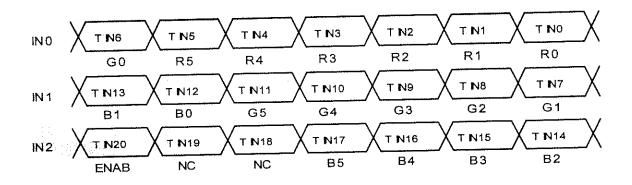
Note 1) Please connect NC pin to nothing. Don't connect it to grand nor to other signal input. Please connect GND to ground. Don't use it as no-connect nor connection with high impedance.

Note 2) 262,144 colors are displayed by the combinations of 18 bits data. (See next page)

# RECOMMENDED TRANSMITTER (SN75LVDS84/85) TO LTM12C278E INTERFACE ASSIGNMENT

# Case1: 6Bit TRANSMITTER

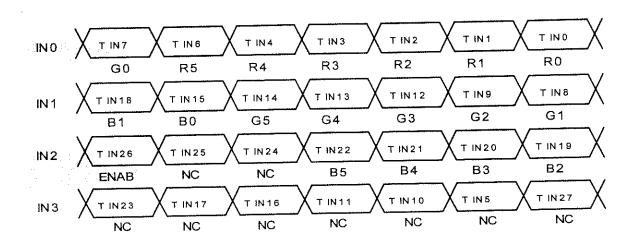
Input Te	erminal No.	Input Signal (Graphics controller output signal)		Output Signal Symbol	To LTM Interfa	12C278E ce(CN1)
			Function	,	Terminal	Symbol
Symbol	SN75LVDS84/85	Symbol	Red Pixels Display Data (LSB)			
TINO	44	R0	Red Pixels Display Data (2007)			
TIN1	45	R1		TOUT0-	No.5 No.6	INO-
TIN2	47	R2	Red Pixels Display Data	TOUT0+		1N0+
TIN3	48	R3	Red Pixels Display Data	1		
TIN4	11	R4	Red Pixels Display Data	1		
TIN5	3	R5	Red Pixels Display Data (MSB)	-		}
TIN6	4	G0	Green Pixels Display Data (LSB)		<del> </del>	
TIN7	6	G1	Green Pixels Display Data	1	1	
TIN8	7	G2	Green Pixels Display Data	TOUT1-	No.8 No.9	IN1- IN1+
TIN9	9	G3	Green Pixels Display Data	TOUT1+		
TIN10	10	G4	Green Pixels Display Data	-		
TIN11	12	G5	Green Pixels Display Data (MSB)	-		
TIN12	13	B0	Blue Pixels Display Data (LSB)	1		
TIN13	15	B1	Blue Pixels Display Data			
TIN14	16	B2	Blue Pixels Display Data			
TIN15	18	В3	Blue Pixels Display Data	TOUT2	No.11 IN2	IN2-
TIN16	19	B4	Blue Pixels Display Data	TOUT2- TOUT2+	No.12	IN2+
TIN17	20	B5	Blue Pixels Display Data (MSB)	100127	100.12	1112
TIN18	22	NC	Non Connection (open)			
	23	NC	Non Connection (open)			<b> </b>
TIN19	25	ENAB	Compound Synchronization Signal			011/
TIN20	26	NCLK	Data Sampling Clock	TCLK OUT-	No.14	CLK-
CLK IN	20	HOLK	Jan	TCLK OUT+	No.15	CLK+



# RECOMMENDED TRANSMITTER (SN75LVDS83) TO LTM12C278E INTERFACE ASSIGNMENT

# Case2: 8Bit TRANSMITTER

Innut T	erminal No.		Input Signal	Output Signal	To LTM1	12C278E ce(CN1)
Hipar i	CITIIII CITTO		(Graphics controller output signal)	Symbol	Terminal	Symbol
Symbol	SN75LVDS83	Symbol	Function	- Oyriboi		
TINO	51	R0	Red Pixels Display Data (LSB)	<del></del>		INO- INO+
TIN1	52	R1	Red Pixels Display Data	TOUTO-	No.5	
TIN2	54	R2	Red Pixels Display Data	TOUT0+	No.6	
TIN3	55	R3	Red Pixels Display Data			
TIN4	56	R4	Red Pixels Display Data			
TIN6	3	R5	Red Pixels Display Data (MSB)			
TIN7	4	G0	Green Pixels Display Data(LSB)			
TIN8	6	G1	Green Pixels Display Data		1	
TIN9	7	G2	Green Pixels Display Data	TOUT1-	No.8	IN1-
TIN12	11	G3	Green Pixels Display Data	TOUT1+	No.9	IN1+
TIN13	12	G4	Green Pixels Display Data			1
TIN14	14	G5	Green Pixels Display Data(MSB)		1	
TIN15	15	B0	Blue Pixels Display Data (LSB)			
TIN18	19	B1	Blue Pixels Display Data			
TIN19	20	B2	Blue Pixels Display Data		110.11	
TIN20	22	B3	Blue Pixels Display Data	TOUT2-		IN2-
TIN21	23	B4	Blue Pixels Display Data	TOUT2+		IN2+
TIN22	24	B5	Blue Pixels Display Data (MSB)	10012	140.12	1
TIN24	27	NC	Non Connection (open)			1
TIN25	28	NC	Non Connection (open)			
TIN26	30	ENAB	Compound Synchronization Signal			
TIN27	50	• NC	Non Connection (open)		1	
TIN5	2	NC	Non Connection (open)	TO (T2	1	_
TIN10	8	NC	Non Connection (open)	TOUT3- TOUT3+	_	
TIN11	10	NC	Non Connection (open)			
TIN16	16	NC	Non Connection (open)			
TIN17	18	NC	Non Connection (open)			
TIN23	25	NC	Non Connection (open)	=0.1(0):=	N= 44	CLK IN-
CLKIN	31	NCLK	Data Sampling Clock	TCLK OUT- TCLK OUT+	No.14 No.15	CLK IN-



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## 2.4.6 Colors Combination Table

		77 74 73 73 71 70 C5 G4 G3 G2 G1 G0 B5 B4 B3 B2 B1 B0	Gray ScaleLevel
	Display	R5 R4 R3 R2 R1 R0   G5 G4 G3 G2 G1 G5 G4 G3 G2 G1	-
	Black		
	Blue		
Ī	Green	T T T T T H H H H H H H H H H	***
Basic	Light Blue	L L L L L H H H H H H H H A T T T T T T T T T T T	
Color	Red	H H H H H H L L L L L L L L L L L L L L	
	Purple	H H H H H H L L L L L L L L L L L L L L	-
	Yellow	H H H H H H H H H H H H H H H H H	_
	White	H H H H H H H H H H H H H L L L L	L 0
	Black		L 1
			L 2
Gray	Dark		L3
Scale of	<b>↑</b>	· · · · · · · · · · · · · · · · · · ·	L60
Red	↓	: : : : : : : : : : : : : : : : : : : :	L61
	Light	H H H H L H L L L L L L L L L L L L L L	L62
	Red	HHHHHLLLLLLLLLLLLLL	Red L63 L 0
	Black	T. T. T. T. L.	L 1
	Diack	T. T. D.	L 2
Gray	Dark	T. J. L.	
Scale of	1	:	L3
Green	1 1	; ;	L60
<u> </u>	Light		L61
100 100			L62
		L L L L L H H H H H H L L L L L L L L L	Green L63
	Green		F 0
	Black		L 1
•	DI.	T T T T T T T T T T T T T T T T T T T	L 2
Gray	Dark		L3
Scale of Blue			L60
Diue	l iabt		L61
:	Light		L62
			Blue L63
	Blue		L 0
	Black		L 1
Gray			L 2
Scale of	Dark	L L L L L L L L L L L L L L L L L L L	L3
White &	1		L60
Black	1.1-1-4		L61
	Light	H H H H L H H H H H L H H H H L H	L62
ji.	. I ar fair	H H H H H L H H H H L H H H H H L	
45	White	H H H H H H H H H H H H H H H H H	Murce nos

Note1 L: Low level voltage, H: High level voltage

# 3. Recommended Operating Conditions 1) 2) 3) 11)

. Kecommona -					Unit	Remarks
	Symbol	Min.	Тур.	Max.	<del>- '\'</del>	
item	V <sub>DD</sub>	3.0	3.3	3.6		
Supply Voltage 4)	VID	0.1		0.6		
Differential input Voltage ***		V <sub>ID</sub>  /2	-	2.4- V <sub>ID</sub> /2	<u>V</u>	
Common-mode Input voltage 2) 4) 6)	V IC	1.10111	-	V <sub>DD</sub> -0.8	<u> </u>	
	<del>                                     </del>	2.0	5.0	6.0	mA(rms)	I <sub>FL</sub> =2.5mA(rms)(Reference)
FL Input Current 7) 8) 9)	- FL	650	700	750	V(rms)	I <sub>FL</sub> =2.5IIIA(1113)(1.010)
FL Driving Voltage 7	$V_{FL}$	40	45	60	kHz	
FI Driving Frequency '	I <sub>F</sub>	1250	_	1600	V(rms)	∫0°C
FL Starting Voltage 7) 10)	V <sub>SFL</sub>	1230		1		

Note 1) The module should be always operated within these ranges. The "Typ," shows the recommendable value.

2) Recommended LVDS transmitter: SN75LVDS84/85, SN75LVDS83 (made by TEXAS INSTRUMENTS) LVDS receiver included in this module is SN75LVDS86 (made by TEXAS INSTRUMENTS). Refer to LVDS specifications.

3) This TFT-LCD module conforms to LVDS standard TIA/EIA-644.

4) Checked Pin Terminal: V<sub>DD</sub>, GND (0V)

5) Checked Pin Terminal : IN0-~CLK+, GND (0V)

6) Symbol: VID , VIC

Please refer to IC for LVDS (SN75LVDS86) specification for  $|V_{
m ID}|$  and  $V_{
m IC}$ 

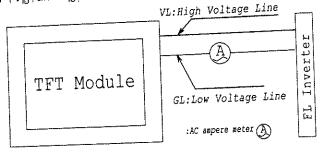
7) Checked Pin Terminal: V<sub>FLH</sub>-V<sub>FLL</sub>

8) If FL input current ( $I_{\rm FL}$ ) is higher than 6.0mArms, then FL lifetime becomes shorter.

9) Measuring Method of  $I_{\rm FL}$ .

10) Input FL starting voltage ( $V_{\rm SFL}$ ) should not be less than one second.

If it were less than one second, it may cause unstable operation of FL.



11) Please adjust LCD operating signal timing and FL driving frequency, to optimize the display quality. There is a possibility that flicker is observed by the interference of LCD operating signal timing and FL driving condition (especially driving frequency), even if the condition satisfies above recommended operating conditions and timing specifications shown in 2.4.4.

## 4. Electrical Characteristics

## 4.1 Test Conditions

Ambient Temperature : T, 25±5°C

65±20%(RH) Ambient Humidity : Ha

: V<sub>DD</sub> 3.3V Supply Voltage

: Refer typical value in "2.4.4 Timing Specifications". Input Signal

: I<sub>FL</sub> =2.5mA(rms) FL Input Current

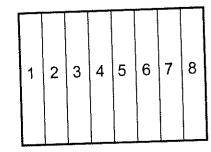
FL Driving Frequency :  $f_{FL} = 44 \text{kHz}$ : HIU-742A FL Inverter

## 4.2 Specifications

			- 4		I I I nit	Remark
Item	Symbol	Min.	Тур.1)	Max.	Unit	V Terminal Current
	I	-	310	500	mA	V <sub>DD</sub> terminal Current
Current Consumption	700	J		- li o rm		

Note 1) The Typical value of I<sub>DD</sub> is measured in the following pattern.

- 1. White
- 2. Yellow
- 3. Purple
- 4. Red
- 5. Light Blue
- 6. Green
- 7. Blue
- 8. Black



# 5. Optical Characteristics

## 5.1 Test Conditions

It is same as 4.1

The measuring method is shown in 11.

# 5.2 Optical Specifications 1)

5.2 Optical S	specim	<i></i>	_		Specificat	ions		Unit	Remark
	Ts	Symbol	Conditions		Min.	Тур	Max.	0	
em			CR>=10	φ = 180°	10				
/iewing Angle		$\theta$	CRZZIO	$\phi = 0^{\circ}$	20	<del>-</del>		0	
				$\phi = 90^{\circ}$	30			<u> </u>	
				$\phi = -90^{\circ}$	100	-			<del> </del>
- A Detio		CR	θ=0°, φ=0	<u>)~</u>	<del></del>	T	50	ms	┥
Contrast Ratio		ton	$\theta = 0^{\circ}, \phi = 0^{\circ}$	0°	\		50	ms	/ <sub>FL</sub> =5.0mA(rms)
Response Time		t <sub>OFF</sub>	-	Gray Scale	130	160	-	cd/m²	\\\ \text{\text{\text{Fr}} = 0.000\text{\tint{\tint{\text{\text{\text{\text{\text{\text{\text{\text{\text{\text{\tint{\text{\tint{\text{\text{\text{\text{\text{\text{\text{\text{\text{\tint{\tint{\tint{\text{\tint{\text{\text{\text{\text{\text{\text{\text{\text{\text{\text{\tint{\text{\text{\text{\text{\text{\text{\text{\tint{\tint{\text{\text{\text{\text{\text{\text{\text{\tint{\tint{\tint{\tint{\tint{\tint{\tint{\text{\text{\text{\text{\text{\text{\tint{\tint{\tint{\tint{\tint{\text{\text{\text{\text{\tint{\tint{\tint{\tint{\tint{\tint{\tint{\tint{\text{\text{\tin\tint{\text{\text{\text{\text{\text{\text{\tint{\tint{\tint{\tint{\text{\text{\text{\text{\text{\text{\text{\texit{\text{\tex{\tin\tin\tint{\tint{\tint{\tint{\tint{\tint{\tint{\text{\tin\tin}\tint{\tint{\tint{\tint{\tint{\tint{\tin\tint{\tint{\tin\tin\tin{\tin\tint{\tin\tin\tin\tint{\tinte\tin\tint{\tint{\tin\tin\tin\tint{\tint{\tin\tint{\tint{\tin\tin\tint{\tint{\tint{\tin}\tint
Luminance	··	L	i evel=L63 (	White)		<del> </del>		%	-
Luminance Un	iformity	LUNF	$\theta = 0^{\circ}, \phi = 0^{\circ}$	Gray Scale	55				
Luminano			Level=L63	(VVIIILE)	0.52	0.59			
Chromaticity	Red	X <sub>B</sub>	Gray Scale	-Vo	0.29	0.35			
Gillome		<i>y</i> <sub>R</sub>	θ=0°, φ		0.27	0.32			
	Green	X <sub>G</sub>	Ditto		0.48	0.54			
		У <sub>G</sub>			0.10	0.15			
	Blue	X <sub>B</sub>	Ditto		0.09				
		<u>У</u> в			0.27		0.38	}	
	White	x <sub>w</sub>	Ditto	•	0.29		0.41	<u> </u>	
		<i>y</i> w	Method".						

Note 1): Refer to "11. Measuring Method".

6.Quality

6.1 Inspection AQL

Total of Major Defects: AQL 0.65 % Total of Minor Defects : AQL 1.5 % Sampling Method: ANSI/ ASQC Z1.4

6.2 Test Conditions

1) Ambient Temperature

: 25±5°C

2) Ambient Humidity

: 65±20%(RH)

3) Illumination

: Approximately 500 lx under the fluorescent lamp

4) Viewing Distance

: Approximately 0.3m by the eyes of the inspector from the module

5) Inspection Angle

:  $\theta = 0^{\circ}$ ,  $\phi = 0^{\circ}$ 

## 6.3 Dimensional Outline

The products shall conform to the dimensions specified in 2.3.2.

Definition of Major and Minor defects are as follows.

	·	
A second second		Class
ltem		Major
Important Dimensions	Dimensional outline, Dimensional between	
	the mounting holes(hinge)	Minor
Others	Dimensions specified in this specifications	

## 6.4 Appearance Test

## 6.4.1 Test Conditions

1) Condition : Non-operating, operating (Pattern : L63 white raster) Same as 6.2

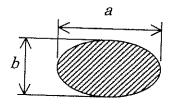
## 6.4.2 Specifications

Item		D					
PCB Appearance	Pattern peeling snapping of	Pattern peeling snapping, electrically short					
<u></u>	Repair portion on BCR is not	Repair portion on BOD					
oldering	Cold solder joint load	Repair portion on PCB is not covered by epoxy resign  Minor					
lezel, Frame,	Distinct stain, rust or scratch	Cold solder joint, lead move when pulled					
onnectors	Journal Stain, Tust of scratch			Major Minor			
lack and White				,,,,,,,			
pots/Lines1)2)				Minor			
	Line Width(mm)	Length(m	m) Acceptable				
	<i>W</i> ≦0.05	<i>L</i> ≦3					
	0.05< <i>W</i> ≦0.07	0.05 < W≤0.07 0.07 < W≤0.10					
<u> </u>	0.07 <w≦0.10< td=""><td></td></w≦0.10<>						
	0.10 <w< td=""><td></td><td>n≦2 2)</td><td></td></w<>		n≦2 2)				
	Average diameter(	mm)	Accontable				
	<i>D</i> ≦0.2		Acceptable count/side				
	0.2 <d≦0.3< td=""><td></td><td>neglect</td><td></td></d≦0.3<>		neglect				
4.	0.3< <i>D</i> ≦0.5	<del></del>	<u></u>				
	0.5 <d< td=""><td></td><td><i>n</i>≦2</td><td></td></d<>		<i>n</i> ≦2				
and the property			0				

Note 1) Inspection area should be within active area.

Note 2) Dusts which are bigger not less than 0.10mm (0.1 < W) shall be judged by "Average Diameter".

Average Diameter D = (a+b)/2 (mm)



### 6.5 Display Quality

#### 6.5.1 Test Conditions

1) Inspection Area : Within active area

2) Driving Condition: Same as test conditions shown in 4.1 and 6.2

3) Test Pattern : White display pattern (gray scale level L63) and black display pattern (gray scale level L0)

#### 6.5.2 Specifications 4)

Item	Description / Specifications		Class
Function	No display, Malfunction		Major
Display Quality 1)	Missing line		Мајог
	Missing Sub-Pixels		Major
** ;	1) Bright defects <sup>2) 3) 4)</sup>	:10pcs. max.	
	2) Dark defects <sup>2) 4)</sup>	: 10pcs max.	
	3) Total defects	: 15pcs max.	
Two.	4) Total number of sub-pixel brig	ght defect within	
	10mm in diameter	:4pcs max.	
er i de er Notae	5) Bright defect conjunction	:2sets max.	
	(2sub-pixels)		
	6) Dark defect conjunction	:2sets max.	
	(2sub-pixels)		
e"	Inconspicuous flicker, crosstalk, I	Newton's ring and other defects :	-
	neglect		
Black and White	Inconspicuous defects : neglect		-
Spots/lines			
Backlight	Missing (Non-operating)		Major

Note 1) Defects of both color filter and black matrix are counted as bright or dark defects.

Inspection area should be within the active area.

Note 2) Bright defect means a bright spot(sub-pixel) on the display pattern of gray scale L0.

Dark defect means a dark spot(sub-pixel) on the display pattern of gray scale L63.

Note 3) Bright defect which can not be found by using 5%ND-Filter shall not be counted as a defect.

Note 4): Testing is conducted only on RED, GREEN, BLUE, WHITE of gray scale L63, and BLACK of gray scale L0.

	4 <u>00 50 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 8 </u>			
RED	GREEN	BLUE	WHITE	# 3 lb \\e4\r*
L63	L63	L63	L63	

## 6.6 Reliability Test

## 6.6.1 Test Conditions

- 1) The module should be driven and inspected under normal test conditions.
- 2) The module should not have condensation of water (moisture) on the module.
- 3) The module should be inspected after two or more hours storage in normal conditions (15 35°C,45 65%(RH)).
- 4) A module shall be used only for one test.

## 6.6.2 Specifications

The module shall have no failure in the following reliability test items.

		Result
Test Item	Test Conditions	3p/3p OK
High Temperature Operation (1)	50°C 192 h	3p/3p OK
High Temperature Storage 2)	60°C 192 h	3p/3p OK
High Temperature and	50°C 80% 192 h	1 -1 -1
High Humidity operation 1)		3p/3p OK
Low Temperature Operation 1)	0°C 192 h	3p/3p OK
Low Temperature Storage 2)	-20°C 192 h	3p/3p OK
Temperature Shock 2)	-20°C ⇔ 60°C	
lemperatoro con o	0.5h 0.5h	
	50 cycles	3p/3p OK
2)	10 - 200 - 10Hz sweep/cycle,	Зр/эр ОТС
Mechanical Vibration 2)	1.5×9.8m/s² constant,	
	X.Y.Z each direction, 0.5h each	0 - 10 - OV
2)	50×9.8m/s², 20ms,	3p/3p OK
Mechanical Shock	±X, ±Y, ±Z each direction,	
7.6	one time each	

Note 1) Operating

Note 2) Non-Operating

Definitions of failure for judgment shall be as follows:

- 1) Function of the module should be maintained.
- 2) Current consumption should be smaller than the specified value.
- 3) Appearance and display quality should not have distinguished degradation.
- 4) Luminance should be larger than 50% of the minimum value specified in 5.2.

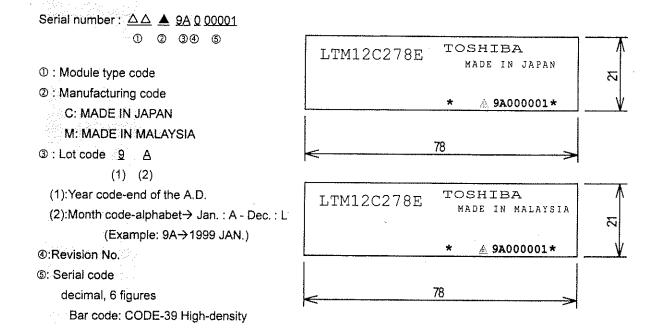
Date:1999-12-13

Date:

New No.NR-LTM12C278E-13 Old No.

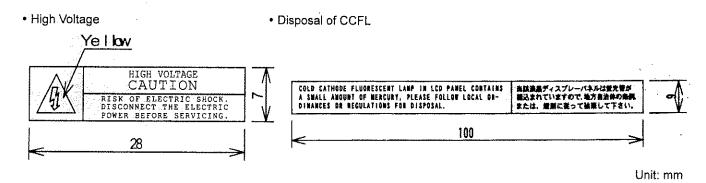
#### 6.7 Labels

#### (1) Product Label

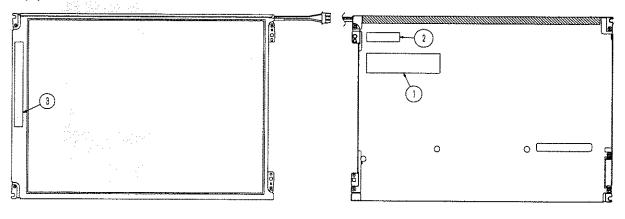


#### (2) Caution Labels

•There are two type of label according to country of origin.







① : Product Label

② : High Voltage

③ : Disposal of CCFL

Date: 1999-12-13 Date: - - New No.NR-LTM12C278E-13 Old No.

## 7. Lifetime

## 7.1 Module (except lamp)

MTTF (Mean Time To Failure): 50,000 h

(This value is not assurance time but inference value by following conditions.)

Conditions : Ambient temperature : 25±5°C (No wind)

: 65%(RH) Ambient humidity

### 7.2 Lamp

## 7.2.1 Test Conditions

Ambient temperature

: 25±5°C (No wind)

Lamp current

: 6.0mA(rms)

Lighting condition

: continuous lighting

Driving frequency

: 44kHz

## 7.2.2 Specifications

MTBF: 10,000 h

Definitions of failure for judgment shall be as follows.

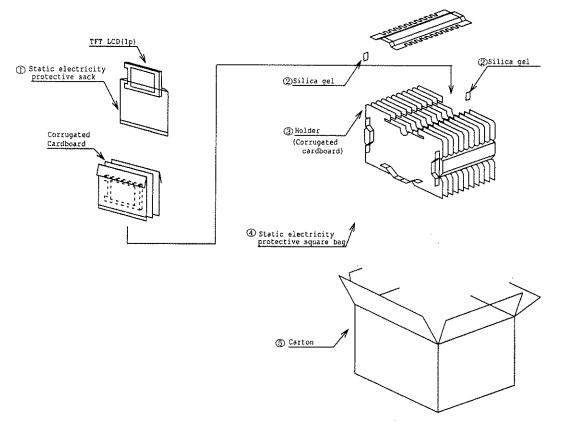
- 1) LCD luminance becomes half of the minimum value specified in 5.2.
- 2) Lamp doesn't light normally.

New No.NR-LTM12C278E-13 Date:1999-12-13

### 8. Packaging

- 8.1 Carton (internal package)
  - (1) Packaging Form
    Corrugated cardboard box

### (2) Packaging Method 1)2)

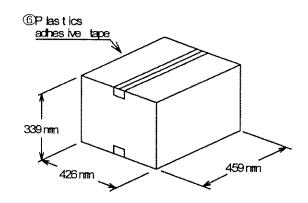


Note 1): Total weight: (Approx.) 12 kg

Note 2): Acceptable number of carton piling: 12 sets Acceptable number of palette piling: 4 sets

### (3) Packaging Material

Number	Quantity	Description
0	20p	Static electricity
		Protective sack
0	6p	Silica gel(50g×6p)
3	1set	Holder
<b>④</b>	1p	Static electric
		Protective square bag
(3)	1p	Corrugated card box
6	<u> </u>	Plastics adhesive tape



Data