



ELECTRONICS

Product Information

ISSUE DATE : June.28, 2004

MODEL NO. : LTA120W1-T01

Note : This product information is subject to change without notice.

For current product information or updates please contact the R&D department.

PREPARED BY : AMLCD Mobile Display Development Team

Samsung Electronics Co . , LTD.



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GENERAL DESCRIPTION

DESCRIPTION

LTA120W1-T01 is a color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching devices. This model is composed of a TFT LCD panel, a driver circuit and a back-light system. The resolution of a 10.0 " contains 800 x 480 pixels and can display up to 262,144colors. 6 o'clock direction is the optimum viewing angle.

FEATURES

- Thin and light weight
- High contrast ratio, Wide view angle, High-speed response
- Wide VGA (800x480 pixels) resolution
- Low power consumption
- Single CCFL(Cold Cathode Fluorescent Light)

APPLICATIONS

- Notebook PC and desktop monitors
- Display terminals for AV application products
- Monitors for Industrial machine

GENERAL INFORMATION

ITEM	SPECIFICATION	UNIT	NOTE
Display area	261.6(H) x 156.96(V) (12.0" diagonal)	mm	A/R=15:9
Driver element	a-Si TFT active matrix		
Display colors	262,144		
Number of pixel	800(R,G,B) x 480 (Wide VGA)	pixel	
Pixel arrangement	RGB vertical stripe		
Pixel pitch	0.3270(H) x 0.3270(V) (TYP.)	mm	
Display Mode	Normally white		
Surface treatment	HAZE (25), HARD-COATING 3H		

MECHANICAL INFORMATION

ITEM		MIN.	TYP.	MAX.	NOTE
Module size	Horizontal (H)	282.5	283.0	283.5	
	Vertical (V)	172.5	173.0	173.5	
	Depth (D)		6.3	6.6	
Weight		-	-	520	

1. ABSOLUTE MAXIMUM RATINGS

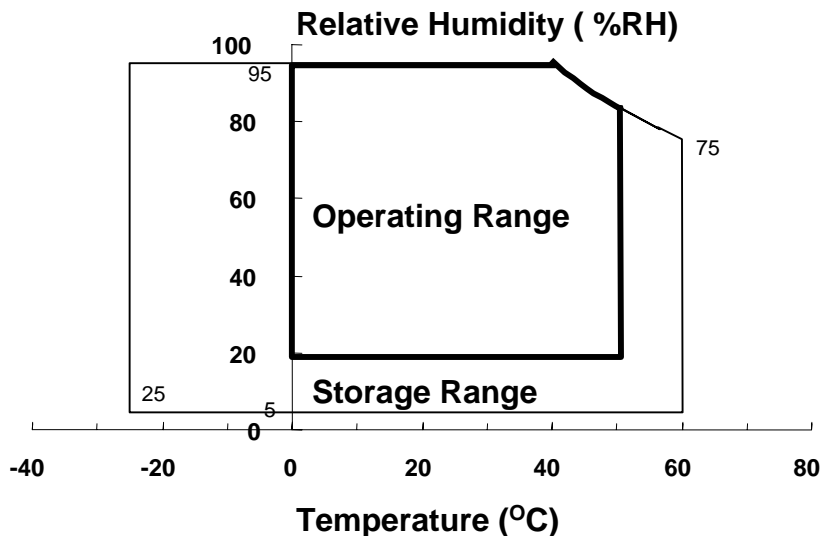
1.1 ABSOLUTE RATINGS OF ENVIRONMENT

ITEM	SYMBOL	MIN.	MAX.	UNIT	NOTE
Storage temperature	T_{STG}	-25	60		(1)
Operating temperature (Temperature of glass surface)	T_{OPR}	0	50		(1) Lamp Current =6.0mA

Note (1) Temperature and relative humidity range are shown in the figure below.

95 % RH Max. ($40\text{ }^{\circ}\text{C} \geq T_a$)

Maximum wet - bulb temperature at $39\text{ }^{\circ}\text{C}$ or less. ($T_a > 40\text{ }^{\circ}\text{C}$) No condensation.



1.2 ELECTRICAL ABSOLUTE RATINGS

(1) TFT LCD MODULE

ITEM	SYMBOL	MIN.	MAX.	UNIT	NOTE
Power Supply Voltage	DVDD	- 0.3	7.0	V	
Power Supply Voltage	AVDD	- 0.3	7.0	V	
Gate IC Voltage (+)	VGG	- 0.3	VEE+45	V	
Gate IC Voltage(-)	VEE	DVDD - 42	- 0.3	V	
Gate IC Voltage	VGG - VEE	-	42	V	
Logic Input Voltage	VIND	- 0.3	DVDD+0.3	V	
Analog Input Voltage	VINA	- 0.3	AVDD+0.3	V	GAM1 ~ 11

(2) BACK-LIGHT UNIT

Ta = 25 ± 2 °C

ITEM	SYMBOL	MIN.	TYP	MAX.	UNIT.	NOTE
Lamp current	IL	3.0	-	7.0	mArms	
Lamp frequency	fL	40		60	KHz	(1)
		40		80		(2)

NOTE (1) Electrical-optical characteristics is ± 10 % can be guaranteed.

(2) Life time and reliability can be guaranteed.

2. OPTICAL CHARACTERISTICS

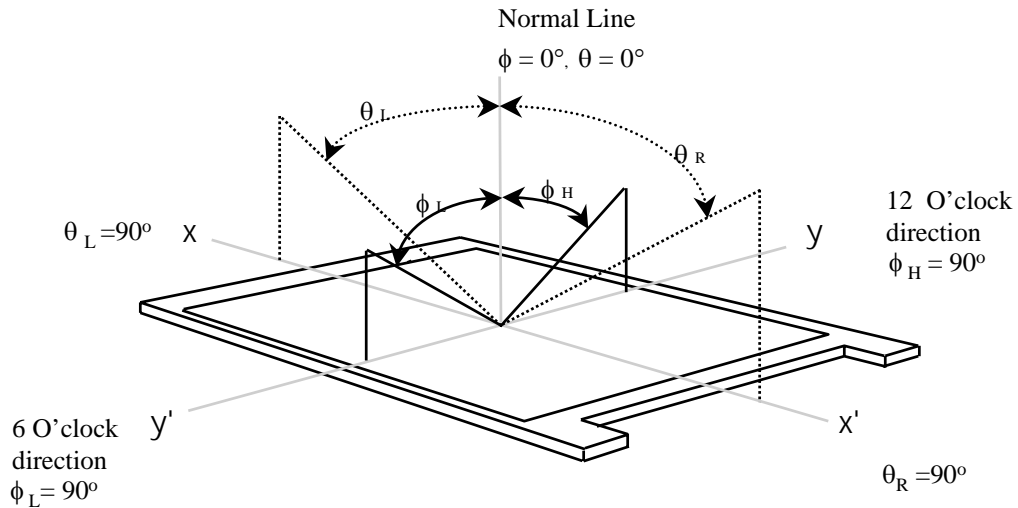
The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state with the methods shown in Note (5).

Measuring equipment : TOPCON BM-5A

* $T_a = 25 \pm 2^\circ\text{C}$, $f_v = 60\text{Hz}$, $f_{\text{CLK}} = 32\text{MHz}$, $I_L = 6.0\text{mA}$

ITEM		SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE	
Contrast Ratio (5 Points)		CR	$\phi = 0,$ $\theta = 0$ Normal Viewing Angle	-	300	-		(1), (2)	
Response Time at T_a	Rising	T_R		-	10	15	msec	(1), (3)	
	Falling	T_F		-	20	25			
Average Luminance of White (5 Points)		$Y_{L,AVE}$			-	350	-	cd/m^2	(4) 6.0mA _{rms}
Color Chromaticity (CIE 1931)	Red	R_X			0.566	0.596	0.626	Measured by PR 650	(1), (5)
		R_Y			0.311	0.341	0.371		
	Green	G_X			0.286	0.316	0.346		
		G_Y			0.497	0.527	0.557		
	Blue	B_X			0.123	0.153	0.183		
		B_Y			0.109	0.139	0.169		
	White	W_X		0.287	0.317	0.347			
		W_Y		0.302	0.332	0.362			
Viewing Angle	Hor.	θ_L	CR(at center point) ≥ 10	55	60	-	Degrees	(1),(5)	
		θ_R		55	60	-			
	Ver.	ϕ_H		40	45	-			
		ϕ_L		40	45	-			

Note 1) Definition of Viewing Angle : Viewing angle range($10 \leq C/R$)

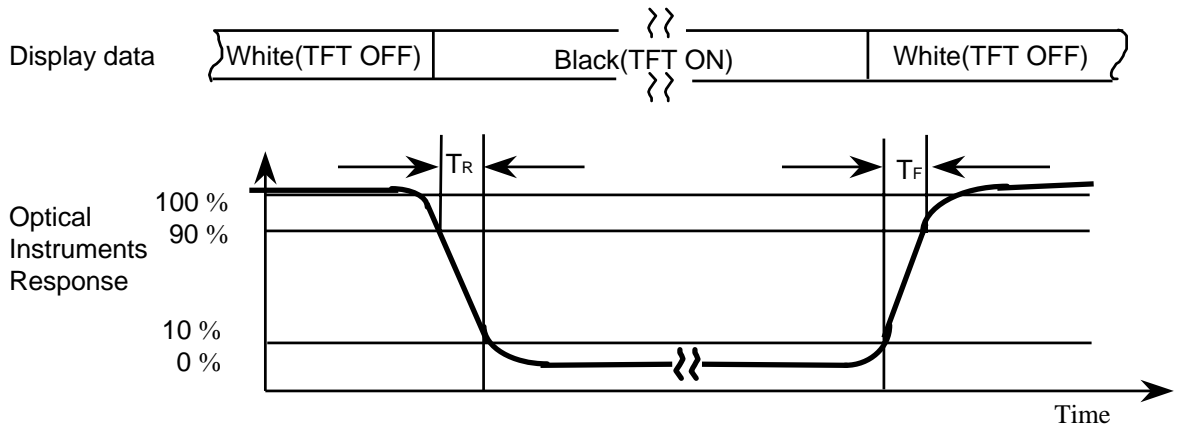


Note 2) Definition of Contrast Ratio (CR) : Ratio of gray max (Gmax) ,gray min (Gmin) at 5 points(1, 3, 5, 7, 9)

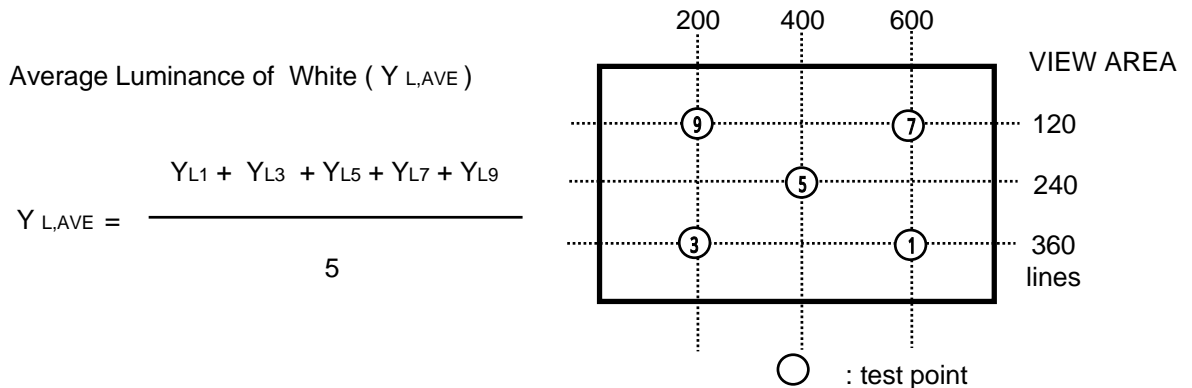
$$CR = \frac{CR(1) + CR(3) + CR(5) + CR(7) + CR(9)}{5}$$

POINTS : (1) , (3) , (5) , (7) , (9) at FIGURE OF NOTE 4)

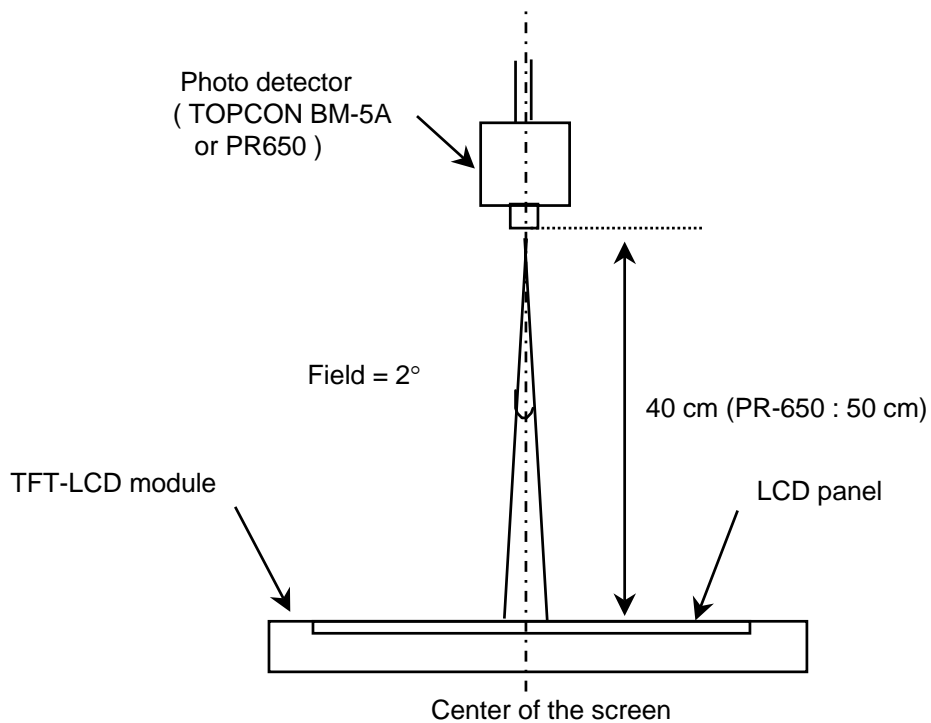
Note 3) Definition of Response time :



Note 4) Definition of Average Luminance of White : measure the luminance of white at 5 points.



Note 5) After stabilizing and leaving the panel alone at a given temperature for 30 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. 30 minutes after lighting the back-light. This should be measured in the center of screen.
Lamp current : 6.0 mA
Environment condition : $T_a = 25 \pm 2 \text{ }^\circ\text{C}$



Optical characteristics measurement setup

3. ELECTRICAL CHARACTERISTICS

3.1 TFT LCD MODULE

Ta=25 ± 2

ITEM		SYMBOL	MIN	TYP	MAX	UNIT	NOTE
Voltage of Power Supply		DVDD	3.0	3.3	3.6	V	
		AVDD	-	5.0	-	V	
		VGG	-	20	-	V	
		VEE	DC	-	- 9.5	-	V
AC	-		5	-	Vp-p		
Input Voltage for Logic signals	High	VIH	0.7 × DVDD	-	DVDD	V	
	Low	VIL	0	-	0.3 × DVDD	V	
Input Reference Voltage		VREF	0.1	-	AVDD - 0.1	V	(1)
Common Voltage	AC	VCOM	-	5.1	-	Vp-p	
	DC		-	2.2	-	V	
Current of Power Supply (2)		IDV	-	5	10	mA	DVDD=3.3V
		IAV	-	30	50	mA	AVDD=5.0V
		IGG	-	0.2	1.0	mA	VGG=20V
		IEE	-	0.2	1.0	mA	VEE= - 12V

- Note (1) Reference voltage should be adjusted depend on VCOM inversion.
Gamma 1 is the opposite phase with VCOM but Gamma11 is same with it .
- (2) The Display pattern is white pattern.

3.2 BACKLIGHT UNIT

The back-light system is an edge - lighting type with a single CCFT (Cold Cathode Fluorescent Tube). The characteristics of a single lamp are shown in the following tables.

INVERTER : SEM SIC-130T

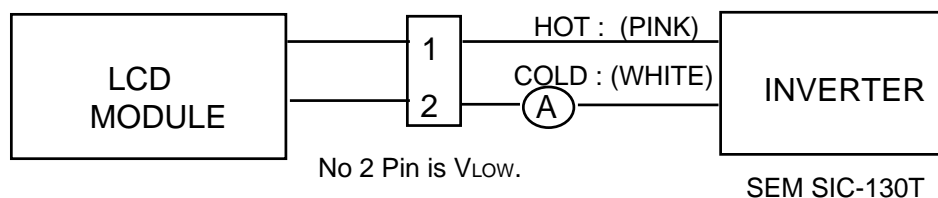
Ta = 25 ± 2°C

ITEM	SYMB	MIN	TYP	MAX	UNIT	NOTE
Lamp Current	I _L	3.0	6.0	7.0	mArms	(1)
Lamp Voltage	V _L	-	920	-	V _{rms}	(1)
Lamp Frequency	f _L	40	-	60	KHz	(2)
Power Consumption	P _L	-	5.52	-	W	(3)
Operating Life Time	H _r	10,000	-	-	Hour	(4)
Startup Voltage	V _s	-	-	1560 (25°C)	V _{rms}	(5)
				2030 (0°C)		

Note) **The waveform of the inverter output voltage must be area symmetric and the design of the inverter must have specifications for the modularized lamp.**

The performance of the back-light, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. So all the parameters of an inverter should be carefully designed so as not to produce too much leakage current from high-voltage output of the inverter. When you design or order the inverter, please make sure that a poor lighting caused by the mismatch of the back-light and the inverter(miss lighting, flicker, etc.) never occur. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

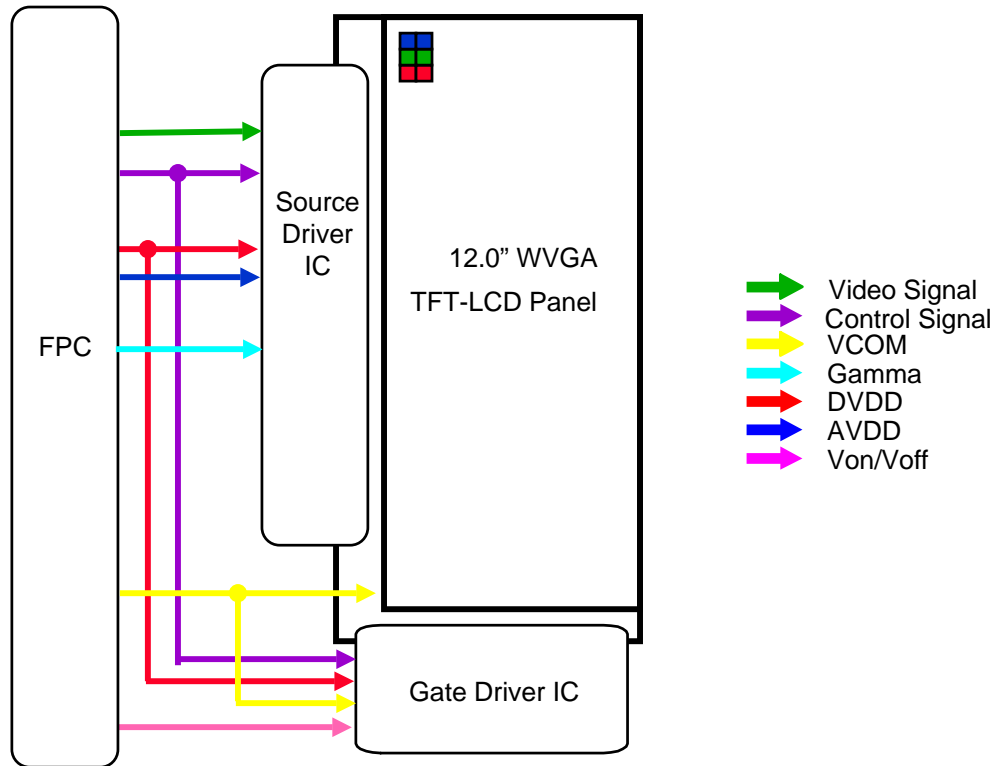
Note (1) Lamp current is measured with a high frequency current meter as shown below.



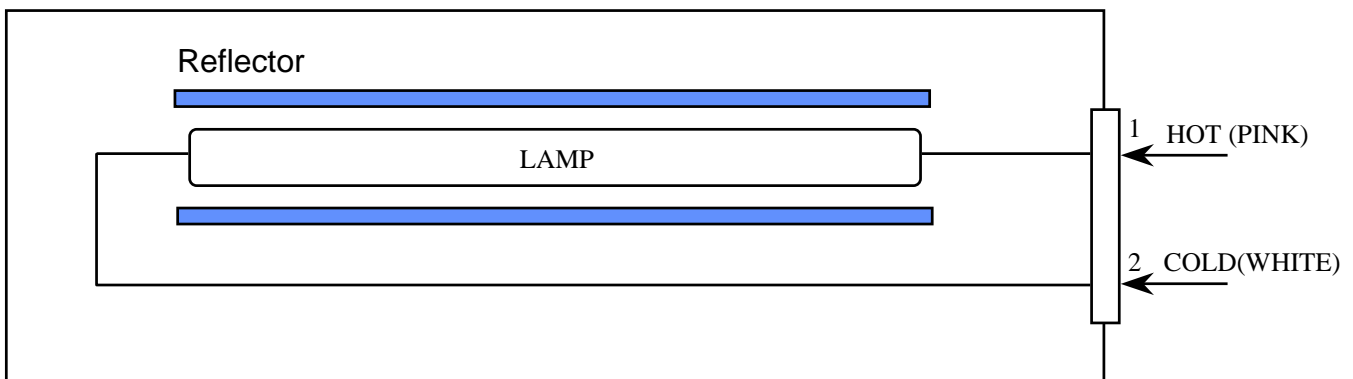
- (2) Lamp frequency may produce interference with horizontal synchronous frequency and this may cause line flow on the display. Therefore lamp frequency should be detached from the horizontal synchronous frequency and its harmonics as far as possible in order to avoid interference.
- (3) refer to $I_L \times V_L$ to calculate.
- (4) Life time (Hr) of a lamp can be defined as the time in which it continues to operate under the condition Ta = 25 ± 2°C and I_L = 6.0 mArms until one of the following event occurs.
1. When the brightness becomes 50% or lower than it's original.
 2. When the Effective ignition length becomes 80% or lower than it's original value.
(Effective ignition length is defined as an area that has less than 70% brightness compared to the brightness in the center point.)
- (5) The voltage above this value should be applied to the lamp for more than 1 second to startup. Otherwise the lamp may not be turned on.

4. BLOCK DIAGRAM

4.1 TFT LCD MODULE



4.2 BACKLIGHT UNIT



Note) The output of the inverter may change according to the material of the reflector.

5. INPUT TERMINAL PIN ASSIGNMENT

5.1. Interface Signal & Power (TTL, Connector : UJU FPC 30 pin)

PIN NO	SYMBOL	FUNCTION	PIN NO	SYMBOL	FUNCTION
1	N.C	No Connection	1	GND	Power Ground
2	DIO2	Down Shift Vertical Signal (STV_L)	2	D15	GREEN DATA(MSB)
3	OE	Output Enable	3	D14	GREEN DATA
4	CPV	Vertical Clock Pulse	4	D13	GREEN DATA
5	DIO1	Up Shift Vertical Signal(STV_H)	5	D12	GREEN DATA
6	GND	Power Ground	6	D11	GREEN DATA
7	N.C	No Connection	7	D10	GREEN DATA(LSB)
8	VDD	Power Supply [+3.3V]	8	PLSR	Right Shift Horizontal Signal(STH_R)
9	N.C	No Connection	9	INV	POWER GND
10	VOFF	Gate Off Voltage	10	GND	Power Ground
11	N.C	No Connection	11	CLK	Horizontal Clock Pulse
12	VON	Gate On Voltage	12	DVDD	Power Supply [+3.3V]
13	N.C	No Connection	13	PRSL	Left Shift Horizontal Signal(STH_L)
14	U/D	DIO1/DIO2 Shift Direction Select	14	TP	Load Output Signal
15	VCOM	Common Voltage	15	D25	BLUE DATA(MSB)
16	GND	Power Ground	16	D24	BLUE DATA
17	AVDD	Power Supply [+5.0V]	17	D23	BLUE DATA
18	GAM10	Gamma Voltage 10	18	D22	BLUE DATA
19	GAM8	Gamma Voltage 8	19	D21	BLUE DATA
20	GAM6	Gamma Voltage 6	20	D20	BLUE DATA(LSB)
21	GAM4	Gamma Voltage 4	21	R/L	PLSR/PRSL Shift Direction Select
22	GAM2	Gamma Voltage 2	22	GAM1	Gamma Voltage 1
23	GND	Power Ground	23	GAM3	Gamma Voltage 3
24	D05	RED DATA(MSB)	24	GAM5	Gamma Voltage 5
25	D04	RED DATA	25	GAM7	Gamma Voltage 7
26	D03	RED DATA	26	GAM9	Gamma Voltage 9
27	D02	RED DATA	27	GAM11	Gamma Voltage 11
28	D01	RED DATA	28	AVDD	Power Supply [+5.0V]
29	D00	RED DATA(LSB)	29	GND	Power Ground
30	GND	Power Ground	30	VCOM	Common Voltage

5.2 BACK LIGHT UNIT

Connector : JST BHSR - 02VS -1
Mating Connector : JST SM02B-BHSS-1

Pin NO.	Symbol	Color	Function
1	HOT	PINK	High Voltage
2	COLD	WHITE	Low Voltage

5.3 Input Signal, Basic Display Colors and Gray Scale of Each Colors

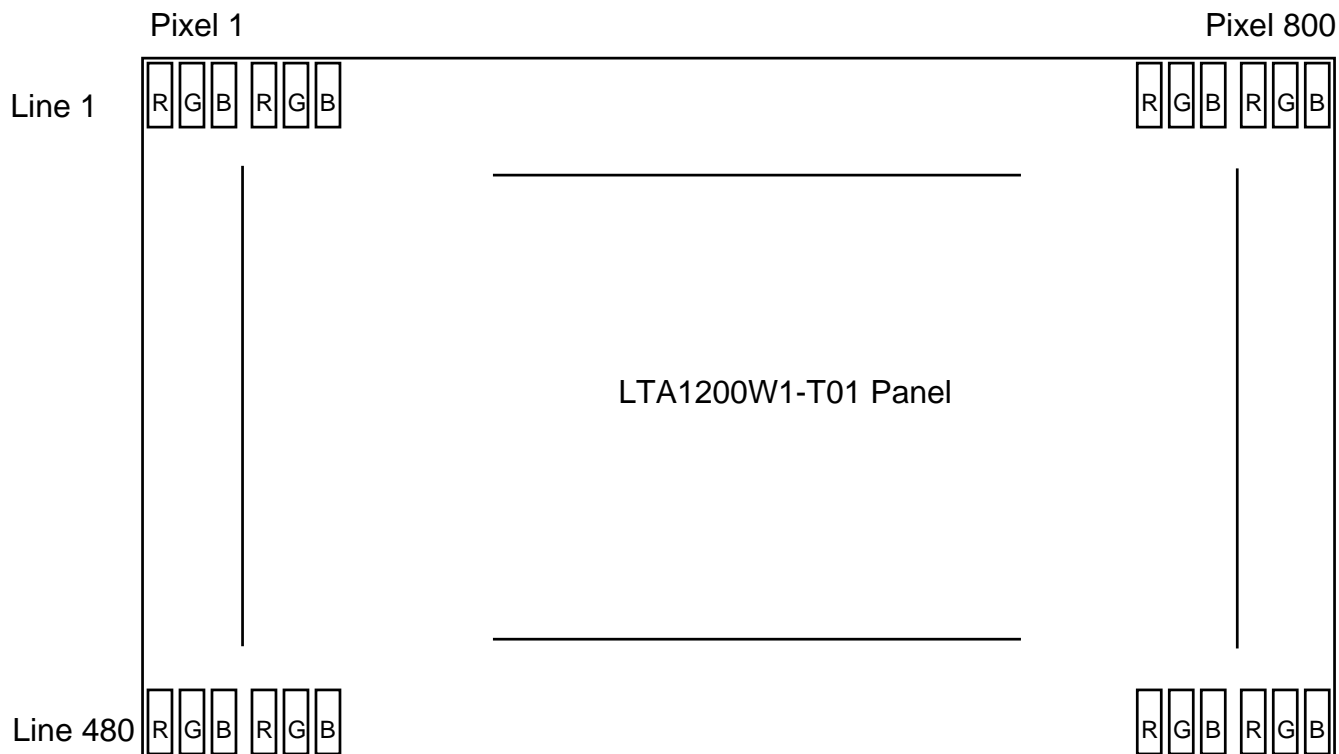
COLOR	DISPLAY	DATA SIGNAL																GRAY SCALE LEVEL	
		RED					GREEN					BLUE							
		R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3		B4
BASIC COLOR	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	-
	BLUE	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	-
	GREEN	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	-
	CYAN	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	-
	RED	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	-
	MAGENTA	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	-
	YELLOW	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	-
	WHITE	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	-
GRAY SCALE OF RED	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R0	
	DARK	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R1	
		0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	R2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	R3~R60	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:			
	LIGHT	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	R61	
		0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	R62	
		RED	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	R63
GRAY SCALE OF GREEN	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	G0	
	DARK	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	G1	
		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	G2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	G3~G60	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:			
	LIGHT	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	G61	
		0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	G62	
		GREEN	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	G63
GRAY SCALE OF BLUE	BLACK	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	B0	
	DARK	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	B1	
		0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	B2	
		:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	B3~B60	
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:			
	LIGHT	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	B61	
		0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	B62	
		BLUE	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	B63

Note

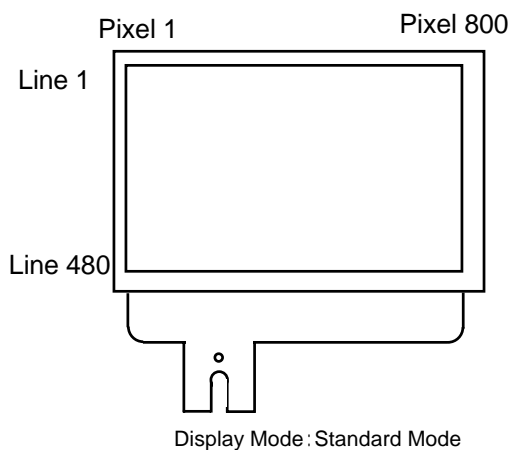
(1) Definition of Gray : Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

(2) Input Signal : 0 = Low level voltage, 1 = High level voltage

5.4 PIXEL FORMAT



5.5 Display Direction Signal



Display Mode	R L	UD
Standard	H	L
R L Reversal	L	L
UD Reversal	H	H
RL/UD Reversal	L	H

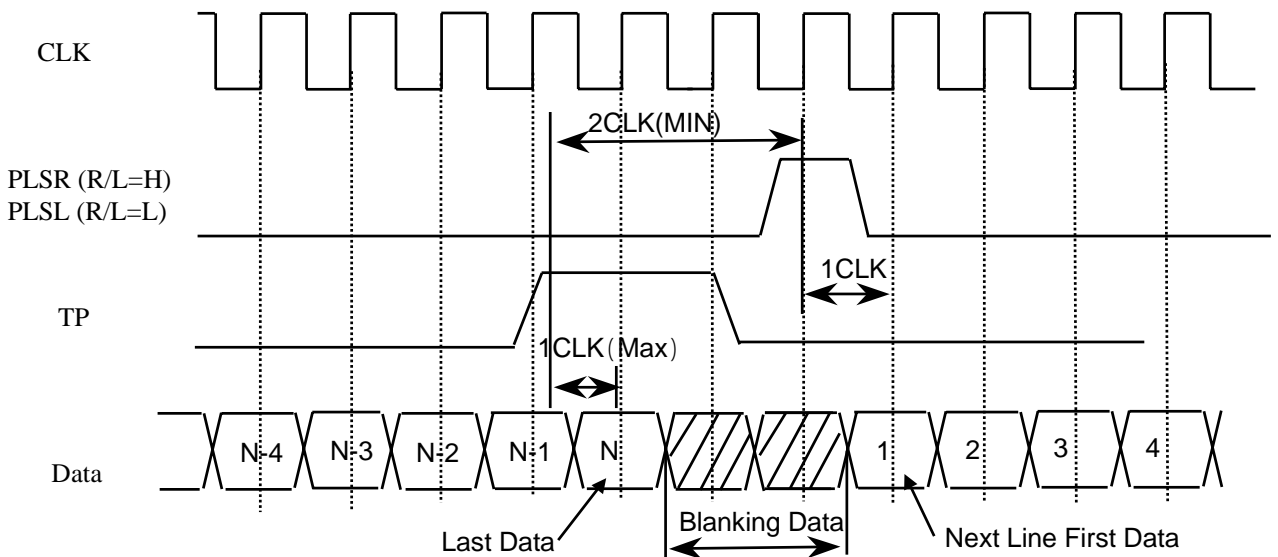
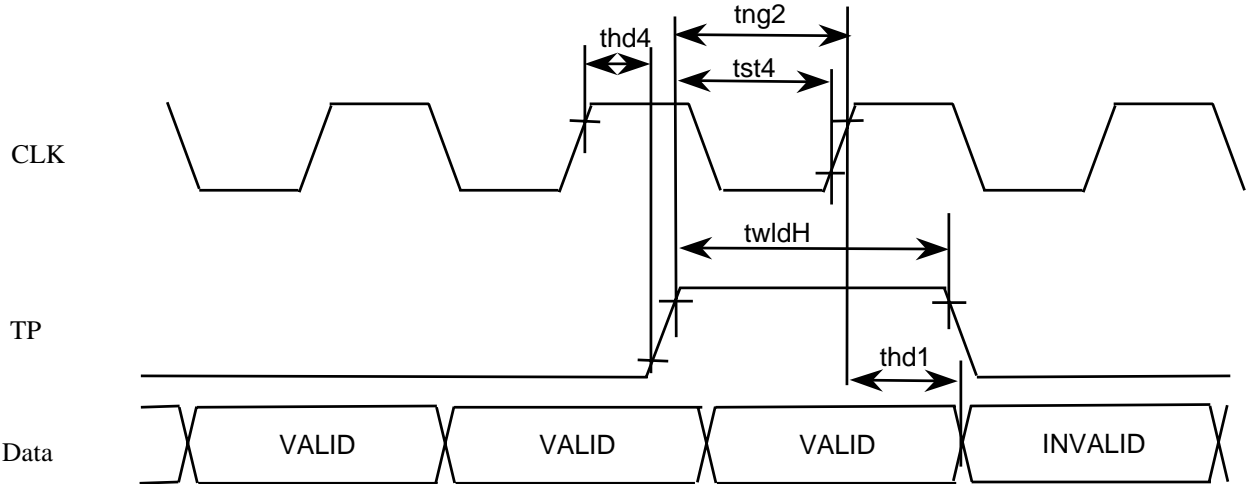
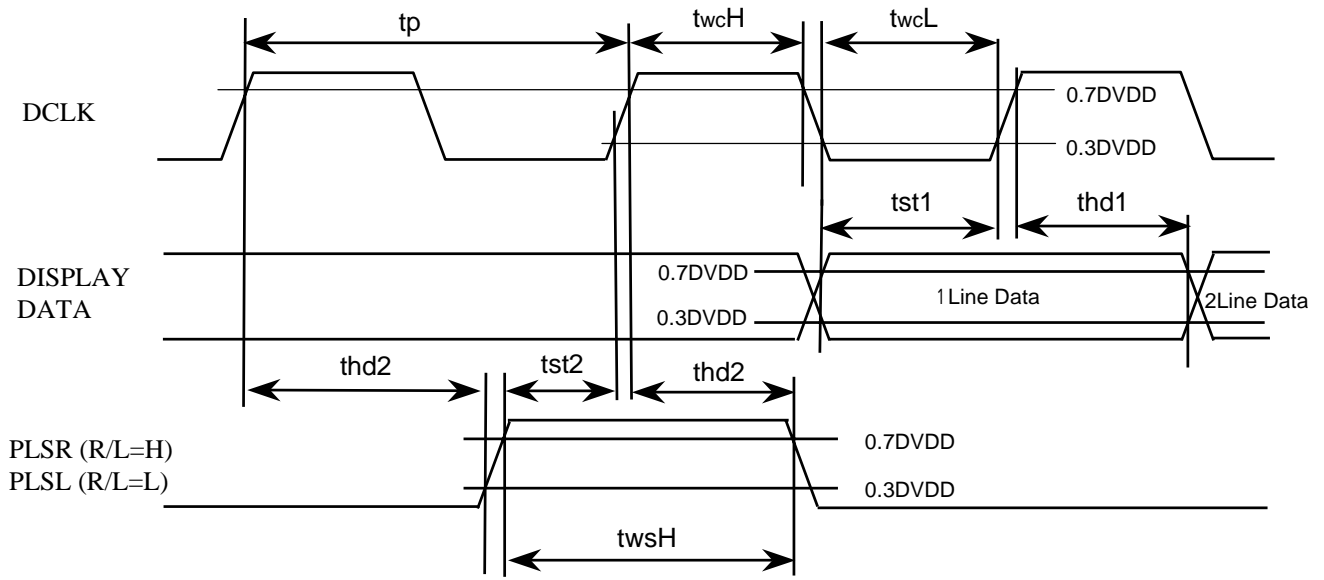
6. INTERFACE TIMING

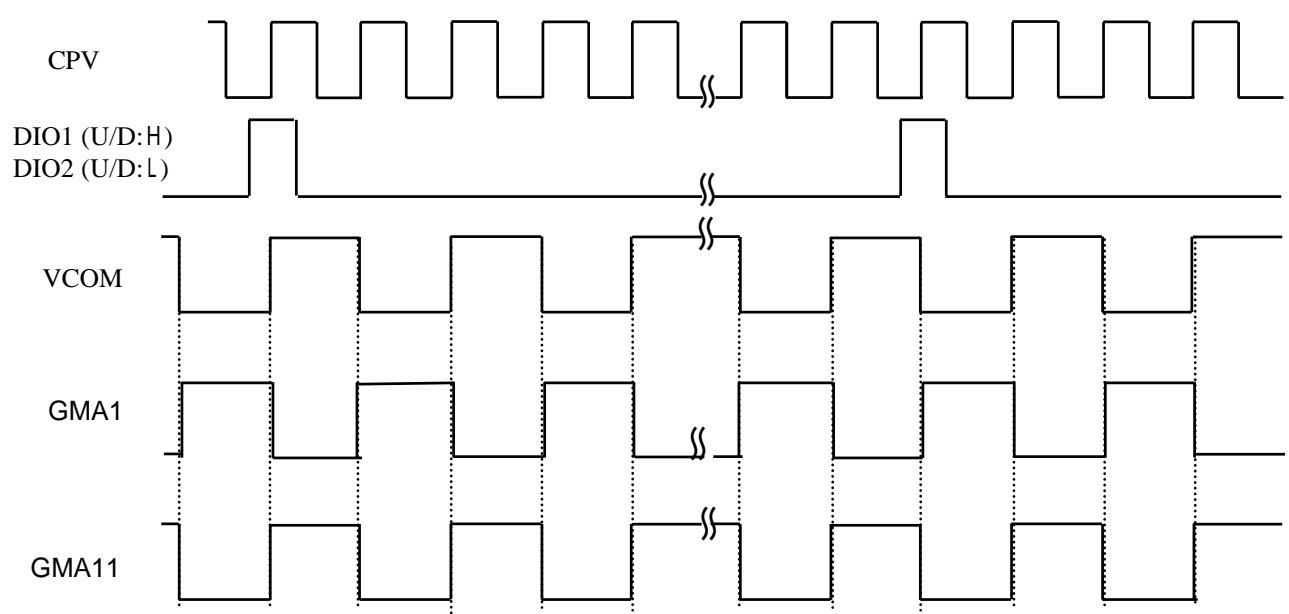
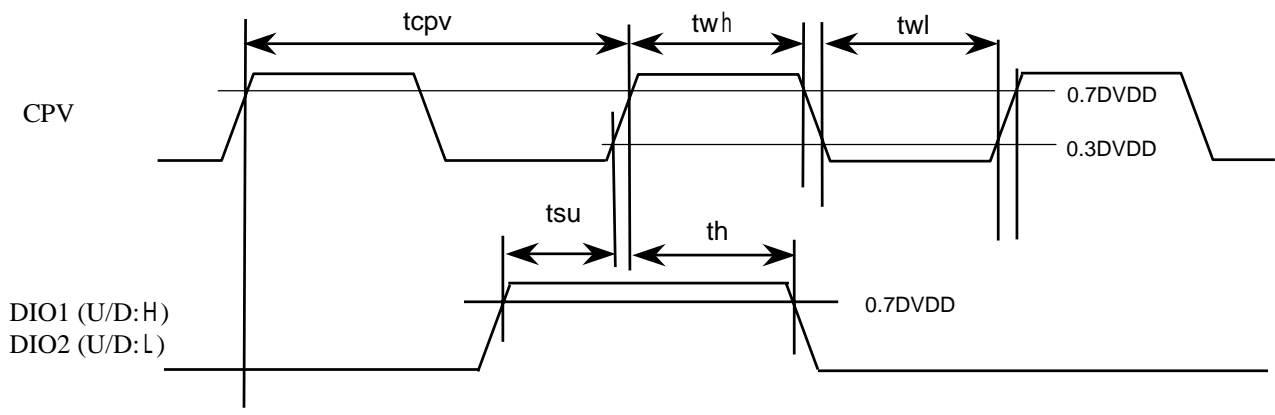
6.1 Timing Parameters

(Ta=25)

Item		Symbol	MIN	TYP	MAX	Unit
Source	Clock frequency	tp		33.2	40	MHz
	Clock High Time	twcH	5			ns
	Clock Low Time	twcL	5			ns
	Shift Signal	PLSR / PRSL		31.5		kHz
	Data Setup Time	tst1	5			ns
	Data Hold Time	thd1	1			ns
	Shift Signal Setup Time	tst2	5			ns
	Shift Signal Hold Time	thd2	1			ns
	Shift Signal H Time	twsH	1			CLK
	TP-CLK Setup Time	tst4	5			ns
	TP-CLK Hold Time	thd4	1			ns
	Last Data Timing	tng2			1	CLK
	TP Signal H Time	twldH	2			CLK
Gate	Shift Signal	DIO1 / DIO2		60		Hz
	CPV Cycle	tcpv	2			μs
	CPV High Time	twH	300			ns
	CPV Low Time	twL	250			ns
	DIO Setup Time	tsu	500			ns
	DIO Hold Time	th	500			ns

6.2 Timing diagrams of interface signal

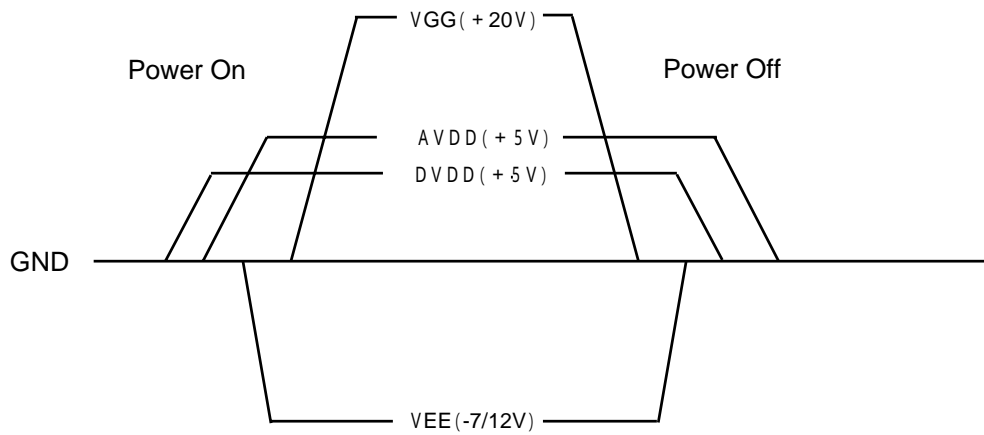




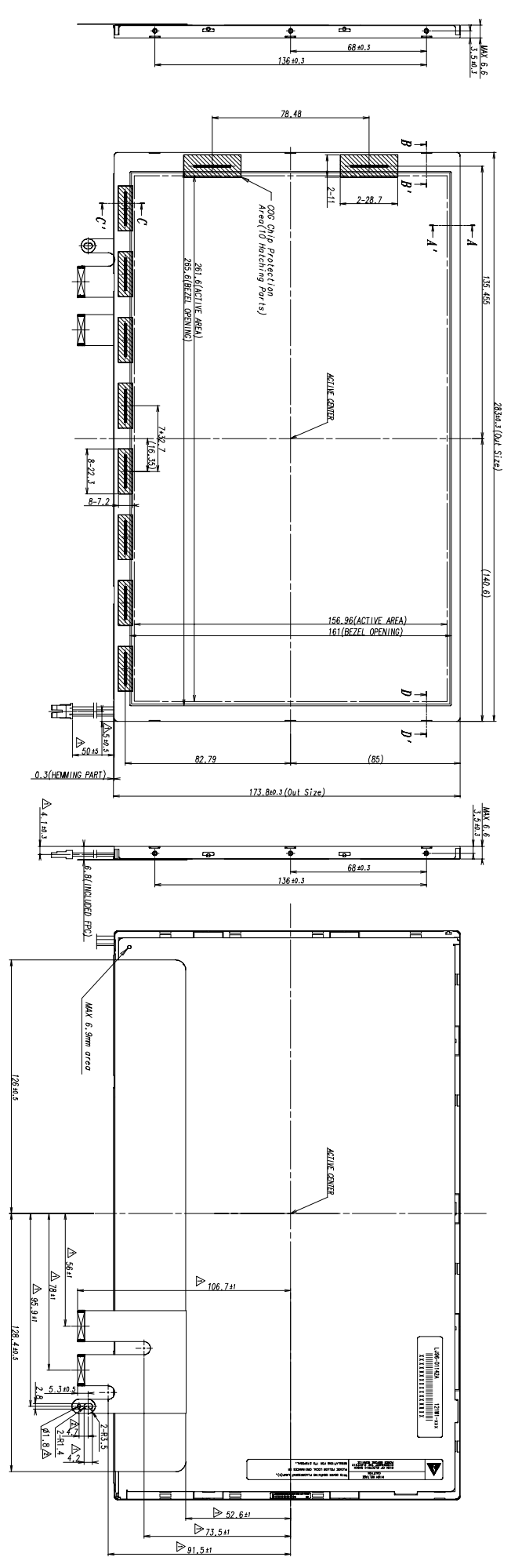
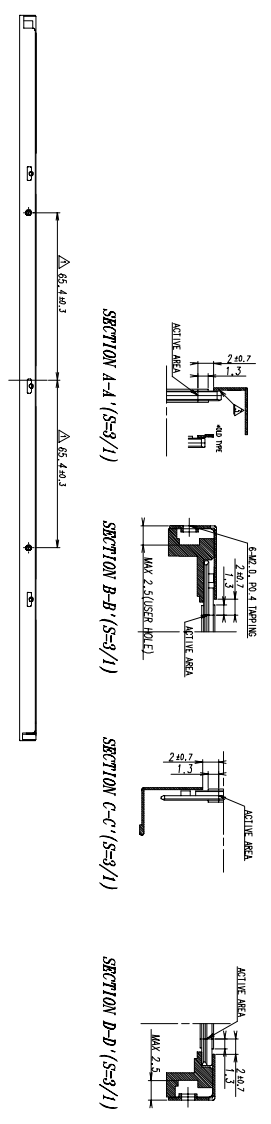
6.3 Power ON/OFF Sequence

- To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.

-Power On : DVDD AVDD VEE VGG Data
-Power OFF : Data VGG VEE AVDD DVDD



FILE NO.	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P
NO.	PART NAME		CODE NO.	SPECIFICATION		QTY	REVISION	DATE	BY	CHECKED	DATE	BY	REVISION	DATE	BY	REVISION



FINAL

- * NOTE**
1. PPG MATING CONNECTOR TO BE SPECIFIED AS BELOW.
- MAKER : UUU ELECTRONICS
- PART NO : 29244-5003R
 2. COCT CONNECTOR FOR BACKLIGHT TO BE SPECIFIED AS BELOW.
- MAKER : JSI
- PART NO : BRSP-02YS-1
 3. ALLOWED DEPTH OF USERHOLE SCREW INSERTION IS 2.5mm MAX.

REV	NO	DATE	DESCRIPTION OF REVISION	DESIGN BY	CHK'D BY
1	01	2023/07/01	INITIAL DESIGN	H.S.M/W	
2	02	2023/07/01	REVISION TO SOLIDWORKS	H.S.M/W	
3	03	2023/07/01	REVISION TO ADD DIMENSIONS	H.S.M/W	
4	04	2023/07/01	REVISION TO ADD MATERIAL	H.S.M/W	
5	05	2023/07/01	REVISION TO ADD FINISH	H.S.M/W	
6	06	2023/07/01	REVISION TO ADD TOLERANCES	H.S.M/W	
7	07	2023/07/01	REVISION TO ADD PART NAME	H.S.M/W	
8	08	2023/07/01	REVISION TO ADD DIMENSIONS	H.S.M/W	
9	09	2023/07/01	REVISION TO ADD MATERIAL	H.S.M/W	
10	10	2023/07/01	REVISION TO ADD FINISH	H.S.M/W	
11	11	2023/07/01	REVISION TO ADD TOLERANCES	H.S.M/W	
12	12	2023/07/01	REVISION TO ADD PART NAME	H.S.M/W	
13	13	2023/07/01	REVISION TO ADD DIMENSIONS	H.S.M/W	
14	14	2023/07/01	REVISION TO ADD MATERIAL	H.S.M/W	
15	15	2023/07/01	REVISION TO ADD FINISH	H.S.M/W	
16	16	2023/07/01	REVISION TO ADD TOLERANCES	H.S.M/W	
17	17	2023/07/01	REVISION TO ADD PART NAME	H.S.M/W	
18	18	2023/07/01	REVISION TO ADD DIMENSIONS	H.S.M/W	
19	19	2023/07/01	REVISION TO ADD MATERIAL	H.S.M/W	
20	20	2023/07/01	REVISION TO ADD FINISH	H.S.M/W	

STEP	LEVEL 1	LEVEL 2	LEVEL 3	DATE	BY	CHK'D BY
1	01	01	01	2023/07/01	H.S.M/W	
2	02	02	02	2023/07/01	H.S.M/W	
3	03	03	03	2023/07/01	H.S.M/W	
4	04	04	04	2023/07/01	H.S.M/W	
5	05	05	05	2023/07/01	H.S.M/W	
6	06	06	06	2023/07/01	H.S.M/W	
7	07	07	07	2023/07/01	H.S.M/W	
8	08	08	08	2023/07/01	H.S.M/W	
9	09	09	09	2023/07/01	H.S.M/W	
10	10	10	10	2023/07/01	H.S.M/W	
11	11	11	11	2023/07/01	H.S.M/W	
12	12	12	12	2023/07/01	H.S.M/W	
13	13	13	13	2023/07/01	H.S.M/W	
14	14	14	14	2023/07/01	H.S.M/W	
15	15	15	15	2023/07/01	H.S.M/W	
16	16	16	16	2023/07/01	H.S.M/W	
17	17	17	17	2023/07/01	H.S.M/W	
18	18	18	18	2023/07/01	H.S.M/W	
19	19	19	19	2023/07/01	H.S.M/W	
20	20	20	20	2023/07/01	H.S.M/W	

NO.	REV.	DATE	DESCRIPTION OF REVISION	DESIGN BY	CHK'D BY
1	01	2023/07/01	INITIAL DESIGN	H.S.M/W	
2	02	2023/07/01	REVISION TO SOLIDWORKS	H.S.M/W	
3	03	2023/07/01	REVISION TO ADD DIMENSIONS	H.S.M/W	
4	04	2023/07/01	REVISION TO ADD MATERIAL	H.S.M/W	
5	05	2023/07/01	REVISION TO ADD FINISH	H.S.M/W	
6	06	2023/07/01	REVISION TO ADD TOLERANCES	H.S.M/W	
7	07	2023/07/01	REVISION TO ADD PART NAME	H.S.M/W	
8	08	2023/07/01	REVISION TO ADD DIMENSIONS	H.S.M/W	
9	09	2023/07/01	REVISION TO ADD MATERIAL	H.S.M/W	
10	10	2023/07/01	REVISION TO ADD FINISH	H.S.M/W	
11	11	2023/07/01	REVISION TO ADD TOLERANCES	H.S.M/W	
12	12	2023/07/01	REVISION TO ADD PART NAME	H.S.M/W	
13	13	2023/07/01	REVISION TO ADD DIMENSIONS	H.S.M/W	
14	14	2023/07/01	REVISION TO ADD MATERIAL	H.S.M/W	
15	15	2023/07/01	REVISION TO ADD FINISH	H.S.M/W	
16	16	2023/07/01	REVISION TO ADD TOLERANCES	H.S.M/W	
17	17	2023/07/01	REVISION TO ADD PART NAME	H.S.M/W	
18	18	2023/07/01	REVISION TO ADD DIMENSIONS	H.S.M/W	
19	19	2023/07/01	REVISION TO ADD MATERIAL	H.S.M/W	
20	20	2023/07/01	REVISION TO ADD FINISH	H.S.M/W	

8. GENERAL PRECAUTIONS

1. Handling

- (a) When the module is assembled, It should be attached to the system firmly using every mounting holes. Be careful not to twist and bend the modules.
- (b) Refrain from strong mechanical shock and / or any force to the module. In addition to damage, this may cause improper operation or damage to the module and CCFL back-light.
- (c) Note that polarizers are very fragile and could be easily damaged. Do not press or scratch the surface harder than a HB pencil lead.
- (d) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, Staining and discoloration may occur.
- (e) If the surface of the polarizer is dirty, clean it using some absorbent cotton or soft cloth.
- (f) The desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane. Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (g) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away thoroughly with soap.
- (h) Protect the module from static , it may cause damage to the C-MOS Gate Array IC.
- (i) Use fingerstalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (j) Do not disassemble the module.
- (k) Do not pull or fold the lamp wire.
- (l) Do not adjust the variable resistor which is located on the back side.
- (m) Protection film for polarizer on the module shall be slowly peeled off just before use so that the electrostatic charge can be minimized.
- (n) Pins of I/F connector shall not be touched directly with bare hands.

2. STORAGE

- (a) Do not leave the module in high temperature, and high humidity for a long time. It is highly recommended to store the module with temperature from 0 to 35 °C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD module in direct sunlight.
- (c) The module shall be stored in a dark place. It is prohibited to apply sunlight or fluorescent light during the store.

3. OPERATION

- (a) Do not connect, disconnect the module in the “ Power On” condition.
- (b) Power supply should always be turned on/off by following item 6.3 “ Power on/off sequence “.
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back-light connector and its inverter power supply shall be a minimized length and be connected directly . The longer cable between the back-light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

4. OTHERS

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. (the supply voltage variation, input voltage variation, variation in part contents and environmental temperature, so on) Otherwise the module may be damaged.
- (d) If the module displays the same pattern continuously for a long period of time, it can be the situation when the image “sticks” to the screen.
- (e) This module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.

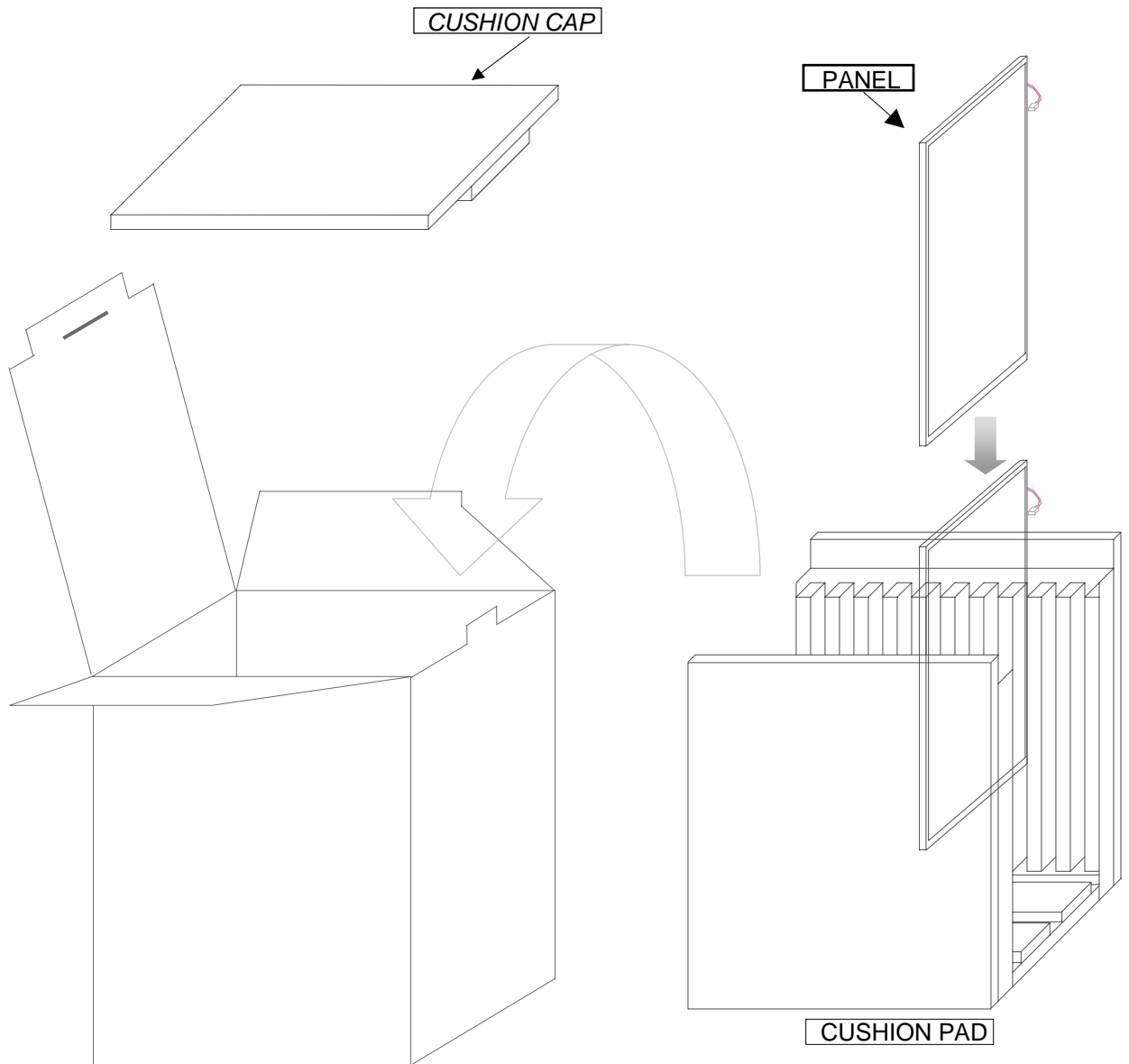
9. Packing

9.1 CARTON(Internal Packing)

(1) Packing Form

Corrugated fiberboard box and corrugated foam as shock absorber

(2)Packing Method



Note (1)Acceptance number of piling : 10 sets

(2)Carton size : 285(W) X 240(D) X 352(H)

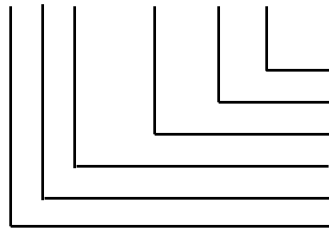
(3)Packing Material

NO.	Parts name	Quantity
1.	Static electric protective sack	10 pcs
2.	Packing case (inner box) include shock absorber	1 set
3.	Pictorial marking	2 pics
4.	Carton	1 set

10. Marking & Others

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

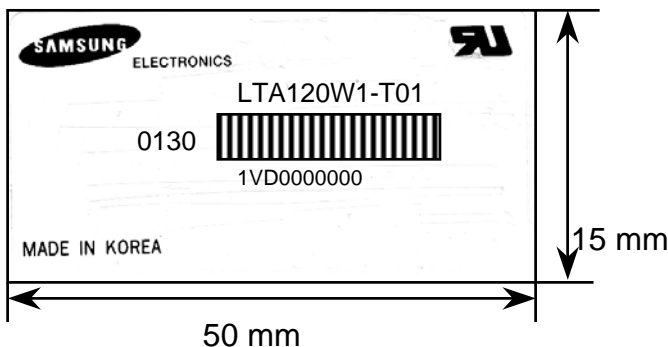
- (1)Parts number : LTA120W1-T01
- (2)Revision : One letter
- (3)Control Code : One letter
- (4)Lot number : X X X ~~XXX~~ ~~XXX~~



Cell Position No.(In the one Glass)
 Glass No.(In the one Lot)
 Lot No.(Glass)
 Month(Note 1)
 Revision
 Line

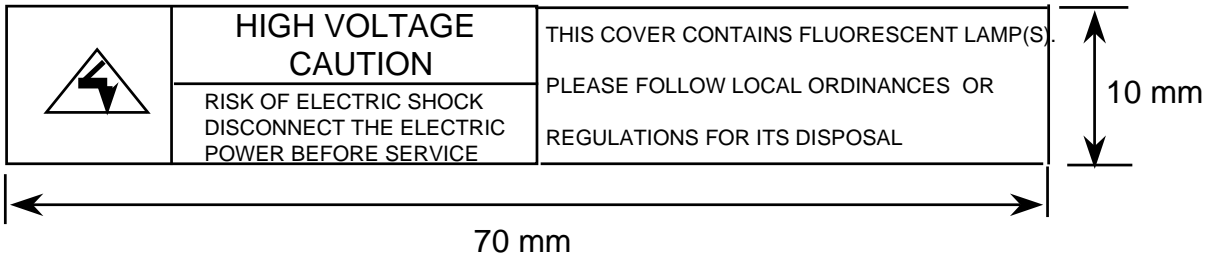
NOTE 1). This code indicating year is omitted in the products of KIHENG site.

(5) Nameplate Indication



LTA... : Parts name
 1VD... : Lot number
 0130 : Inspected work week

High voltage caution



(6) Packing box attach

