

SPECIFICATION FOR APPROVAL

Product Specification

(·	♦)	Preliminary Specification
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) Final Specification

litle	9.7" XGA IFT LCD		
	i I		

Customer	General
MODEL	

SUPPLIER	LG Display Co., Ltd.		
*MODEL	LP097X02		
Suffix	SLQ1		

^{*}When you obtain standard approval, please use the above model name without suffix

APPROVED BY	SIGNATURE
Please return 1 copy for you your signature and commer	

APPROVED BY	SIGNATURE
J. K. Kim / S. Manager	
REVIEWED BY	
G. W. Do / Manager	
PREPARED BY	
H. H. Lee / Engineer	
Product Engineerin LG Display Co.,	

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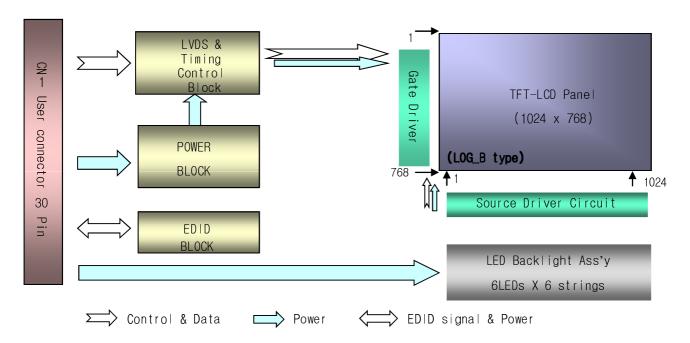
RECORD OF REVISIONS

Revision No	Revision Date	Page	Description	EDID ver
0.1	7. Feb. 2010	-	First Draft	TBD



1. General Description

The LP097X02 is a Color Active Matrix Liquid Crystal Display with an integral LED backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally Black mode. This TFT-LCD has 9.7 inches diagonally measured active display area with XGA resolution(1024 horizontal by 768 vertical pixel array). Each pixel is divided into Red, Green and Blue subpixels or dots which are arranged in vertical stripes. Gray scale or the brightness of the sub-pixel color is determined with a 6-bit gray scale signal for each dot, thus, presenting a palette of more than 262,144 colors. The LP097X02 has been designed to apply the interface method that enables low power, high speed, low EMI. The LP097X02 is intended to support applications where thin thickness, low power are critical factors and graphic displays are important. In combination with the vertical arrangement of the sub-pixels, the LP097X02 characteristics provide an excellent flat display for office automation products such as Notebook PC.



General Features

Active Screen Size	9.7 inches diagonal
Outline Dimension	210.32(H) × 164.37 (V) × 2.88(D, Max.) mm ※ PCB area : 5.22(Max.)
Pixel Pitch	0.192 mm × 0.192 mm
Pixel Format	1024 horiz. by 768 vert. Pixels RGB strip arrangement
Color Depth	6-bit, 262,144 colors
Luminance, White	400 cd/m²(Typ., @I _{LED} =20mA)
Power Consumption	Logic : 0.7W(typ.@Mosaic), Back Light : 2.1W (typ.@ I _{LED} = 20mA)
Weight	143g (Max.)
Display Operating Mode	Transmissive mode, normally Black
Surface Treatment	Glare, Anti-reflective treatment of the front polarizer, 2H

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2. Absolute Maximum Ratings

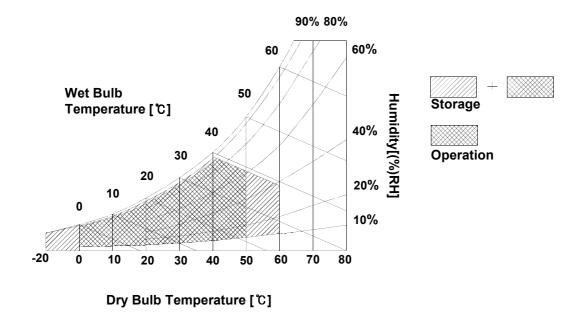
The following are maximum values which, if exceeded, may cause faulty operation or damage to the unit.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Val	ues	Units	Notes	
i arameter	Symbol	Min	Max	Office		
Power Input Voltage	VCC	-0.3	4.0	Vdc	at 25 ± 5°C	
Operating Temperature	Тор	0	50	°C	1	
Storage Temperature	Hst	-20	60	°C	1	
Operating Ambient Humidity	Нор	10	90	%RH	1	
Storage Humidity	Нѕт	10	90	%RH	1	

Note: 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39°C Max, and no condensation of water.



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3. Electrical Specifications

3-1. Electrical Characteristics

The LP097X02 requires two power inputs. One is employed to power the LCD electronics and to drive the TFT array and liquid crystal. The second input which powers the LED BL.

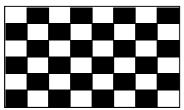
Table 2. ELECTRICAL CHARACTERISTICS

Values

Parameter	Cumbal		Unit	Notos		
Parameter	Symbol	Min	Тур	Max	Offic	Notes
MODULE :						
Power Supply Input Voltage	VCC	3.0	3.3	3.6	V_{DC}	
Power Supply Input Current	I _{CC} Mosaic	-	212	244	mA	1
Power Consumption	Pc	-	0.7	0.81	Watt	1
Differential Impedance	Zm	90	100	110	Ohm	2
LED Backlight :						
(Without LED Driver)						
LED Driver input Volatge (on system)	VLED			12	V	3
Operating Current per string	I _{LED}	5	20	25	mA	4
Power Consumption	P_{BL}		2.1	2.6	Watt	5
Life Time		10,000	-	-	Hrs	6

Note)

1. The specified current and power consumption are under the Vcc = 3.3V, $25^{\circ}C$, fv = 60Hz condition whereas Mosaic pattern is displayed and fv is the frame frequency.



- 2. This impedance value is needed to proper display and measured form LVDS Tx to the mating connector.
- 3. LED input voltage must be input below than 12V to operate normally for LED Driver.
- 4. The typical operating current $\,$ is for the typical surface luminance (L_{WH}) in optical characteristics.
- 5. The LED power consumption shown above does not include power of external LED driver circuit for typical current condition.
- 6. The life time is determined as the time at which brightness of lamp is 50% compare to that of initial value at the typical lamp current.

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3-2. Interface Connections

This LCD employs two interface connections, a 30 pin connector is used for the module electronics interface and the other connector is used for the integral backlight system.

The electronics interface connector is a model 20525-030E-02 manufactured by I-PEX.

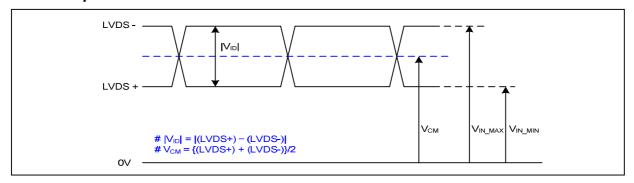
Table 3. MODULE CONNECTOR PIN CONFIGURATION (CN1)

Pin	Symbol	Description	Notes
1	GND	Ground	[LVDS Receiver]
2	VCC	Power Supply, 3.3V Typ.	SiliconWorks, SW0627B
3	VCC	Power Supply, 3.3V Typ.	[Connector]
4	V EEDID	DDC 3.3V power	20525-030E-02 (IPEX), 30pin
5	Thermistor	Connect to thermistor (Customer request)	[Mating Connector]
6	CIk EEDID	DDC Clock	I-PEX 20524-030T-01 series or equivalent (micro-coax type)
7	DATA EEDID	DDC Data	or equivalent (more edux type)
8	RIN 0-	Negative LVDS differential data input	[Connector pin arrangement]
9	RIN 0+	Positive LVDS differential data input	LCD front view
10	GND	Ground	
11	RIN 1-	Negative LVDS differential data input	1 30
12	RIN 1+	Positive LVDS differential data input	<u> </u>
13	GND	Ground	
14	RIN 2-	Negative LVDS differential data input	
15	RIN 2+	Positive LVDS differential data input	
16	GND	Ground	
17	CLKIN-	Negative LVDS differential clock input	
18	CLKIN+	Positive LVDS differential clock input	
.19	GND	Ground	
20	NC	No Connection	
21	Vdc	LED Anode (Positive)	
.22	Vdc	LED Anode (Positive)	
23	NC	No Connection	
.24	Vdc1	LED Cathode (Negative)	
25	Vdc2	LED Cathode (Negative)	
26	Vdc3	LED Cathode (Negative)	
	Vdc4	LED Cathode (Negative)	
	Vdc5	LED Cathode (Negative)	
29	Vdc6	LED Cathode (Negative)	
30	Thermistor	Connect to thermistor (Customer request)	



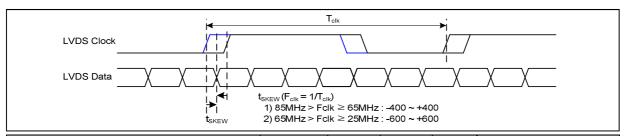
3-3. LVDS Signal Timing Specifications

3-3-1. DC Specification



Description	Symb ol	Min	Max	Unit	Notes
LVDS Differential Voltage	$ V_{ID} $	100	600	mV	-
LVDS Common mode Voltage	V_{CM}	0.6	1.8	٧	-
LVDS Input Voltage Range	V _{IN}	0.3	2.1	V	-

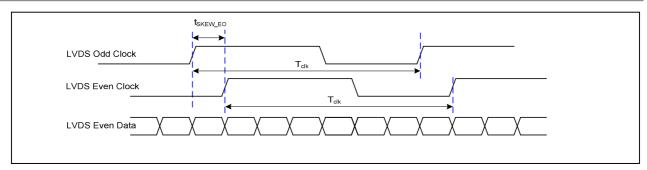
3-3-2. AC Specification



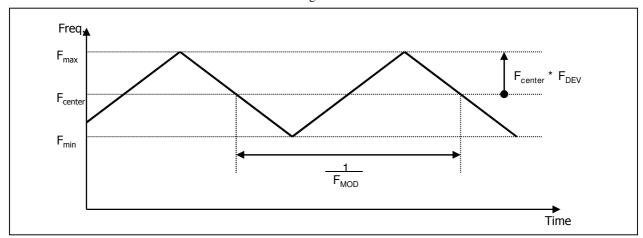
Description	Symbol	Min	Max	Unit	Notes
LVDS Clock to Data Skow Margin	t _{skew}	- 240	+ 240	ps	105MHz > Fclk ≥ 95MHz
LVDS Clock to Data Skew Margin	t _{SKEW} - 400 + 400 ps		85MHz > Fclk ≥ 65MHz		
LVDS Clock to Clock Skew Margin (Even to Odd)	t _{skew_eo}	- 1/7	+ 1/7	T _{clk}	-
Maximum deviation of input clock frequency during SSC	F _{DEV}	-	± 3	%	-
Maximum modulation frequency of input clock during SSC	F _{MOD}	-	200	KHz	-

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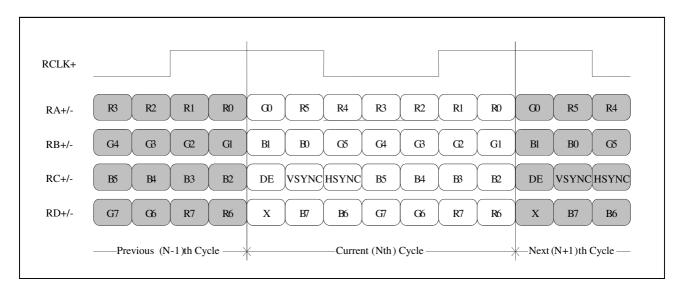
< Clock skew margin between channel >



< Spread Spectrum >

3-3-3. Data Format

-. LVDS 1 Port



< LVDS Data Format >

Condition: VCC =3.3V



Product Specification

3-4. Signal Timing Specifications

This is the signal timing required at the input of the User connector. All of the interface signal timing should be satisfied with the following specifications and specifications of LVDS Tx/Rx for its proper operation.

Table 6. TIMING TABLE

ITEM	Symbol		Min	Тур	Max	Unit	Note
DCLK	Frequency	f_{CLK}	97	100.03	103	MHz	
	Active	Thp	1024	1024	1024		
Hsync	Period	t _{WH}	1960	2084	2223	Tclk	
	Width-Active	t _{WHA}	240	320	400		
Vsync	Active	t _{VP}	768	768	768		
	Period	t _{wv}	776	800	824	tHP	
	Width-Active	t _{wva}	3	10	17		
	Horizontal back porch	t _{HBP}	400	480	560	+CI I/	
Data	Horizontal front porch	t_{HFP}	180	260	320	tCLK	
Enable	Vertical back porch	t_{VBP}	4	6	12	+UD	
	Vertical front porch	t _{VFP}	1	16	32	tHP	



High: 0.7VCC Data Enable, Hsync, Vsync Low: 0.3VCC 0.5 Vcc DCLK t_{HP} Hsync **t**WHA t_{HFP} t_{HBP} Data Enable Vsync twva t_{VFP} t_{VBP} Data Enable

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3-6. Color Input Data Reference

The brightness of each primary color (red, green and blue) is based on the 6-bit gray scale data input for the color; the higher the binary input, the brighter the color. The table below provides a reference for color versus data input.

Table 7. COLOR DATA REFERENCE

									Inp	out Co	olor D	ata							
	Color			RE	ED					GRE	EN					BL	UE		
`	30101	MSE	3				LSB	MSE	3				LSB	MSE	3				LSB
	_	R 5	R 4	R 3	R 2	R 1	R 0	G 5	G 4	G 3	G 2	G 1	G 0	B 5	B 4	В3	B 2	B 1	B 0
	Black	0	0	0		0	0	0	0	0	0	0	0	0	0		0	0	0
	Red	1	.1	.1	1	1	1	0	0		0	0	0	0	0	. 0	0	0	0
	Green	0	0	0	0	0	0	1	1	. 1				0	0	0	0	0	0
Basic	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1		1
Color	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED	RED (01)	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
	RED (62)	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (63)	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (01)	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
GREEN					 														
	GREEN (62)	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
	GREEN (63)	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	BLUE (00)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (01)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE					 														• • • • • •
	BLUE (62)	0	0	0	0	0	0	 0	0	0	0	0	0	1	1	 1	 1	1	0
	BLUE (63)	0	0	0	0	0	0	0	0	0	0	0	0	 1	1	1	 1	1	1

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3-7. Power Sequence

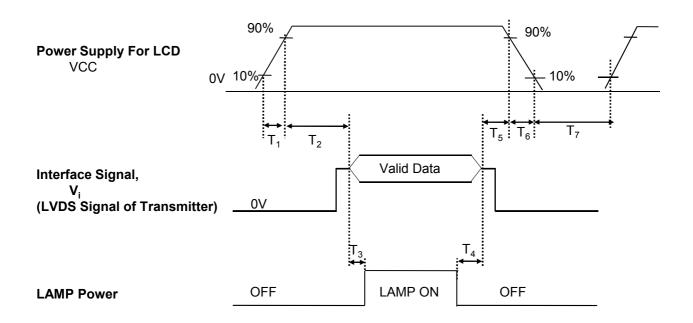


Table 8. POWER SEQUENCE TABLE

Parameter		Value		Units
	Min.	Тур.	Max.	
T ₁	0.5	-	10	(ms)
T ₂	0	-	50	(ms)
T ₃	200	-	-	(ms)
T ₄	200	-	-	(ms)
T ₅	0	-	50	(ms)
T ₆	0	-	10	(ms)
T ₇	400	-	-	(ms)

Note)

- 1. Valid Data is Data to meet "3-3. LVDS Signal Timing Specifications"
- 2. Please avoid floating state of interface signal at invalid period.
- 3. When the interface signal is invalid, be sure to pull down the power supply for LCD VCC to 0V.
- 4. Lamp power must be turn on after power supply for LCD and interface signal are valid.

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4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 20 minutes in a dark environment at 25°C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of Φ and Θ equal to 0° .

FIG. 1 presents additional information concerning the measurement equipment and method.

FIG. 1 Optical Characteristic Measurement Equipment and Method

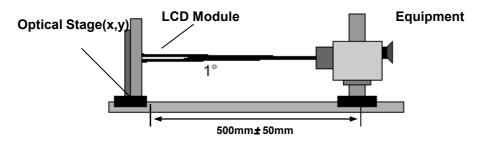


Table 9. OPTICAL CHARACTERISTICS

Ta=25°C, VCC=3.3V, f_{V} =60Hz, f_{CLK} = 100.03MHz, ILED = 20mA

				1				
Para	meter	Symbol	Condition	Min	Тур	Max	Units	Notes
Average L	uminance	L _{AVE}	5 Points (ILED= 20mA)	340	400	-	cd/m²	Fig 2
Luminanc	e variation	$\delta_{\text{ WHITE}}$	17 points	-	1.4	1.6	%	Fig 2
С	C/R		Center 1 Point	500	800	-	-	
Respor	Response time		-	-	35	50	ms	Fig 3
	Horizontal		φx(Left,Right)	± 80	± 89	-		
Viewing Angle	Vertical	Θ	φyu(Up)	80	89	-	ø	Fig 4
-	vertical	Θ	φyd(Down)	80	89	-		
•			RX	0.575	0.605	0.635		
		RED	RY	0.315	0.345	0.375		
		ODEEN	GX	0.300	0.330	0.360		
Color Coord	dinates	GREEN	GY	0.535	0.565	0.595		
			BX	0.120	0.150	0.180		
		BLUE	BY	0.090	0.120	0.150		
		\A/I IITE	WX	0.279	0.309	0.339		
		WHITE	WY	0.287	0.317	0.347		
Cross	s Talk	DSHA	-	-	-	4.0	%	Fig 5
Gray	Scale	-	-		Gamn	na 2.2		

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Notes)

1. Contrast Ratio(CR) is defined mathematically as

- 2. Response time is the time required for the display to transition from white to black (rise time, Tr_R) and from black to white(Decay Time, Tr_D). For additional information see FIG 3.
- 3. Surface luminance is the average of 5 point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see FIG 1.

$$L_{WH}$$
 = Average($L_1, L_2, ... L_5$)

4. The variation in surface luminance , The panel total variation (δ_{WHITE}) is determined by measuring L_N at each test position 1 through 13 and then defined as followed numerical formula. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Maximum}(\mathsf{L}_1, \mathsf{L}_2, \, \dots \, \mathsf{L}_{17})}{\text{Minimum}(\mathsf{L}_1, \mathsf{L}_2, \, \dots \, \mathsf{L}_{17})}$$

- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 4.
- 6. Gray scale specification

՝ f _∨ =60⊦	Ηz
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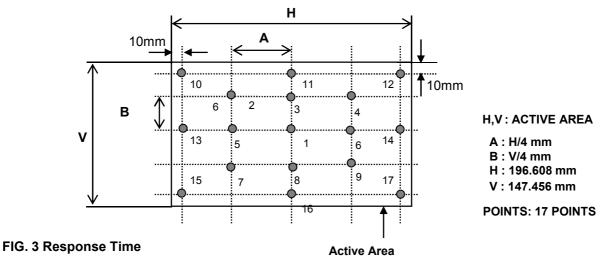
Gray Level	Luminance [%] (Typ)
LO	0.13
L7	1.00
L15	4.30
L23	9.80
L31	19.2
L39	34.2
L47	53.5
L55	74.5
L63	100

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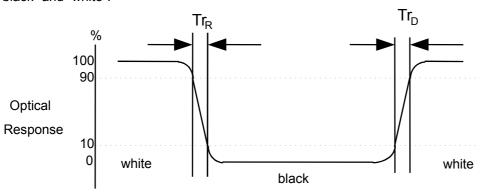


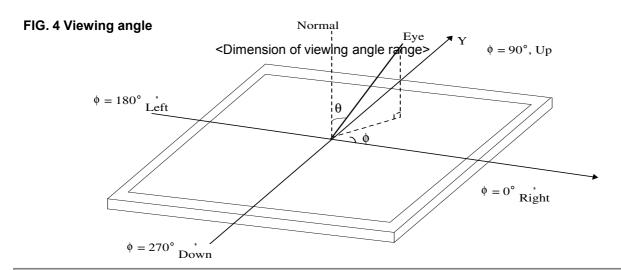
FIG. 2 Luminance

<Measuring point for Average Luminance & measuring point for Luminance variation>



The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".





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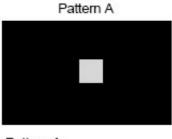
FIG. 5 Cross talk

No visual cross-talk will be allowed. Two luminance values are measured at center spot with 50×50 pixels. The cross-talk, D_{SHA} , is defined as,

$$D_{SHA} = (L_B - L_A)/L_B \cdot 100\%$$

Where, $L_A = Luminance$ in Pattern A

L_B = Luminance in Pattern B.



Pattern A Gray Scale = 31 in center Black in surrounding area



Pattern B Gray Scale = 31 full screen

5. Mechanical Characteristics

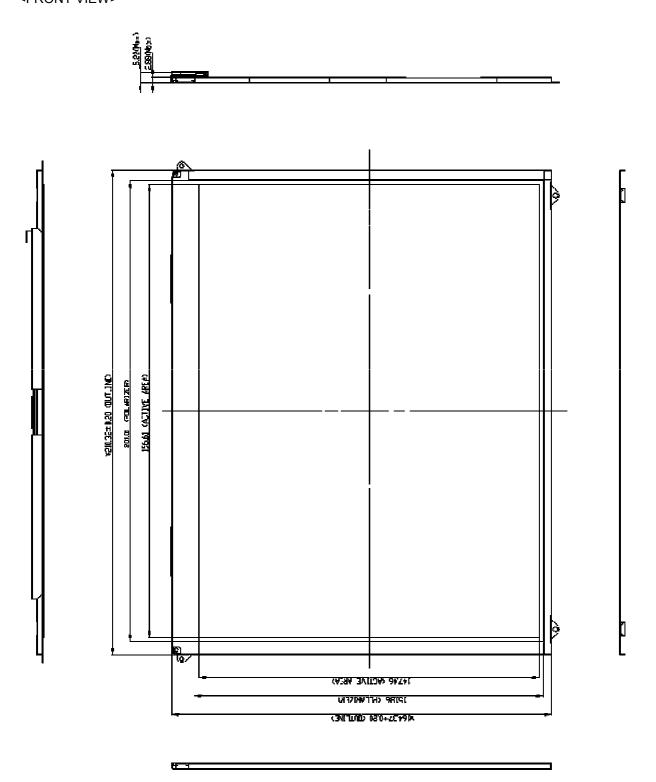
The contents provide general mechanical characteristics for the model LP097X02. In addition the figures in the next page are detailed mechanical drawing of the LCD.

	Horizontal	210.32 ± 0.20mm
Outline Dimension	Vertical	164.37 ± 0.20mm
	Thickness	2.88mm(Max.)
Bezel Area	Horizontal	201.01mm(POL)
bezei Alea	Vertical	151.86mm(POL)
Active Diepley Area	Horizontal	196.608mm
Active Display Area	Vertical	147.456mm
Weight	143g (Max.)	
Surface Treatment	Hard coating(2H), Gla	are treatment of the front Polarizer (Haze 0%)

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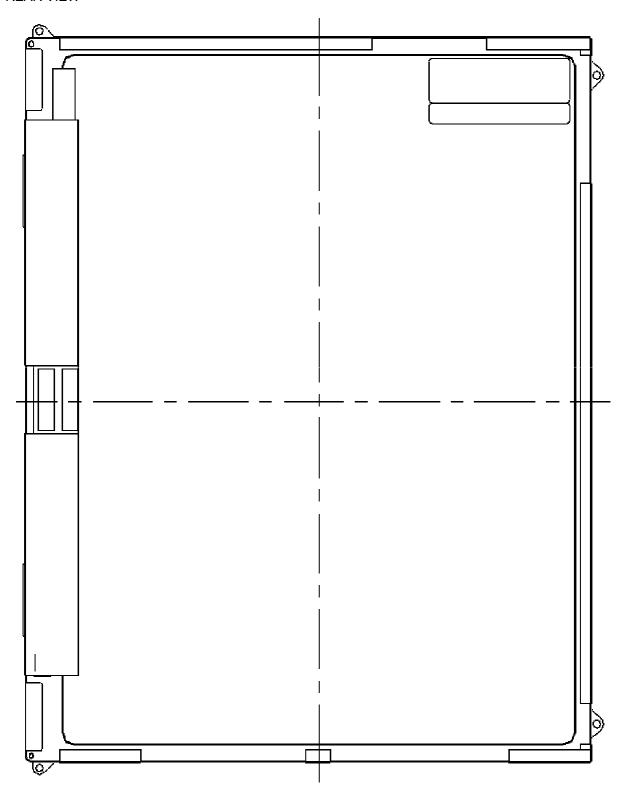


<FRONT VIEW>





<REAR VIEW>





6. Reliability

Environment test condition

No.	Test Item	Conditions				
1	High temperature storage test	Ta= 60°C, 240h				
2	Low temperature storage test	Ta= -20°C, 240h				
3	High temperature operation test	Ta= 50°C, 50%RH, 240h				
4	Low temperature operation test	Ta= 0°C, 240h				
5	Vibration test (non-operating)	Sine wave, 10 ~ 500 ~ 10Hz, 1.5G, 0.37oct/min 3 axis, 1hour/axis				
6	Shock test (non-operating)	Half sine wave, 180G, 2ms one shock of each six faces(I.e. run 180G 6ms for all six faces)				
7	Altitude operating storage / shipment	0 ~ 10,000 feet (3,048m) 24Hr 0 ~ 40,000 feet (12,192m) 24Hr				

{ Result Evaluation Criteria }

There should be no change which might affect the practical display function when the display quality test is conducted under normal operating condition.

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7. International Standards

7-1. Safety

a) UL 60950-1:2003, First Edition, Underwriters Laboratories, Inc.,

Standard for Safety of Information Technology Equipment.

b) CAN/CSA C22.2, No. 60950-1-03 1st Ed. April 1, 2003, Canadian Standards Association,

Standard for Safety of Information Technology Equipment.

c) EN 60950-1:2001, First Edition,

European Committee for Electrotechnical Standardization(CENELEC)

European Standard for Safety of Information Technology Equipment.

7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) C.I.S.P.R "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.
- c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)

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8. Packing

8-1. Designation of Lot Mark

a) Lot Mark

	Α	В	С	D	Е	F	G	Н	I	J	К	L	М
--	---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C : SIZE(INCH) D : YEAR

E: MONTH $F \sim M$: SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

8-2. Packing Form

a) Package quantity in one box: 30 pcs

b) Box Size: 478mm × 365mm × 328mm

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9. PRECAUTIONS

Please pay attention to the followings when you use this TFT LCD module.

9-1. MOUNTING PRECAUTIONS

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
 Do not touch the surface of polarizer for bare hand or greasy cloth. (Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzene. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

9-2. OPERATING PRECAUTIONS

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage : $V=\pm 200 \text{mV}(\text{Over and under shoot voltage})$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

 And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.

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9-3. ELECTROSTATIC DISCHARGE CONTROL

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

9-4. PRECAUTIONS FOR STRONG LIGHT EXPOSURE

Strong light exposure causes degradation of polarizer and color filter.

9-5. STORAGE

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

 It is recommended that they be stored in the container in which they were shipped.

9-6. HANDLING PRECAUTIONS FOR PROTECTION FILM

- (1) When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) The protection film is attached to the polarizer with a small amount of glue. If some stress is applied to rub the protection film against the polarizer during the time you peel off the film, the glue is apt to remain on the polarizer.
 - Please carefully peel off the protection film without rubbing it against the polarizer.
- (3) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the polarizer after the protection film is peeled off.
- (4) You can remove the glue easily. When the glue remains on the polarizer surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 1/3

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APPENDIX A. Enhanced Extended Display Identification Data (EEDID™) 3/3

TBD

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