

# SPECIFICATION FOR APPROVAL

( ) Preliminary Specification

() Final Specification

Title

# 42.0" WUXGA TFT LCD

BUYER	
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.		
*MODEL	LC420WU2		
SUFFIX	SLA1 (RoHS Verified)		

\*When you obtain standard approval, please use the above model name without suffix

	APPROVED BY	SIGNATURE DATE	APPROVED BY	SIGNATURE DATE
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# **RECORD OF REVISIONS**

Revision No.	Revision Date	Page	Description
0.0	Jul.26, 2006	-	Preliminary Specification(First Draft)
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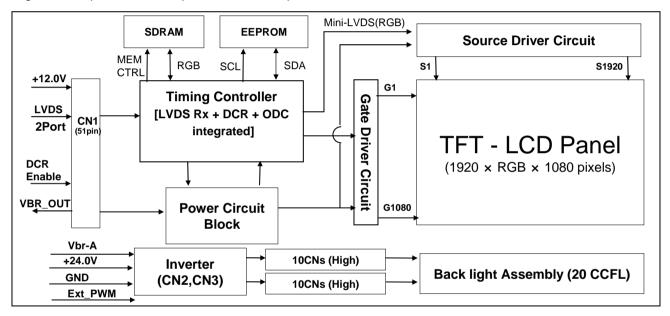


# 1. General Description

The LC420WU2 is a Color Active Matrix Liquid Crystal Display with an integral Cold Cathode Fluorescent Lamp(CCFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 42.02 inch diagonally measured active display area with WUXGA resolution (1080 vertical by 1920 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. Therefore, it can present a palette of more than 16.7M(true) colors.

It has been designed to apply the 8-bit 2-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



# **General Features**

Active Screen Size	42.02 inches(1067.31mm) diagonal
Outline Dimension	983.0(H) x 576.0 (V) x 51.0 mm(D) (Typ.)
Pixel Pitch	0.4845 mm x 0.4845 mm
Pixel Format	1920 horiz. by 1080 vert. Pixels, RGB stripe arrangement
Color Depth	8-bit, 16.7 M colors
Luminance, White	550 cd/m <sup>2</sup> (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free ( R/L 178 (Typ.), U/D 178 (Typ.))
Power Consumption	Total TBD W (Typ.) (Logic=TBD W, Inverter=TBDW [I <sub>BL</sub> =TBD mA] )
Weight	13.0К g (Тур.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer

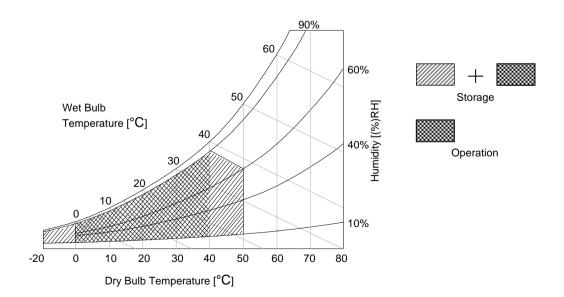
# 2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Table 1. ABSOLU	JTE MAXIMUM RATINGS
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Parameter		Symbol Value		Unit	Remark		
Γd	arameter	Symbol	Min	Max	Onit	Remark	
Power Input	LCM	Vlcd	-0.3	+14.0	VDC	at 25 $\pm$ 2 °C	
Voltage	Backlight inverter	VBL	+21.6	+28.0	VDC		
ON/OFF Cont	ON/OFF Control Voltage		-0.3	+5.25	Vdc		
Brightness Co	ontrol Voltage	VBr	0	+5.0	VDC		
Operating Ter	mperature	Тор	0	+40	°C		
Storage Temperature		Тѕт	-20	+50	°C	Note 1	
Operating Ambient Humidity		Нор	10	90	%RH	NOLE 1	
Storage Humi	idity	Hs⊤	10	90	%RH		

Note : 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 °C Max. and no condensation of water.



# 3. Electrical Specifications

# **3-1. Electrical Characteristics**

It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the CCFL backlight and inverter circuit.

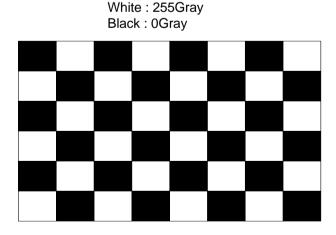
### Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Value	Unit	Note			
	Cymbol	Min	Тур	Max	Offic			
Circuit :								
Power Input Voltage	VLCD	11.4	12.0	12.6	Vdc			
Device lanut Overcent	ILCD	-	TBD	TBD	mA	1		
Power Input Current		-	TBD	TBD	mA	2		
Power Consumption	PLCD	-	TBD	TBD	Watt	1		
Rush current	Irush	-	-	3.0	А	3		

Note : 1. The specified current and power consumption are under the V<sub>LCD</sub>=12.0V,  $25 \pm 2^{\circ}$ C, f<sub>V</sub>=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f<sub>V</sub> is the frame frequency.

2. The current is specified at the maximum current pattern.

3. The duration of rush current is about 2ms and rising time of power input is 1ms (min.).



Mosaic Pattern(8 x 6)

### Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Symbol		Values	Linit	Notes			
Para				Min	Тур	Max	Unit	notes	
Inverter :									
Power Supply Inp	ut Voltage		Vbl	22.8	24.0	26.2	Vdc	1	
Unloading Input V	oltage					28	Vdc		
Power Supply Inp	ut Voltage Rip	ple		-0.2		0.2	Vp-p		
Power Supply Inp	ut Current		IBL	-	TBD	TBD	A	1	
Power Supply Input Current(In-Rush)			Irush	h		TBD	A	VBL = 22.8V EXTVbr- B=100% Vbr - 3.3V	
Power Consumpti	on		Pbl	-	TBD	TBD	W	1	
Input Voltage for	Brightness	Adjust	Vbr	0.0	-	3.3	Vdc	2	
Control System	0/0//	On	V on	2.5	-	5.0	Vdc		
Signals	On/Off	Off	V off	-0.3	0.0	0.8	Vdc		
Brightness Adj(Burst	mode)		EXTVbr-B	20		100	%		
			PAL	145	150	155			
PWM Frequency for I	NTSC & PAI			195	200	205	Hz	3	
			NTSC	175	180	185		0	
				235	240	245			
Lamp Voltage			Vout	TBD	TBD	TBD	V(rms)	1	
Lamp Current			lout	TBD	TBD	TBD	mÁ(rms	1	
Life Time				50,000			Hrs	4	

Notes :

1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 120 minutes at 25±2°C

The specified current and power consumption are under the typical supply Input voltage 24V and Vbr 1.65V, it is total power consumption.

The ripple voltage of the power supply input voltage is under 0.5 Vp-p.

LPL recommend Input Voltage is  $24.0V \pm 5\%$ .

#### 2. Brightness Control.

This VBR Voltage control brightness.

VBR Voltage	Function			
3.3V	Maximum Brightness (TBD%)			
0V	Minimum Brightness (TBD%)			

3. LPL recommend that the PWM freq. is synchronized with the treble harmonic of Vsync signal of system.

- 4. The life is determined as the time at which luminance of the lamp is 50% compared to that of initial value at the typical lamp current on condition of continuous operating at 25 ± 2°C. Specified value is when lamp is aligned horizontally.
- 5. The lamp life time for LCM is guaranteed minimum 50,000 hours when Vbr(Analog) and PWM(Burst) are maximum values.

# **3-2. Interface Connections**

LG.PHILIPS LCD 👸

This LCD module employs two kinds of interface connection, a 51-pin connector is used for the module electronics and two 12-pin connectors are used for the integral backlight system.

### 3-2-1. LCD Module

- LCD Connector(CN1): FI-R51S-HF(manufactured by JAE) or KN25-51P-0.5SH(manufactured by Hirose)
- Mating Connector : FI-R51HL(JAE) or compatible

No	Symbol	Description		No	Symbol	Description
1	Reserved(NC)	No connection	Π	27	GND	GND
2	Reserved (NC)	No connection	Π	28	RE0N	SECOND CHANNEL 0-
3	Reserved (NC)	No connection	Ì	29	RE0P	SECOND CHANNEL 0+
4	Reserved (NC)	No connection	Π	30	RE1N	SECOND CHANNEL 1-
5	Reserved (NC)	No connection	Π	31	RE1P	SECOND CHANNEL 1+
6	Reserved (NC)	No connection		32	RE2N	SECOND CHANNEL 2-
7	Reserved (NC)	No connection		33	RE2P	SECOND CHANNEL 2+
8	Reserved (NC)	No connection	Π	34	GND	Ground
9	Reserved (NC)	No connection		35	RECLKN	SECOND CLOCK CHANNEL C-
10	Reserved (NC)	No connection		36	RECLKP	SECOND CLOCK CHANNEL C+
11	GND	Ground		37	GND	Ground
12	RO0N	FIRST CHANNEL 0-		38	RE3N	SECOND CHANNEL 3-
13	RO0P	FIRST CHANNEL 0+	Π	39	RE3P	SECOND CHANNEL 3+
14	RO1N	FIRST CHANNEL 1-	Π	40	Reserved (NC)	No connection
15	RO1P	FIRST CHANNEL 1+	Π	41	Reserved (NC)	No connection
16	RO2N	FIRST CHANNEL 2-	Π	42	GND	GND
17	RO2N	FIRST CHANNEL 2+	Π	43	GND	GND
18	GND	Ground	Π	44	GND	Ground
19	ROCLKN	FIRST CLOCK CHANNEL C-	Ì	45	GND	Ground
20	ROCLKP	FIRST CLOCK CHANNEL C+		46	GND	Ground
21	GND	Ground	Π	47	NC	NC
22	RO3N	FIRST CHANNEL 3-		48	VLCD	Power Supply +12.0V
23	RO3P	FIRST CHANNEL 3+	Π	49	VLCD	Power Supply +12.0V
24	Reserved (NC)	No connection		50	VLCD	Power Supply +12.0V
25	Reserved (NC)	No connection		51	VLCD	Power Supply +12.0V
26	GND	GND		-	-	-

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Note : 1. All GND(ground) pins should be connected together to the LCD module's metal frame.

- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the IEA 664 Standard.
- 4. Specific pins(pin No. #1~#10) are used for internal data process of the LCD module.

If not used, these pins are no connection.

5. The pin no 44 is LCD Test option.

"AGP" (Auto Generation LCM operates Pattern) or "NSB" (No Signal Black) is case that LVDS signals are out of frequency or abnormal condition in spite of 12 volt power supply. LPL recommends "NSB". (AGP : "VCC" or "OPEN" / NSB : "GND")

# Table 5. Required signal assignment for Flat Link (Thine : THC63LVD823) Transmitter(Pin7="L)

Host System	THC63LVD823	FI-R51S-HF	Timing
24 Bit	or Compatible		Controller
R10/R20         R11/R21         R12/R22         R13/R23         R14/R24         R15/R25         R16/R26         R17/R27         G10/G20         G11/G21         G12/G22         G13/G23         G14/G24         G15/G25         G16/G26         G17/G27         B10/B20         B11/B21         B12/B22         B13/B23         B14/B24         B15/B25         B16/B26         B17/B27         Hsync         Vsync         Data Enable         CLOCK	54/62       TA1-TA1+2         57/83       TA1-TA1+2         58/84       TB1-/TB1+2         59/85       4         60/86       TC1-/TC1+4         51/79       TCLK1-4         52/80       TCLK1+4         63/91       TD1-/TD1+4         64/92       65/93         66/94       TA2-/TA2+3         67/95       68/96         61/89       62/90         73/99       TCLK2-7         73/99       TCLK2-7	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	RxO0-         RxO1-         RxO2-         RxO3-         RxC0+         RxE1-         RxE1+         RxE2-         RxE2+         RxE3-         RxE3+         LG / DISM         DCR Enable

#### Note:

- 1. The LCD module uses a 100 Ohm( ) resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

### Table 6. Required signal assignment for Flat Link (Thine : THC63LVD823) Transmitter(Pin7="H")

Host System 24 Bit	THC63LVE or Compat		IS_HE	Timing Controller
R10/R20 R11/R21 R12/R22 R13/R23 R14/R24 R15/R25 R16/R26	51/79 TA1- 52/80 53/81 TB1- 54/82 57/83 TC1- 58/84 59/85 TC	+/TA1- 48 12 +/TB1- 46 14 +/TC1- 43 17 CLK1+ 41 19	$100 \leq 100 < 100 \leq 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 100 < 1000 < 100 < 100 < 100 < 100 < 100 < 100$	Rx00-/Rx00+ Rx01-/Rx01+ Rx02-/Rx02+ Rx0CLK- Rx0CLK+
R17/R27 G10/G20 G11/G21 G12/G22 G13/G23 G14/G24	61/89 62/90 TD1 63/91	CLK1- 42 20 +/TD1- 40 23 +/TA2- 36 28 37 28	100 ≶ 3 100 ≶	RxO3-/RxO3+
G15/G25 G16/G26 G17/G27 B10/B20 B11/B21 B12/B22	67/95 68/96 тС2 <sup>.</sup> 69/97 70/98 т(	+/TB2- 34 35 37 37 37 37 37 37 37 37 37 37	$\begin{array}{c c} 1 \\ 1 \\ 1 \\ 2 \\ 3 \\ 5 \\ 100 \\ 100$	RxE2-/RxE2+ RxECLKIN- RxECLKIN+ RxE3-/RxE3+
B13/B23 B14/B24 B15/B25 B16/B26 B17/B27 Hsync	74/100	CLK2- 27 36 +/TD2 28 38 7 10	3 	LG / <b>DISM</b> DCR Enable
Vsync Data Enable CLOCK	9 10		LCD M	lodule

Note:

- 1. The LCD module uses a 100 Ohm( ) resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.



#### 3-2-2. Backlight Inverter

Master	Slave
-Inverter Connector : S14B-PH-SMC	-Inverter Connector : S12B-PH-SMC
(manufactured by JST) or Equivalent	(manufactured by JST) or Equivalent
- Mating Connector : PHR-14 or Equivalent	-Mating Connector : PHR-12 or Equivalent

#### Table 7. INVERTER CONNECTOR PIN CONFIGULATION

Pin No	Symbol	Description	Master	Slave	Note
1	VBL	Power Supply +24.0V	VBL	VBL	
2	VBL	Power Supply +24.0V	VBL	VBL	
3	VBL	Power Supply +24.0V	VBL	VBL	
4	VBL	Power Supply +24.0V	VBL	VBL	
5	VBL	Power Supply +24.0V	VBL	VBL	
6	GND	POWER GND	GND	GND	
7	GND	POWER GND	GND	GND	
8	GND	POWER GND	GND	GND	1
9	GND	POWER GND	GND	GND	
10	GND	POWER GND	GND	GND	
11	Vbr-a	0.0V ~ 3.3V	VBR	Don't care	2
12	Von/off	0.0V ~ 5.0V	On/Off	Don't care	3, Open/High for B/L on as default
13	EXTVbr-b	0.0V ~ 3.3V	External PWM	-	4
14	NC	No Connection	Reserved	-	5

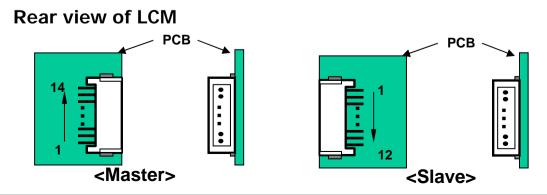
Note : 1. GND should be connected to the LCD module's metal frame.

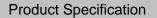
2. Minimum Brightness : VBR-A = 0.0V Maximum Brightness : VBR-A = 3.3V "OPEN" : VBR-A = 1.6V

3. Rising Edge : Lamp "ON" / Falling Edge : Lamp "OFF" / OPEN : Lamp "ON"

4. Pin#13 can be opened. (if Pin #13 is open, EXTVBR-B is 100%)

5. Pin#14 can be opened. (Even though Pin #14 is open, there is no effect on inverter operating)





# 3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

ТЕМ	Symbol	Min	Тур	Max	Unit	Note
Display Period	tHV	-	960	-	tclk	
Blank	tHB	100	140	320	tclk	
Total	tHP	1060	1100	1280	tclk	2200/2
Display Period	tVV	-	1080	-	Lines	
Blank	tVB	11	45	69	Lines	
Total	tVP	1091	1125	1149	Lines	
	Display Period Blank Total Display Period Blank	Display PeriodtHVBlanktHBTotaltHPDisplay PeriodtVVBlanktVB	Display PeriodtHVBlanktHBTotaltHPDisplay PeriodtVVBlanktVB	Display PeriodtHV-960BlanktHB100140TotaltHP10601100Display PeriodtVV-1080BlanktVB1145	Display Period         tHV         -         960         -           Blank         tHB         100         140         320           Total         tHP         1060         1100         1280           Display Period         tVV         -         1080         -           Blank         tVV         11         45         69	Display Period         tHV         -         960         -         tclk           Blank         tHB         100         140         320         tclk           Total         tHP         1060         1100         1280         tclk           Display Period         tVV         -         1080         -         Lines           Blank         tVB         11         45         69         Lines

Table 8. TIMING TABLE for NTSC (DE Only Mode)

דו	ITEM		Min	Тур	Мах	Unit	Note
	DCLK	fCLK	70	74.25	77	MHz	148.5/2
Frequency	Horizontal	fH	65.5	67.5	68.9	KHz	
	Vertical	fV	57	60	63	Hz	

Table 7 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specification for normal operation.

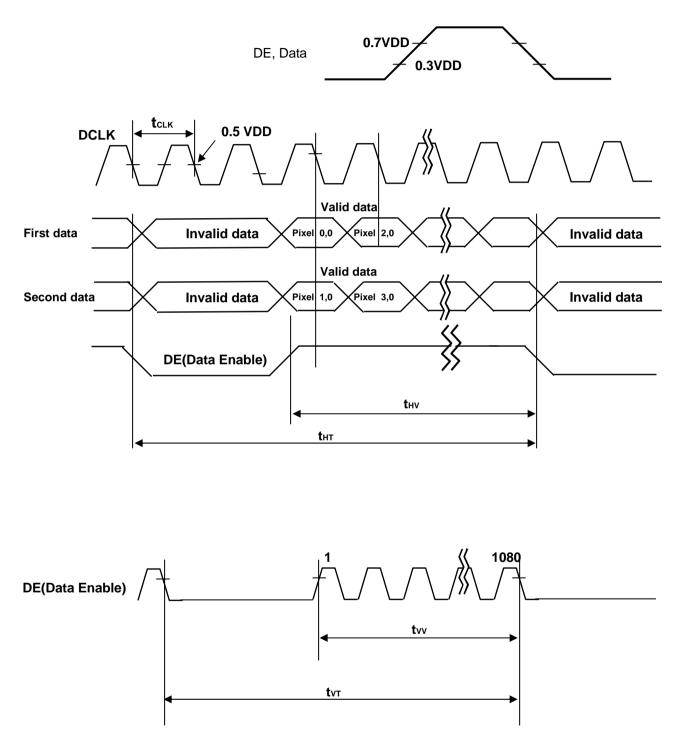
I	TEM	Symbol	Min	Тур	Max	Unit	Note
	Display Period	tHV	-	960	-	tclk	
Horizontal	Blank	tHB	100	140	240	tclk	
	Total	tHP	1060	1100	1200	tclk	2200/2
	Display Period	tVV	-	1080	-	Lines	
Vertical	Blank	tVB	25	45	65	Lines	
	Total	tVP	1105	1125	1145	Lines	
	TEM	Symbol	Min	Тур	Max	Unit	Note
	DCLK	fCLK	59.63	61.88	67.5	MHz	123.75/2
Frequency	Horizontal	fH	55.25	56.25	57.25	KHz	
	Vertical	fV	47	50	53	Hz	

Table 9. TIMING TABLE for PAL (DE Only Mode)

Note : The Input of HSYNC & VSYNC signal does not have an effect on normal operation(DE Only Mode). <u>The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.</u>



# 3-4. Signal Timing Waveforms



# 3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 10 provides a reference for color versus data input.

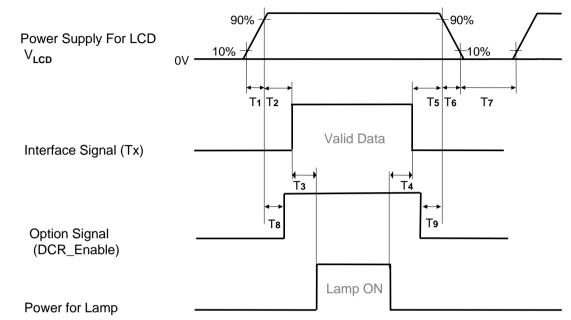
#### Table 10. COLOR DATA REFERENCE

													Inpu	it Co	olor	Data	à									
	Color					RE	D					_		GRE	EEN					_		BL	UE			
			MS								MS								MS							SB
									R1 F							G2								B2		
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1		0	0	0	0	0	0	0
Basic	Blue (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0		1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED										•								•								
	RED (254)		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN							•								•											
	GREEN (254)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																										
	BLUE (254)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1



# 3-6. Power Sequence

### 3-6-1. LCD Driving circuit



#### Table 11. POWER SEQUENCE

Devementer		Value		l locit			
Parameter	Min	Тур	Max	Unit			
T1	0.5	-	10	ms			
T2	0.5	-	50	ms			
Т3	200	-	-	ms			
T4	200	-	-	ms			
Т5	0.5	-	50	ms			
Т6	-	-	300	ms			
T7	1.0	-	-	S			
Т8		0 < T8 < T2					
Т9		0 < T9 < T5					

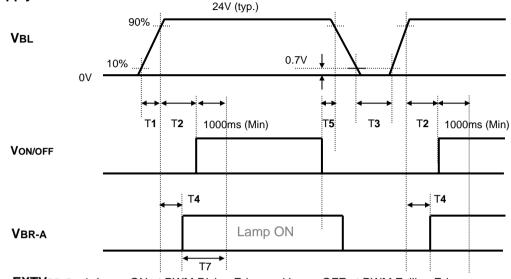
Note: 1. Please avoid floating state of interface signal at invalid period.

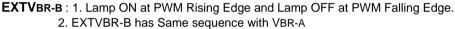
- 2. When the interface signal is invalid, be sure to pull down the power supply  $V_{LCD}$  to 0V.
- 3. The case when the T2/T5 exceed maximum specification, it operates protection pattern(Black pattern) till valid signal inputted. There is no reliability problem.
- 4. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 5. If the on time of option signal(DISM or AI\_Enable) precedes the on time of Power(VLCD), check the LCD logic Power(Vcc) is under 0.8V, otherwise it will be happened abnormal display.
- 6. Flicker would come out when power on-off(T7=under 1s) is continuously tested over several ten-times



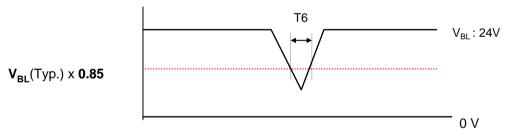
#### 3-6-2. Sequence for Inverter







#### 3-6-3. Deep condition for Inverter



#### Table 12. Power Sequence for Inverter

Parameter		Values		Units	Remarks
Falameter	Min Typ		Max	Units	Remarks
T1	20	-	-	ms	1
T2	500	-	-	ms	
Т3	200	-	-	ms	
T4	0		-	ms	2
T5	10	-	-	ms	
Т6	-	-	10	ms	<b>V<sub>BL</sub></b> (Тур) х <b>0.85</b>
T7	1000	-	-	ms	3

Notes : 1. T1 describes rising time of 0V to 24V and is not applied at restarting time.

2. T4(max) is less than T2.

3. In T7 section, EXTVBR-B should be duty 100% and VBR-A is recommended 3.3V.



# 4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 2 Hrs in a dark environment at  $25 \pm 2^{\circ}$ C. The specified optical values are measured at an approximate 50cm distance from the LCD surface on condition that viewing angle of  $\Phi$  and  $\theta$  equal to 0 °.

FIG. 1 shows additional information concerning the measurement equipment and method.

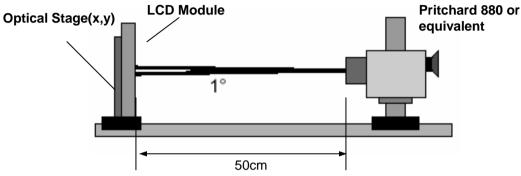


FIG. 1 Optical Characteristic Measurement Equipment and Method

Table 13. OPTICAL CHARACTERISTICS

 $Ta=25\pm2^{\circ}C$ ,  $V_{LCD}=12.0V$ , fv=60Hz, Dclk=148.5MHz VBR=1.65V

Damana		Querra ha a l		Value		1.1.4.14	Nista
Parame	ter	Symbol	Min	Тур	Max	Unit	Note
Contrast Ratio		CR	600	800	-		1
		CR with DCR	1100	1600	-		
Surface Luminance, white		L <sub>WH</sub>	400	550	-	cd/m <sup>2</sup>	2
Luminance Variation	ו	δ <sub>WHITE</sub> 5P	-	-	1.6		3
Response Time	Gray-to-Gray	G to G	-	8	16		4
(Gray-to-Gray)	Rise + decay	Tr <sub>R +</sub> Tr⊳	-	18	-	ms	4
	RED	Rx		TBD			
		Ry		TBD			
	GREEN	Gx		TBD			
Color Coordinates		Gy	Тур	TBD	Тур +0.03		
[CIE1931]	BLUE	Bx	-0.03	TBD			
		Ву		TBD			
	WHITE	Wx		0.285			
		Wy		0.293			
Viewing Angle (CR>	10)						
x axis,	right(φ=0°)	θr	85	89	-		
x axis,	left (φ=180°)	θΙ	85	89	-		
y axis,	up (¢=90°)	θu	85	89	-	degree	5
y axis,	down (φ=270°)	θd	85	89	-		
Gray Scale							6



Note :

1. Contrast Ratio(CR) is defined mathematically as :

Contrast Ratio = Surface Luminance with all white pixels Surface Luminance with all black pixels

Measure Position : center 5point Max C/R (typical 800:1)

- 2. Surface Luminance(L<sub>WH</sub>) is the luminance value measured at an approximate 50cm distance from the center 1-point of LCD surface as all pixels displaying white. See FIG. 2 for more information.
- 3. The variation of surface luminance ,  $\delta$  WHITE is defined as :

 $\delta \text{ WHITE(5P)} = \text{Maximum}(L_{\text{on1}}, L_{\text{on2}}, L_{\text{on3}}, L_{\text{on4}}, L_{\text{on5}}) / \text{Minimum}(L_{\text{on1}}, L_{\text{on2}}, L_{\text{on3}}, L_{\text{on4}}, L_{\text{on5}})$ 

Where  $L_{\text{on1}}$  to  $L_{\text{on5}}$  are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.

- 4. Response time is defined as the required time for the transition from G(N) to G(M) (Rise Time, Tr<sub>R</sub>) and from G(M) to G(N) (Decay Time, Tr<sub>D</sub>). For additional information see the FIG. 3. (N<M)
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- Gray scale specification Gamma Value is approximately 2.2. For more information, see the Table 13.

Table 14.	GRAY	SCALE	SPECIFIC	CATION
-----------	------	-------	----------	--------

Gray Level	Luminance [%] (Typ.)	Luminance [%] (Typ.) with DCR
LO	TBD	TBD
L15	TBD	TBD
L31	TBD	TBD
L47	TBD	TBD
L63	TBD	TBD
L79	TBD	TBD
L95	TBD	TBD
L111	TBD	TBD
L127	TBD	TBD
L143	TBD	TBD
L159	TBD	TBD
L175	TBD	TBD
L191	TBD	TBD
L207	TBD	TBD
L223	TBD	TBD
L239	TBD	TBD
L255	ТВД	ТВД
Ver. 0.0	Jul. 26, 2006	18 /28



Measuring point for surface luminance & measuring point for luminance variation.

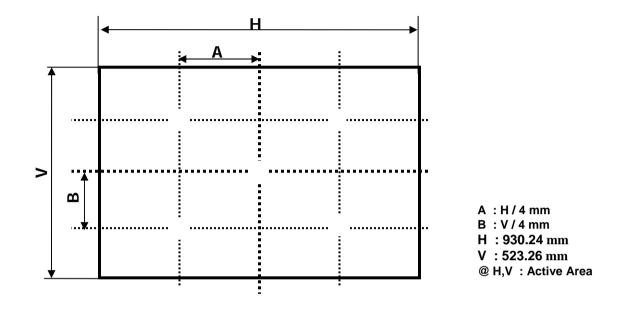


FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".

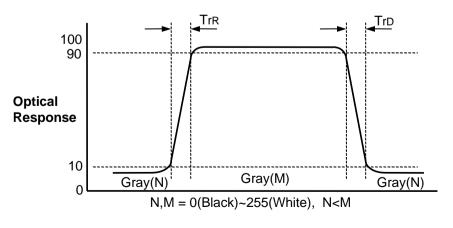


FIG. 3 Response Time



### Dimension of viewing angle range

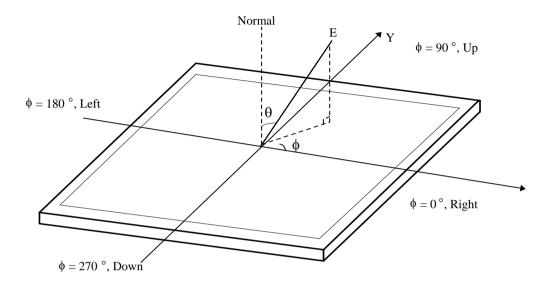


FIG. 4 Viewing Angle

# **5. Mechanical Characteristics**

Table 15 provides general mechanical characteristics.

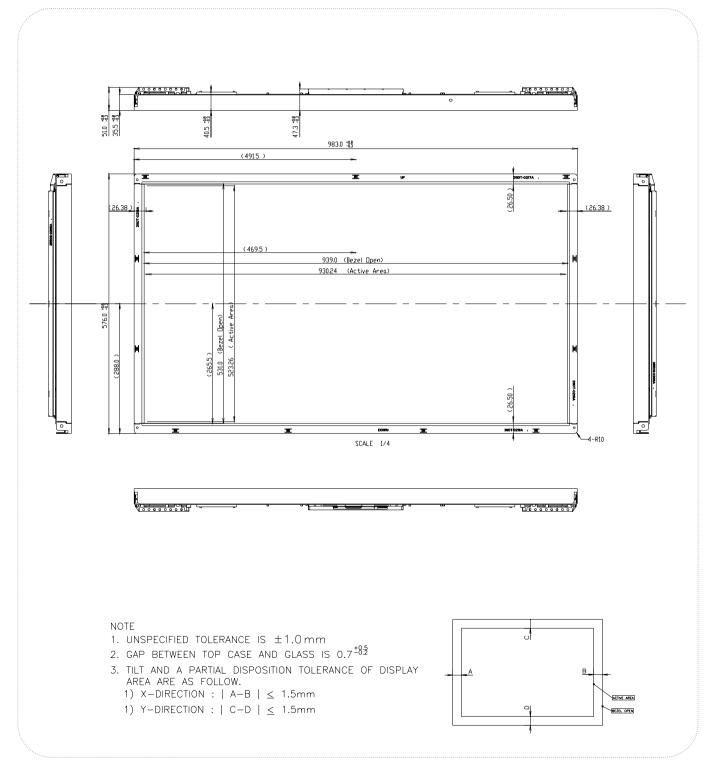
### Table 15. MECHANICAL CHARACTERISTICS

Item	Value				
	Horizontal	983.0 mm			
Outline Dimension	Vertical	576.0 mm			
	Depth	51.0 mm			
Dezel Aree	Horizontal	939.0 mm			
Bezel Area	Vertical	531.0 mm			
Active Dieplay Area	Horizontal	930.24 mm			
Active Display Area	Vertical	523.26 mm			
Weight	14.5Kg (Typ.), 15.5Kg (Max.)				
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer				

Note : Please refer to page22 and 23 for mechanic drawings in terms of tolerance.

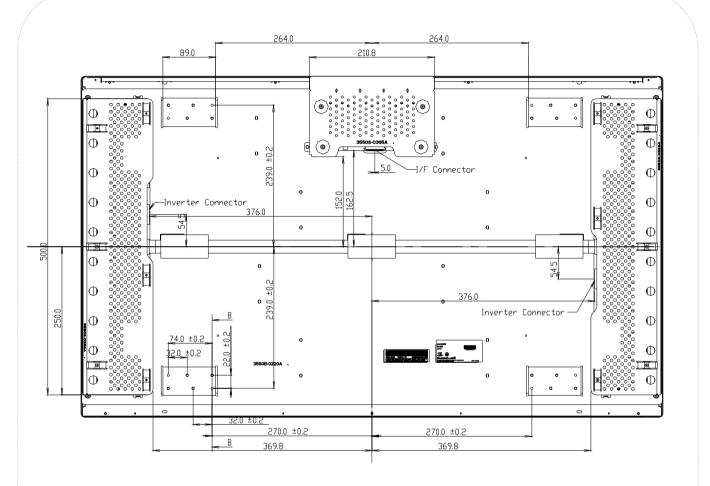


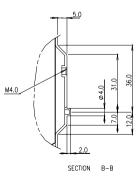
### <FRONT VIEW>





## <REAR VIEW>





# 6. Reliability

# Table 16. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition			
1	High temperature storage test	Ta= 50°C 240h			
2	Low temperature storage test	Ta= -20°C 240h			
3	High temperature operation test	Ta= 40°C 50%RH 240h			
4	Low temperature operation test	Ta= 0°C 240h			
5	Vibration test (operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, 30 min One time each direction			
6	Shock test (operating)	Shock level : 50Grms Waveform : half sine wave, 11ms Direction : ±X, ±Y, ±Z One time each direction			
7	Humidity condition Operation	Ta= 40 °C ,90%RH			
8	Altitude operating storage / shipment	0 - 14,000 feet(4267.2m) 0 - 40,000 feet(12192m)			



# 7. International Standards

### 7-1. Safety

a) UL 60065, 7<sup>th</sup> Edition, dated June 30, 2003, Underwriters Laboratories, Inc., Standard for Audio, Video and Similar Electronic Apparatus.
b) CAN/CSA C22.2, No. 60065:03, Canadian Standards Association, Standard for Audio, Video and Similar Electronic Apparatus.
c) IEC60065:2001, 7<sup>th</sup> Edition CB-scheme and EN 60065:2002, Safety requirements for Audio, Video and Similar Electronic Apparatus..

### 7-2. EMC

a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992

b) CISPR22 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." International Special Committee on Radio Interference.

c) EN 55022 "Limits and Methods of Measurement of Radio Interface Characteristics of Information Technology Equipment." European Committee for Electrotechnical Standardization.(CENELEC), 1998 (Including A1: 2000)



# 8. Packing

# 8-1. Designation of Lot Mark

a) Lot Mark



A,B,C : SIZE(INCH)

- D : YEAR
- F : PANEL CODE
- H : ASSEMBLY CODE

E : MONTH G : FACTORY CODE I,J,K,L,M : SERIAL NO.

Note

#### 1. YEAR

Year	97	98	99	2000	2001	2002	2003	2004	2005	2006	2007
Mark	7	8	9	0	1	2	3	4	5	6	7

### 2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	А	В	С

3. PANEL CODE

Panel Code	P1 Factory	P2 Factory	P3 Factory	P4 Factory	P5 Factory	Hydis Panel
Mark	1	2	3	4	5	Н

4. FACTORY CODE

Factory Code	LPL Gumi	LPL Nanjing	Hee Sung
Mark	К	С	D

5. SERIAL NO.

Year	1 ~ 99999	100000 ~
Mark	00001 ~ 99999	A0001 ~ A9999, , Z9999

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

# 8-2. Packing Form

a) Package quantity in one Pallet : 12 pcs

b) Pallet Size : 1150 mm X 1000 mm X 820 mm.

# 9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

# 9-1. Mounting Precautions

- (1) You must mount a module using holes arranged in four corners or four sides.
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.

Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)

- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

# 9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :
  - V=±200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw. (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.



# 9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

### 9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

### 9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.It is recommended that they be stored in the container in which they were shipped.

### 9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.