

# SPECIFICATION FOR APPROVAL

( ) Preliminary Specification

( • ) Final Specification

Title

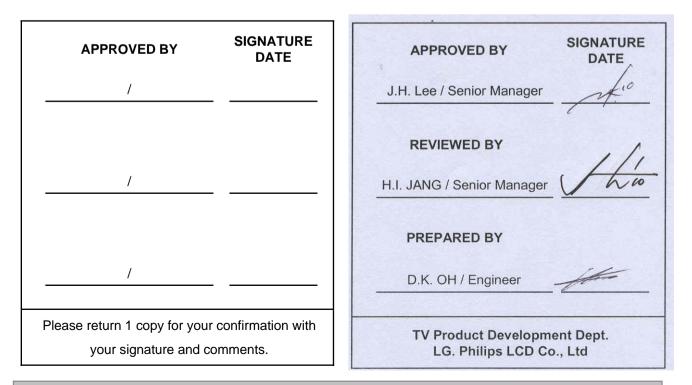
This Product must be used for a TV Application This is not designed for the public display.

## 32.0" WXGA TFT LCD

BUYER	
MODEL	

SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LC320WXN
SUFFIX	SAC1 (RoHS Verified)

\*When you obtain standard approval, please use the above model name without suffix



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## **RECORD OF REVISIONS**

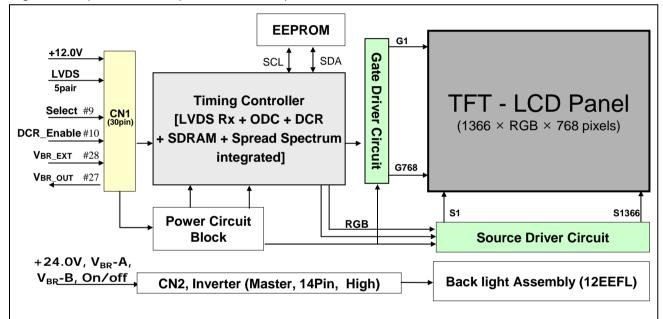
Revision No.	Revision Date	Page	Description
0.1	Sept, 05, 2007	-	Preliminary Specification(First Draft)
0.2	Oct, 01, 2007	-	Second Draft
0.3	Oct, 17, 2007	-	Third Draft
0.4	Oct, 30, 2007	-	Fourth Draft
0.5	Nov, 21, 2007	-	Fifth Draft
1.0	Dec, 03, 2007	-	Final Specification Table
1.1	Mar, 10, 2008	13	Change of Power Sequence T2 & T7. (This Sequence is permitted temporarily in this model.)

### **1. General Description**

The LC320WXN is a Color Active Matrix Liquid Crystal Display with an integral External Electrode Fluorescent Lamp(EEFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive type display operating in the normally black mode. It has a 31.51 inch diagonally measured active display area with WXGA resolution (768 vertical by 1366 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus presenting a palette of more than 16.7M(true) colors.

It has been designed to apply the 8-bit 1-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



#### **General Features**

Active Screen Size	31.51 inches(800.4mm) diagonal
Outline Dimension	760.0 mm(H) x 450.0 mm(V) x 48.0 mm(D) (Typ.)
Pixel Pitch	170.25/ x 510.75/ x RGB
Pixel Format	1366 horiz. by 768 vert. pixels RGB stripe arrangement
Color Depth	8bit, 16,7 M colors
Luminance, White	500 cd/m <sup>2</sup> (Center 1 point) (Typ.)
Viewing Angle (CR>10)	Viewing angle free (R/L 178(Typ.), U/D 178(Typ.))
Power Consumption	Total 87.5Watt (Typ.) (Logic=3.5 W, Lamp= 84W [VBR-A=1.65V] )
Weight	6,150g(Typ.)
Display Operating Mode	Transmissive mode, normally black
Surface Treatment	Hard coating(3H), anti-glare treatment of the front polarizer (Haze 13%)

#### 2. Absolute Maximum Ratings

The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

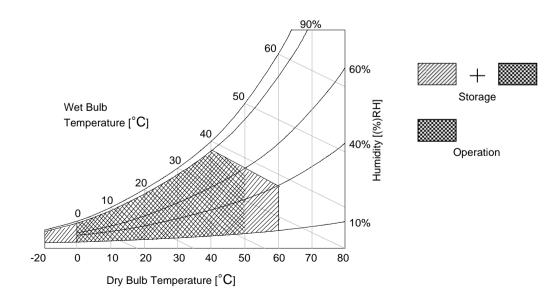
#### Table 1. ABSOLUTE MAXIMUM RATINGS

De	arameter	Symbol	Va	lue	Linit	Remark			
Γc	lameter	Symbol	Min	Max	Unit	Remark			
Power Input	LCM	VLCD	-0.3	+14.0	Vdc	at 25 $\pm$ 2 °C			
Voltage	Backlight inverter	VBL	-0.3	+27.0	VDC				
ON/OFF Control Voltage		V <sub>ON/OFF</sub>	-0.3	+5.5	Vdc				
Brightness Co	ontrol Voltage	Vbr	0	+5.0	VDC				
Operating Ter	mperature	Тор	0	+50	°C				
Storage Tem	perature	Тѕт	-20	+60	°C	Note 1.2			
Operating Ambient Humidity		Нор	10	90	%RH	Note 1,2			
Storage Hum	dity	Нsт	10	90	%RH				

Notes : 1. Temperature and relative humidity range are shown in the figure below.

Wet bulb temperature should be 39 °C Max. and no condensation of water.

2. Gravity mura can be guaranteed under 40  $^\circ\!C$  condition.



### 3. Electrical Specifications

#### **3-1. Electrical Characteristics**

It requires two power inputs. One is employed to power for the LCD circuit.

The other Is used for the EEFL backlight and inverter circuit.

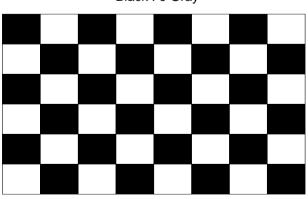
#### Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol		Value	Unit	Note	
	Cymbol	Min	Тур	Max	Ölik	11010
Circuit :						
Power Input Voltage	V <sub>LCD</sub>	11.4	12.0	12.6	V <sub>DC</sub>	
Power Input Current		-	295	384	mA	1
Power Input Current	LCD	-	380	494	mA	2
Power Consumption	P <sub>LCD</sub>	-	3.5	4.6	Watt	1
Rush current	I <sub>RUSH</sub>	-	-	3.0	А	3

Notes : 1. The specified current and power consumption are under the V<sub>LCD</sub>=12.0V,  $25 \pm 2^{\circ}$ C, f<sub>V</sub>=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f<sub>V</sub> is the frame frequency.

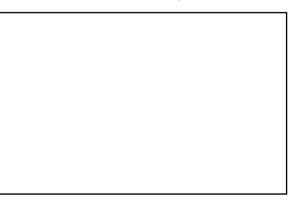
2. The current is specified at full white pattern.

3. The duration of rush current is about 2ms and rising time of power input is 1ms (min.).



White : 255 Gray Black : 0 Gray

White : 255 Gray



Mosaic Pattern(8 x 6)

Full White pattern

#### Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Dor	omotor		Symbol		Values		Unit	Notoo
Par	ameter		Symbol	Min	Тур	Max	Unit	Notes
Inverter :								
Power Supply Input	VBL	22.8	24.0	25.2	Vdc	1		
Power Supply Input	Voltage Rip	ple		-	-	0.5	Vp-p	1
	IBL_A	-	3.5	3.85	А	V <sub>BR-A</sub> = 1.65V 1		
Power Supply	After Aging		IDL_A	-	3.6	4.0	A	V <sub>BR-A</sub> = 3.3V 1
Input Current	Before Agir	na	IBL_B	-	4.0	4.5	А	V <sub>BR-A</sub> = 1.65V 2
	Derore Agir	ig		-	4.5	5.0	А	V <sub>BR-A</sub> = 3.3V 2
Power Supply Input	Power Supply Input Current (In-Rush)			-	-	6.3	A	$V_{BL} = 22.8V$ $V_{BR-B} = 3.3V$ $V_{BR-A} = 1.65V$
Power Consumption	า		PBL	-	84	92.4	W	V <sub>BR-A</sub> = 1.65V 1
	Brightness	Adjust	Vbr-a	0.0	1.65	3.3	Vdc	
Input Voltage for Control System	On/Off	On	V on	2.5	-	5.0	Vdc	
Signals	01/01	Off	V off	-0.3	0.0	0.8	Vdc	
	Brightness	Adjust	Vbr-b	0	-	3.3	V	3
Lamp:								
Life Time				50,000			Hrs	4

Notes :

1. Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 120 minutes at 25±2°C. The specified current and power consumption are under the typical supply Input voltage 24Vand VBR (VBR-A : 1.65V & VBR-B :3.3V), it is total power consumption.

The ripple voltage of the power supply input voltage is under 0.5 Vp-p. LPL recommend Input Voltage is 24.0V  $\pm$  5%.

2. Electrical characteristics are determined within 30 minutes at 25±2°C. The specified currents are under the typical supply Input voltage 24V.

3. The brightness of the lamp after lighted for 5minutes is defined as 100%.

TS is the time required for the brightness of the center of the lamp to be not less than 95% at typical current. The screen of LCD module may be partially dark by the time the brightness of lamp is stable after turn on. 4. Specified Values are for a single lamp which is aligned horizontally.

- The life time is determined as the time which luminance of the lamp is 50% compared to that of initial value at the typical lamp current (VBR-A : 1.65V & VBR-B :3.3V), on condition of continuous operating at 25± 2°C
- 5. The duration of rush current is about 20 ms.

#### **3-2. Interface Connections**

This LCD module employs two kinds of interface connection, a 30-pin connector is used for the module electronics and One connectors(14-pin) are used for the integral backlight system.

#### 3-2-1. LCD Module

- LCD Connector(CN1) : FI-X30SSL-HF (Manufactured by JAE) or Equivalent
- Mating Connector : FI-X30C2L (Manufactured by JAE) or Equivalent

#### Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No.	Symbol	Description	Note
1	VLCD	Power Supply +12.0V	
2	VLCD	Power Supply +12.0V	
3	VLCD	Power Supply +12.0V	
4	VLCD	Power Supply +12.0V	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
7	GND	Ground	
9	Select	Select LVDS Data format	1
10	DCR Enable	Dynamic CR Enable ( 'L ' = Disable , 'H' = Enable )	2
11	GND	Ground	
12	RA-	LVDS Receiver Signal(-)	
13	RA+	LVDS Receiver Signal(+)	
14	GND	Ground	
15	RB-	LVDS Receiver Signal(-)	
16	RB+	LVDS Receiver Signal(+)	
17	GND	Ground	
18	RC-	LVDS Receiver Signal(-)	
19	RC+	LVDS Receiver Signal(+)	
20	GND	Ground	
21	RCLK-	LVDS Receiver Clock Signal(-)	
22	RCLK+	LVDS Receiver Clock Signal(+)	
23	GND	Ground	
24	RD-	LVDS Receiver Signal(-)	
25	RD+	LVDS Receiver Signal(+)	
26	GND	Ground	
27	VBR_OUT	VBR output form LCD module	
28	VBR_EXT	External VBR input from System to LCD module	
20	Deserved	Low : Normal Operating	
29	Reserved	High : Interlace Free Mode	
30	GND	Ground	3

Notes: 1. The pin no 9 is an option pin for DISM or LG format. (VESA Format = "GND" / JEIDA Format = "VCC") Please refer to Appendix for further details.

- 2. The pin no 10 is an option pin for DCR Function (Enable = "VCC" / Disable = "GND")
- 3. The pin no 30 is LCD Test option.

LCM operates "AGP" (Auto Generation Pattern) or "NSB" (No Signal Black) is case that LVDS signals are out of frequency or abnormal condition in spite of 12 volt power supply.

- LPL recommends "NSB". (AGP : "VCC" or "OPEN" / NSB : "GND")
- 4. All GND (ground) pins should be connected together, which should be also connected to the LCD module's metal frame.
- 5. All VLCD (power input) pins should be connected together.
- 6. Input Levels of LVDS signals are based on the IEA 664 Standard.

#### 3-2-2. Backlight Inverter

Master

-Inverter Connector : 20022WR-14B1

(manufactured by Yeon-Ho) or Equivalent

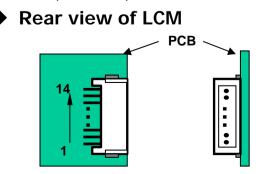
- Mating Connector : PHR-14 or Equivalent

Pin No	Symbol	Description	Master	Note
1	VBL	Power Supply +24.0V	VBL	
2	VBL	Power Supply +24.0V	VBL	
3	VBL	Power Supply +24.0V	VBL	
4	VBL	Power Supply +24.0V	VBL	
5	VBL	Power Supply +24.0V	VBL	
6	GND	Backlight Ground	GND	
7	GND	Backlight Ground	GND	
8	GND	Backlight Ground	GND	1
9	GND	Backlight Ground	GND	
10	GND	Backlight Ground	GND	
11	Vbr-A	Analog dimming voltage DC 0.0V ~ 3.3V (Typ : 1.65V)	Vbr-a	2, 3
12	VON/OFF	0.0V ~ 5.0V	On/Off	
13	Vbr-B	Burst dimming voltage DC 0.0V ~ 3.3V	VBR-B	3
14	Status	Normal : Upper 3.0V Abnormal : Under 0.7V	Status	4

#### Table 7. INVERTER CONNECTOR PIN CONFIGULATION

Notes : 1. GND should be connected to the LCD module's metal frame.

- If Pin #11 is open, VBR-A = 1.65V. When apply over 1.65V( ~ 3.3V) continuously, its luminance is increasing however lamp's life time is decreasing.
   It could be usable for boost up luminance when using DCR (=Dynamic contrast ratio) function only.
- 3. Minimum Brightness : VBR-B = 0V Maximum Brightness : VBR-B = 3.3V
- 4. Even though Pin #14 is open, there is no effect on inverter operating, The output terminal of inverter.
- 5. Each impedance of pin #11,12 and 13 is  $147[k\Omega]$ ,  $38[k\Omega]$ ,  $118[k\Omega]$



### 3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

Table 6. TIMING TABLE for NTSC & PAL

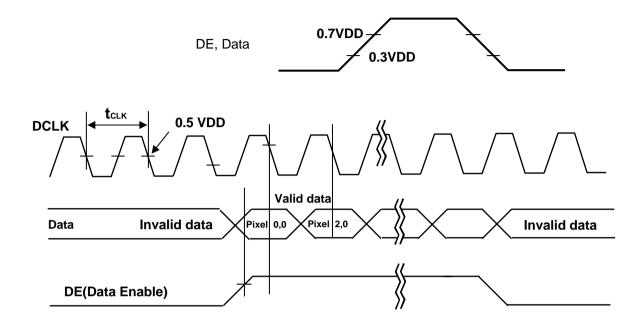
#### [ DE (Data Enable) Only ]

ITEM	Symbol		Min	Тур	Мах	Unit	Note
DOLK	Period		12.5	13.8	15.8	ns	
DCLK	Frequency	-	63	72.4	80	MHz	
	Period	tнт	1456	1528	1920	<b>t</b> CLK	
	Horizontal Valid	tH∨	1366	1366	1366	tCLK	
	Horizontal Blank	-	tHP- tH∨	162	tHP- tHV		
Hsync	Frequency	fн	45	47.4	50	KHz	
	Width	twн	-	32	-	tCLK	
	Horizontal Back Porch	tнвр	24	50	-		
	Horizontal Front Porch	tHFP	40	80	-		
	Period	t∨⊤	776	790	1063	tHP	
	Vertical Valid	t∨∨	768	768	768	tHP	
	Vertical Blank	-	tvp-tvv	22	tvp-tvv	tHP	
Vsync	Frequency	f∨	47	60	63	Hz	Note 1) PAL : 47~53Hz
	Width	tw∨	-	5	-	tHP	NTSC : 57~63Hz
	Vertical Back Porch	tvвр	5	15	-	Hz	
	Vertical Front Porch	tVFP	1	2	-	tHP	

Note :

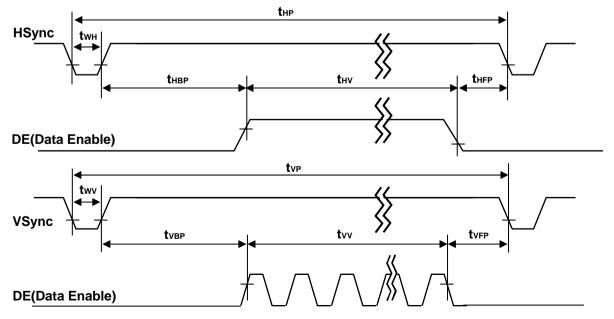
- 1. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.
- 2. Above Timing Tables are only valid for DE Mode.

## 3-4. Signal Timing Waveforms



### \* Reference : Sync. Relation

 $t_{VB} = t_{VFP} + t_{WV} + t_{VBP}$ 



<sup>\*</sup> the = thee + twh +thee

#### 3-5. Color Data Reference

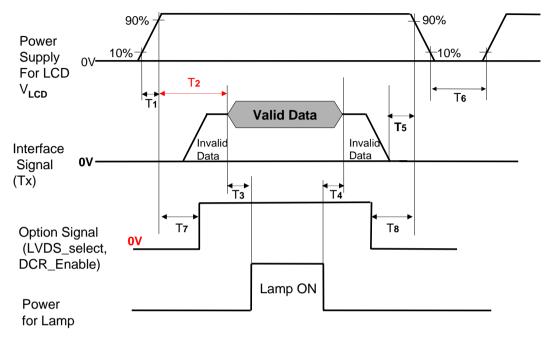
The brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

#### Table 7. COLOR DATA REFERENCE

													Inpu	ut Co	olor	Data	а									
	Color					RE	D				GREEN							BLUE								
			MS								MS							SB	_							SB
			R7	R6	R5	R4	R3	R2	R1 F	R0	G7	G6	G5	G4	G3	G2	G1	G0		B6	B5	B4	B3	B2	B1	B0
	Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White		1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																										
	RED (254)		1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)		1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																										
	GREEN (254)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000)	Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																										
	BLUE (254)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

#### **3-6. Power Sequence**

#### 3-6-1. LCD Driving circuit

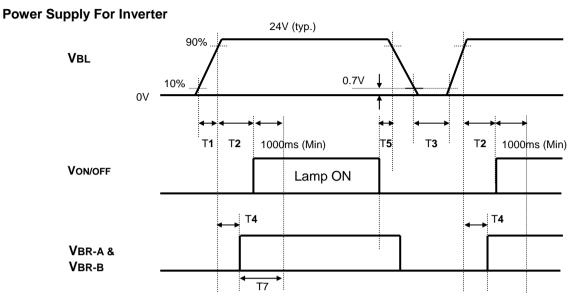


#### Table 9. POWER SEQUENCE

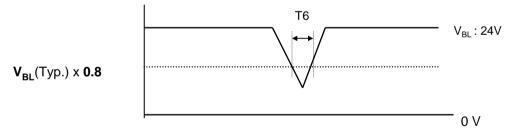
Deremeter		l la it	Natas		
Parameter	Min	Min Typ Max		Unit	Notes
T1	0.5	-	20	ms	
T2	2	-	30	ms	4
Т3	200	-	-	ms	3
T4	200	-	-	ms	3
T5	0	-	-	ms	
T6	2.0	-	-	S	5
T7	2	-	30	ms	4
Т8	0	-	-	ms	

- Note: 1. Please avoid floating state of interface signal at invalid period.
  - 2. When the interface signal is invalid, be sure to pull down the power supply  $V_{LCD}$  to 0V.
    - 3. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
    - 4. This Sequence is permitted temporarily in this model.
    - 5. T6 should be measured after the Module has been fully discharged between power off and on period.

#### 3-6-2. Sequence for Inverter



#### **3-6-3.** Deep condition for Inverter



#### Table 12. Power Sequence for Inverter

Doromotor		Values		Units	Remarks
Parameter	Min	Тур	Max	Units	Remaiks
T1	20	-	-	ms	1
T2	500	-	-	ms	
Т3	200	-	-	ms	
T4	0		-	ms	2
T5	10	-	-	ms	
Т6	-	-	10	ms	<b>V<sub>BL</sub></b> (Тур) х <b>0.8</b>
T7	1000	-	-	ms	3

Note : 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time 2. T4(max) is less than T2.

3. In T7 section,  $V_{BR}\text{-}B$  should be max level(3.3V) and  $~V_{BR}\text{-}A$  should be 1.65V.

### 4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and for 30 minutes in a dark environment at  $25\pm2^{\circ}$ C. The values are specified at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to 0 °.

FIG. 1 shows additional information concerning the measurement equipment and method.

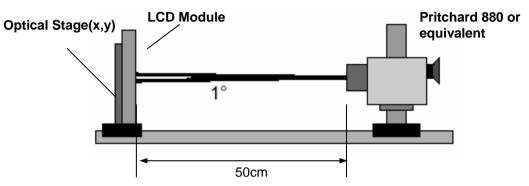


FIG. 1 Optical Characteristic Measurement Equipment and Method

#### Table 10. OPTICAL CHARACTERISTICS

Ta= 25±2°C, V<sub>LCD</sub>=12.0V, fv=60Hz, Dclk=72.4MHz, VBR-A =1.65V, VBR-B =3.3V

David		Symbol		Value		11.1	Nata	
Para	Parameter		Min	Тур	Max	Unit	Note	
Contrast Ratio (T	BD)	CR	800	1100	-		1	
Surface Luminan	ce, white	L <sub>WH</sub>	400	500		cd/m <sup>2</sup>	2	
Luminance Varia	tion	δ <sub>WHITE</sub> 5P	-	-	1.3		3	
Response Time		G to G	-	8	12	ms	4	
	DED	Rx		0.636				
	RED	Ry		0.335				
	ODEEN	Gx		0.291				
Color Coordinates	GREEN	Gy	Тур	0.613	Тур			
[CIE1931]	BLUE	Bx	-0.03	0.146	+0.03			
	BLUE	Ву		0.061				
	WHITE	Wx		0.279				
	VVIIIE	Wy		0.292				
Viewing Angle (C	R>10)							
x a:	x axis, right( <sub>\$=0°</sub> )		89	-	-			
x axis, left ( $\phi$ =180°)		θI	89	-	-		_	
y a	kis, up (φ=90°)	θu	89	-	-	degree	5	
y a:	kis, down (φ=270°)	θd	89	-	-			
Gray Scale				2.2			6	

Notes :1. Contrast Ratio(CR) is defined mathematically as :

- CR(Contrast Ratio) = Maximum CRn (n=1, 2, 3, 4, 5)
- $CRn = \frac{Surface Luminance at position n with all white pixels}{2}$ 
  - Surface Luminance at position n with all black pixels
  - n = the Position number(1, 2, 3, 4, 5). For more information, see FIG 2.
- Surface luminance are determined after the unit has been 'ON' and 30min after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance ,  $\delta$  WHITE is defined as :  $\delta$  WHITE(5P) = Maximum(L<sub>on1</sub>,L<sub>on2</sub>, L<sub>on3</sub>, L<sub>on4</sub>, L<sub>on5</sub>) / Minimum(L<sub>on1</sub>,L<sub>on2</sub>, L<sub>on3</sub>, L<sub>on4</sub>, L<sub>on5</sub>)

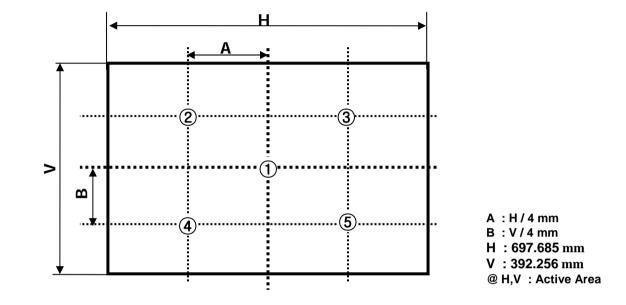
Where  $L_{on1}$  to  $L_{on5}$  are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.

- 4. Response time is the time required for the display to transition from G(N) to G(M) (Rise Time, Tr<sub>R</sub>) and from G(M) to G(N) (Decay Time, Tr<sub>D</sub>). For additional information see the FIG. 3. (N<M)</p>
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 6. Gray scale specification

Gamma Value is approximately 2.2. For more information, see the Table 11.

Gray Level	Luminance [%] (Typ.)
LO	0.10
L15	0.32
L31	1.10
L47	2.60
L63	4.90
L79	8.10
L95	12.1
L111	16.7
L127	21.6
L143	28.0
L159	35.4
L175	43.9
L191	53.3
L207	64.1
L223	75.8
L239	88.0
L255	100

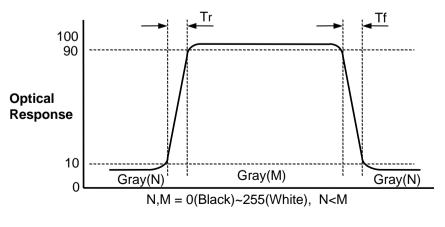
#### Table 11. GRAY SCALE SPECIFICATION



Measuring point for surface luminance & measuring point for luminance variation.

FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Gray(M)".





#### Dimension of viewing angle range

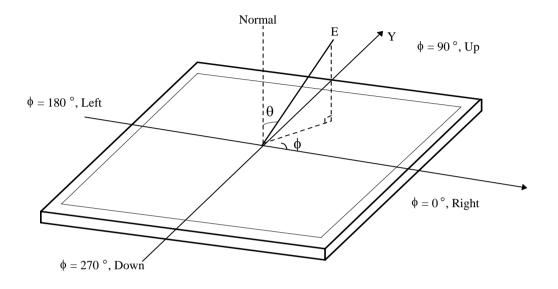


FIG. 4 Viewing Angle

### 5. Mechanical Characteristics

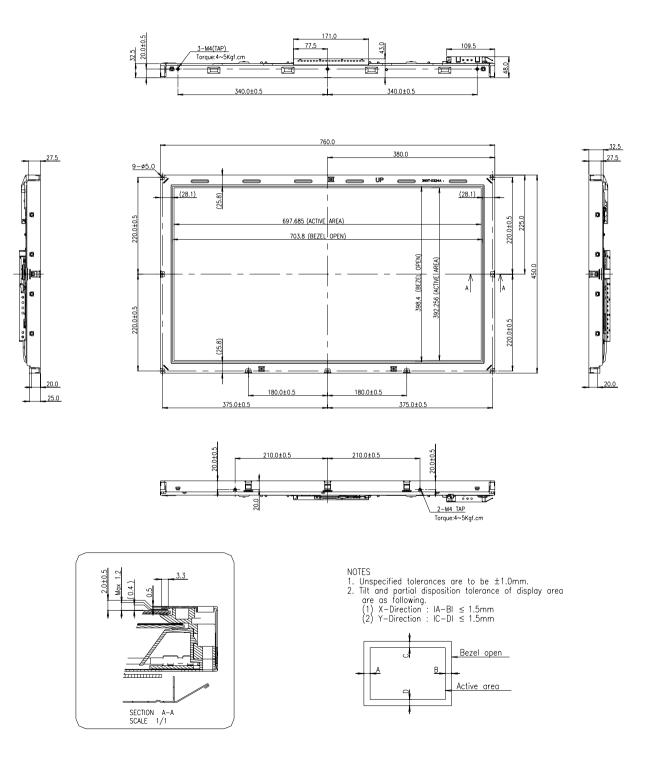
Table 12 provides general mechanical characteristics.

Table 12.	MECHANICAL	<b>CHARACTERISTICS</b>
-----------	------------	------------------------

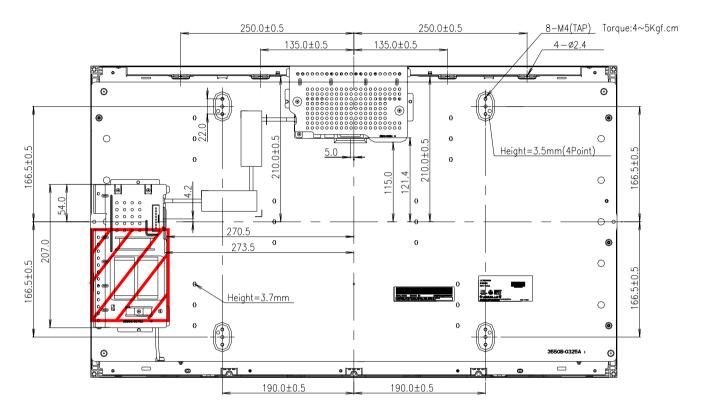
Item		Value	
	Horizontal	760.0mm	
Outline Dimension	Vertical	450.0 mm	
	Depth	48.0 mm	
Dezel Aree	Horizontal	703.8mm	
Bezel Area	Vertical	398.4mm	
Active Display Area	Horizontal	697.685mm	
Active Display Area	Vertical	392.256mm	
Weight	6,150 g(Typ.), 6,770g(Max)		
Surface Treatment	Hard coating(3H) Anti-glare treatment of the front polarizer(13%		

Note : 1.Please refer to a mechanic drawing in terms of tolerance at the next page.

#### <FRONT VIEW>



#### <REAR VIEW>



Notes : It should be recommended that any exterior materials do not go passing up the red area slanted. (For example, electrical cable, system board, etc). Otherwise, it could cause that abnormal display happens.

### 6. Reliability

### Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition			
1	High temperature storage test	Ta= 60°C 240h			
2	Low temperature storage test	Ta= -20°C 240h			
3	High temperature operation test	Ta= 50°C 50%RH 240h			
4	Low temperature operation test	Ta= 0°C 240h			
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0G RMS Bandwidth : 10-300Hz Duration : X,Y,Z, 10 min One time each direction			
6	Shock test (non-operating)	Shock level : 100G Waveform : half sine wave, 2ms Direction : $\pm X$ , $\pm Y$ , $\pm Z$ One time each direction			
7	ESD test	Condition : 150pF, 330 ohm Case , air Evaluation : ± 15kV			
8	Humidity condition Operation	Ta= 40 °C 90%RH 240h			
9	Altitude operating storage / shipment	0 - 14,000 feet(4267.2m) 0 - 40,000 feet(12192m)			

Note : Before and after Reliability test, LCM should be operated with normal function.

#### 7. International Standards

#### 7-1. Safety

a) UL 60065, 7<sup>th</sup> Edition, dated June 30, 2003, Underwriters Laboratories, Inc., Standard for Audio, Video and Similar Electronic Apparatus.
b) CAN/CSA C22.2, No. 60065:03, Canadian Standards Association, Standard for Audio, Video and Similar Electronic Apparatus.
c) IEC60065:2001, 7<sup>th</sup> Edition CB-scheme and EN 60065:2002, Safety requirements for Audio, Video and Similar Electronic Apparatus..

#### 7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) CISPR13 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
   CISPR22 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" International Special Committee on Radio Interference.
- c) EN55013 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
   EN55022 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" European Committee for Electro Technical Standardization.(CENELEC), 1988(Including A1:2000)

### 8. Packing

### 8-1. Designation of Lot Mark





A,B,C : SIZE(INCH) E : MONTH

D : YEAR F ~ M : SERIAL NO.

Note

1. YEAR

Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

2. MONTH

[	Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
	Mark	1	2	3	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial No. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

### 8-2. Packing Form

a) LCM quantity in one pallet : 12 pcs

b) Pallet Size : 1030mm X 870mm X 740mm

### 9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

### 9-1. Mounting Precautions

- (1)You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer.
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

#### 9-2. Operating Precautions

- (1) The spike noise causes the mis-operation of circuits. It should be lower than following voltage :  $V=\pm 200 mV(Over and under shoot voltage)$
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.) And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer.
- (4) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
  - (if not, it causes metallic foreign material and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) It is recommended to avoid the signal cable and conductive material over the inverter transformer for it can cause the abnormal display and temperature rising.
- (11) Partial darkness may happen during 3~5 minutes when LCM is operated initially in condition that luminance is under 40% at low temperature (under 5°C). This phenomenon which disappears naturally after 3~5 minutes is not a problem about reliability but LCD characteristic.

### 9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

### 9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

### 9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.It is recommended that they be stored in the container in which they were shipped.

### 9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normalhexane.

#### # APPENDIX-I-1

Required signal assignment for Flat Link (DS90C385) Transmitter(Pin9="L")

Host System	D	S90C385				Timing
24 Bit	or	Compatible	IS10	0-L30E	B-C23	Controller
RED0	51				1	
RED1	52	TxOUT0-	48	12	~ >	RxIN0-
RED2	54	TxOUT0+	47	13	100Ω ≶	RxIN0+
RED3	55					
RED4	56					
RED5	3	TxOUT1-	46	15		RxIN1-
RED6	50	TxOUT1+	45	16	100Ω ≶	RxIN1+
RED7	2					
GREEN0	4		10			
GREEN1	6	TxOUT2-	42	18	4000 <	RxIN2-
GREEN2	7	TxOUT2+	41	19	100요 ≶	RxIN2+
GREEN3	11					
GREEN4	12					
GREEN5	14	TxCLKOUT-	40	21	4000	RxCLKIN-
GREEN6	8	TxCLKOUT+	39	22	100요 ≶	RxCLKIN+
GREEN7	10					
BLUE0	15					
BLUE1	19	TxOUT3-	38	24	4000 <	RxIN3-
BLUE2	20	TxOUT3+	37	25	100Ω ≶	RxIN3+
BLUE3	22					
BLUE4	23			9		
BLUE5	24			30		LCD Test
BLUE6	16					
BLUE7	18					
Hsync	27					
Vsync	28		ରୁ ଜ	I		
Data Enable			GND		LCD M	adula
СГОСК	31					loaule

Notes:

- 1. The LCD module uses a 100  $Ohm(\Omega)$  resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

#### # APPENDIX-I-2

Required signal assignment for Flat Link (DS90C385) Transmitter(Pin9="H")

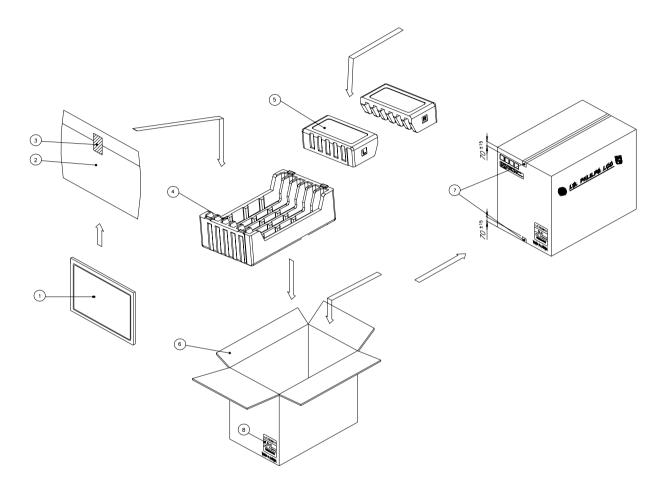
Host System	1	DS90C385				Timing
24 Bit	or	Compatible	IS10	0-L30E	3-C23	Controller
RED0	50		10		I	
RED1	2	TxOUT0-	48	12	100Ω ≶	RxIN0-
RED2	51	TxOUT0+	47	13	10052 5	RxIN0+
RED3	52					
RED4	54		40			
RED5	55	TxOUT1-	46	15	100Ω ≷	RxIN1-
RED6	56	TxOUT1+	45	16	10032 5	RxIN1+
RED7	3					
GREEN0			42			
GREEN1	10	TxOUT2-	42	18	100Ω ≷	RxIN2-
GREEN2	4	TxOUT2+	41	19	10052 5	RxIN2+
GREEN3	6					
GREEN4	7		4.0			
GREEN5	11	TxCLKOUT-	40	21	4000 \$	RxCLKIN-
GREEN6	12	TxCLKOUT+	39	22	100Ω ≶	RxCLKIN+
GREEN7	14					
BLUE0	16					
BLUE1	18	TxOUT3-	38	24	4000 2	RxIN3-
BLUE2	15	TxOUT3+	37	25	100Ω ≶	RxIN3+
BLUE3	19					
BLUE4	20			9		
BLUE5	22			30		LCD Test
BLUE6	23					
BLUE7	24					
Hsync	27					
Vsync	28		< 0	I		
Data Enable			GND Vcc			
CLOCK	31		-		LCD M	odule

Notes:

- 1. The LCD module uses a 100  $Ohm(\Omega)$  resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

### # APPENDIX- ||-1

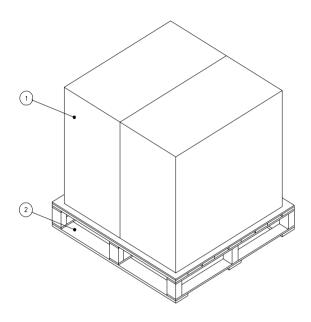
■ LC320WXN—SAC1 Packing Ass'y

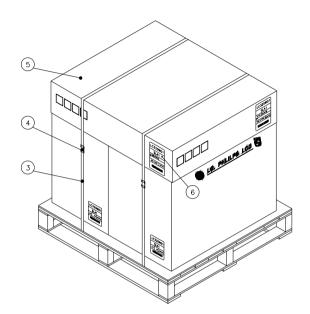


NO.	DESCRIPTION	MATERIAL
1	LCD MODULE	
2	BAG	AL
3	TAPE	MASKING 20MM X 50M
4	PACKING, BOTTOM	EPS
5	PACKING, TOP R_L	EPS
6	BOX	PAPER_DW3
7	TAPE	OPP 70MMX300M
8	LABEL	YUPO PAPER 100X100

### # APPENDIX- ||-2

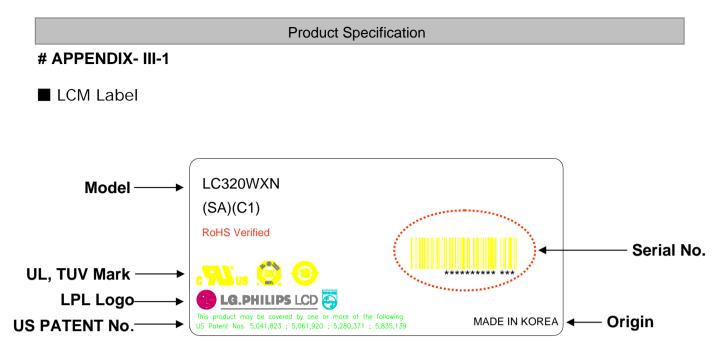
■ LC320WXN-SAC1 Pallet Ass'y



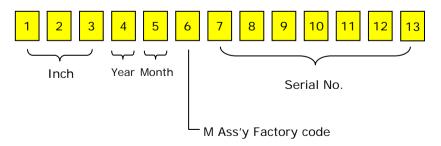


Box quantity per pallet: 2ea Pallet size: L1030 x W870 x H740 Pallet gross weight: 113.0kg

NO.	DESCRIPTION	MATERIAL		
1	PACKING ASS'Y			
2	PALLET	Plywood		
3	BAND	PP		
4	CLIP, BAND	STEEL		
5	ANGLE, Cover	PAPER (DW3)		
6	LABEL	PAPER		



### Serial No. (See CAS 26page for more information)



### # APPENDIX- III-2

### Box Label

LC320WXN							
SAC1							
6 PCS	001/01-01						
MADE IN	RoHS Verifie	d					

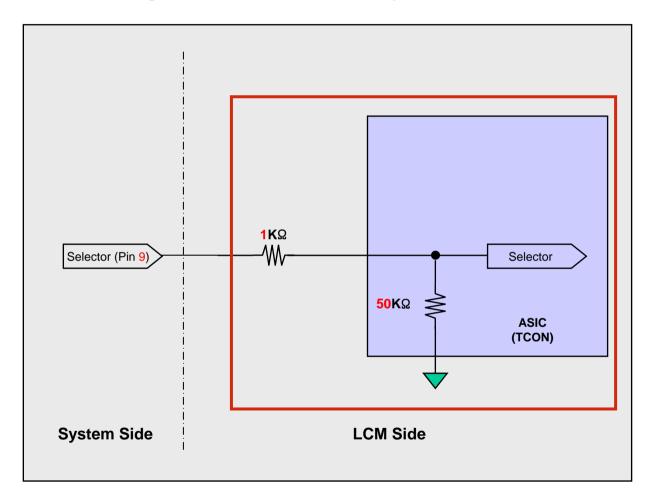
### Pallet Label

LC320WXN					
SAC1					
12 PCS	001/01-01				
MADE I	RoHS Verified				

### **# APPENDIX- IV**

Option Pin Circuit Block Diagram

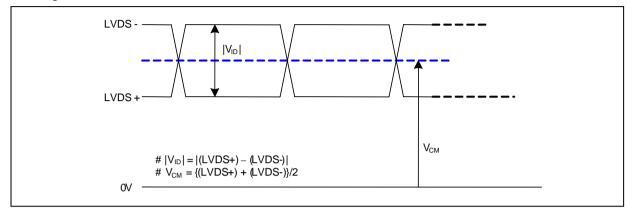
### **Circuit Block Diagram of LVDS Format Selection pin**



### **# APPENDIX- X**

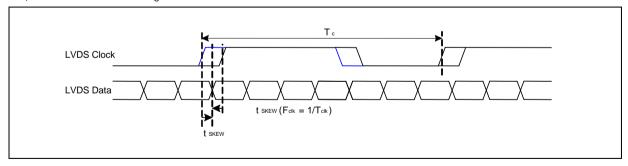
**LVDS** Input characteristics

### 1. DC Specification



Description	Symbol	Min	Max	Unit	Notes
LVDS Differential Voltage	V <sub>ID</sub>	200	500	mV	-
LVDS Common mode Voltage	V <sub>CM</sub>	1.0	1.5	V	-

### 2. AC Specification

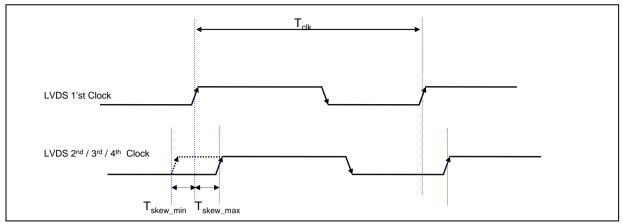


1) LVDS Clock to data timing

Description	Symbol	Min	Мах	Unit	Notes
LVDS Clock to Data Skew Margin	t <sub>skew</sub>	- (Tc / 7)* 0.2	+(Tc / 7)* 0.2	ps	Note 1

Note> 1. If Tc=13.8ns ,  $t_{SKEW}$  Min= - 394ps Max= + 394ps

### 2) LVDS Clock timing



### < LVDS inter-port Clock timing >

Description	Symbol	Min	Мах	Unit	Notes
LVDS inter-port Clock Skew	T <sub>skew</sub>	-2.5	2.5	ns	-