

# SPECIFICATION FOR APPROVAL

( ) Final Specification

BUYER	
MODEL	

SUPPLIER	LG Display Co., Ltd.		
*MODEL	LC320DXE		
SUFFIX	SFR1 (RoHS Verified)		

APPROVED BY	SIGNATURE DATE			
Please return 1 copy for your	confirmation with			
vour signature and co	mments			

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# **RECORD OF REVISIONS**

Revision Date	Page	Description
Aug, 22, 2012	-	Preliminary Specification (First Draft)
Oct, 31. 2012	5	Table 2. ELECTRICAL CHARACTERISTICS Rush Current Max Changed
	15,16,17	Luminance Variation 9P, Note3, Fig2 Changed
	Aug, 22, 2012	Aug, 22, 2012 - Oct, 31. 2012 5

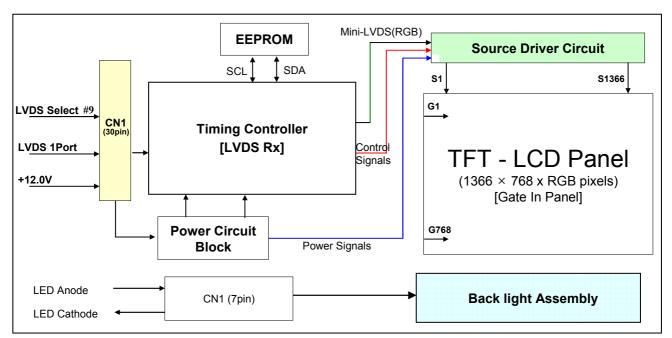
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## 1. General Description

The LC320DXE is a Color Active Matrix Liquid Crystal Display with an integral Light Emitting Diode (LED) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 31.51 inch diagonally measured active display area with WXGA resolution (768 vertical by 1366 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arranged in Horizontal stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot, thus presenting a palette of more than 16.7M(6bit + FRC) colors.

It has been designed to apply the 8-bit 1-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.



#### **General Features**

Active Screen Size	31.51 inches(800.4mm) diagonal
Outline Dimension	725.2 mm(H) x 424.4 mm(V) x 35.0 mm(D) (Typ.)
Pixel Pitch	170.25 / x 510.75 / x RGB
Pixel Format	1366 horiz. by 768 vert. pixels RGB stripe arrangement
Color Depth	8bit(D), 16,7 M colors
Luminance, White	300 cd/m² (Center 1 point) (Typ.)
Viewing Angle (CR>10)	Viewing angle free ( R/L 178(Min.), U/D 178(Min.))
Power Consumption	Total 35.66 W (Typ.) (Logic=3.1 W , LED Driver = 32.56 W [(IF_cathode=400mA)])
Weight	3,800g (Typ.) (TBD)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze < 1%)
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## 2. Absolute Maximum Ratings

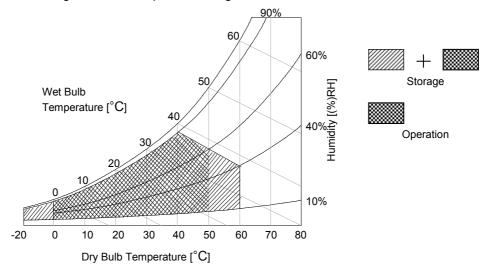
The following items are maximum values which, if exceeded, may cause faulty operation or permanent damage to the LCD module.

Table 1. ABSOLUTE MAXIMUM RATINGS

Parameter		Symbol	Value		Unit	Note
		Symbol	Min	Max	Oilit	NOTE
Power Input Voltage	LCD Circuit	VLCD	-0.3	+14.0	VDC	
LED Input Voltage	Forward Voltage	VF	-	+98	VDC	1
T-Con Option Selection Voltage		VLOGIC	-0.3	+4.0	VDC	
Operating Temperature		Тор	0	+50	°C	2,3
Storage Temperature		Тѕт	-20	+60	°C	2,3
Panel Front Temperature		Tsur	-	+68	°C	4
Operating Ambient Humidity		Нор	10	90	%RH	0.0
Storage Humidity		Нѕт	10	90	%RH	2,3

Notes 1. Ambient temperature condition (Ta =  $25 \pm 2$  °C)

- 2. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be Max 39°C, and no condensation of water.
- 3. Gravity mura can be guaranteed below 40°C condition.
- 4. The maximum operating temperatures is based on the test condition that the surface temperature of display area is less than or equal to 68°C with LCD module alone in a temperature controlled chamber. Thermal management should be considered in final product design to prevent the surface temperature of display area from being over 68°C. The range of operating temperature may be degraded in case of improper thermal management in final product design.



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## 3. Electrical Specifications

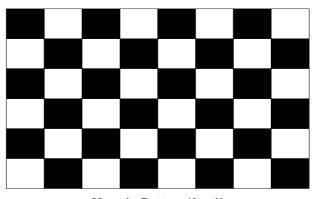
## 3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the LED backlight

Table 2. ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Value			l l=:4	
		Min	Тур	Max	Unit	notes
Circuit :						
Power Input Voltage	VLCD	10.8	12.0	13.2	VDC	
Power Input Current	ILCD	-	255	332	mA	1
		-	320	416	mA	2
Power Consumption	PLCD		3.1	4.0	Watt	1
Rush current	IRUSH	-	-	4.0	А	3

- Notes : 1. The specified current and power consumption are under the  $V_{LCD}$ =12.0V, Ta=25  $\pm$  2°C,  $f_V$ =60Hz condition, and mosaic pattern(8 x 6) is displayed and  $f_V$  is the frame frequency.
  - 2. The current is specified at the maximum current pattern.
  - 3. The duration of rush current is about 2ms and rising time of power input is 0.5ms (min.).
  - 4. Ripple voltage level is recommended under  $\pm 5\%$  of typical voltage



Mosaic Pattern(8 x 6)

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White: 255 Gray Black: 0 Gray

Table 3. ELECTRICAL CHARACTERISTICS (Continue)

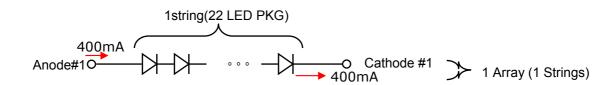
Parameter		Symbol	Values			Unit	Note
		Symbol	Min	Тур	Max	Offic	Note
Backlight Assem	bly :	-					
Forward Current	Anode	I <sub>F (anode)</sub>		400		mAdc	±5%
(one array)	Cathode	I <sub>F (cathode)</sub>	380	400	420	mAdc	2, 3
Forward Voltage	·	V <sub>F</sub>	74.8	81.4	88.0	Vdc	4
Power Consumption	on	P <sub>BL</sub>	-	32.6	35.2	W	5
Burst Dimming Du	ty	On duty	1		100	%	
Burst Dimming Frequency		1/T	95		182	Hz	7
LED Array : (APP	ENDIX-V)	-					
Life Time			30,000			Hrs	6

Notes: The design of the LED driver must have specifications for the LED array in LCD Assembly.

The electrical characteristics of LED driver are based on Constant Current driving type.

The performance of the LED in LCM, for example life time or brightness, is extremely influenced by the characteristics of the LED Driver. So, all the parameters of an LED driver should be carefully designed. When you design or order the LED driver, please make sure unwanted lighting caused by the mismatch of the LED and the driver (no lighting, flicker, etc) has never been occurred. When you confirm it, the LCD—Assembly should be operated in the same condition as installed in your instrument.

- 1. Electrical characteristics are based on LED Array specification.
- 2. Specified values are defined for a Backlight Assembly. (IBL :2 LED array/LCM)
- Each LED array has one anode terminal and one cathode terminals.
   The forward current(I<sub>F</sub>) of the anode terminal is 400mA and it supplies 400mA into 1 strings, respectively



- 4. The forward voltage(V<sub>F</sub>) of LED array depends on ambient temperature (Appendix-VII)
- 5. Maximum level of power consumption is measured at initial turn on. Typical level of power consumption is measured after 1hrs aging at  $25 \pm 2$ °C.
- 6. The life time(MTTF) is determined as the time at which brightness of the LED is 50% compared to that of initial value at the typical LED current on condition of continuous operating at  $25 \pm 2^{\circ}$ C, based on duty 100%.
- The reference method of burst dimming duty ratio.
   It is recommended to use synchronous V-sync frequency to prevent waterfall (Vsync \* 2 =Burst Frequency)

Though PWM frequency is over 182Hz (max252Hz), function of backlight is not affected.

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### 3-2. Interface Connections

This LCD module employs two kinds of interface connection, a 30-pin connector is used for the module electronics and 14-pin connector is used for the integral backlight system.

#### 3-2-1. LCD Module

- LCD Connector(CN1): KDF71G-30S-1H(Manufactured by Hirose)
- Mating Connector : : FI-X30C2L (Manufactured by JAE) or Equivalent

Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

Pin No.	Symbol	Description	Note
1	VLCD	Power Supply +12.0V	
2	VLCD	Power Supply +12.0V	
3	VLCD	Power Supply +12.0V	
4	VLCD	Power Supply +12.0V	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
8	GND	Ground	
9	LVDS Select	'H' =JEIDA , 'L' or NC = VESA	Appendix IV
10	NC	No Connection	4
11	GND	Ground	
12	RA-	LVDS Receiver Signal(-)	
13	RA+	LVDS Receiver Signal(+)	
14	GND	Ground	
15	RB-	LVDS Receiver Signal(-)	
16	RB+	LVDS Receiver Signal(+)	
17	GND	Ground	
18	RC-	LVDS Receiver Signal(-)	
19	RC+	LVDS Receiver Signal(+)	
20	GND	Ground	
21	RCLK-	LVDS Receiver Clock Signal(-)	
22	RCLK+	LVDS Receiver Clock Signal(+)	
23	GND	Ground	
24	RD-	LVDS Receiver Signal(-)	
25	RD+	LVDS Receiver Signal(+)	
26	GND	Ground	
27	NC	No Connection (Note 4)	4
28	NC	No Connection (Note 4)	4
29	NC	No Connection (Note 4)	4
30	GND	Ground	

Notes: 1. All GND (Ground) pins should be connected together to the LCD module's metal frame.

- 2. All VLCD (power input) pins should be connected together.
- 3. All Input levels of LVDS signals are based on the EIA 644 Standard.
- 4. These pins are used only for LGD (Do not connect)
- 5. Specific pin No. #30 is used for "No signal detection" of system signal interface. It should be GND for NSB (No Signal Black) while the system interface signal is not. If this pin is "H", LCD Module displays AGP (Auto Generation Pattern).

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## 3-2-2. Backlight Module

## [CN1]

1) LED Array assy Connector (Plug)

: SMH200-07 (TBD)

2) Mating Connector (Receptacle)

: SMAW200A-H07AA2 (TBD)

Table 5. BACKLIGHT CONNECTOR PIN CONFIGURATION(CN101)

No	Symbol	Description	Note
1	L1 Cathode	LED Output Current L1	
2	NC		
3	NC		
4	NC		1 Block
5	NC		
6	NC		
7	L1 Anode	LED Input Current for L1	

### ◆ Rear view of LCM



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## 3-3. Signal Timing Specifications

Table 6 shows the signal timing required at the input of the LVDS transmitter. All of the interface signal timings should be satisfied with the following specification for normal operation.

Table 6. TIMING TABLE (DE Only Mode)

ITE	ITEM		Min	Тур	Max	Unit	Note
	Display Period	thv	1	1366	-	tclk	
Horizontal	Blank	tнв	90	162	410	tclk	
	Total	tHP	1456	1528	1776	tclk	
	Display Period	tvv	-	768	-	tHP	
Vertical	Blank	t∨B	20 (126)	22 (180)	240 (295)	tHP	1
	Total	t∨P	788 (894)	790 (948)	1008 (1063)	tHP	

ITE	ITEM		Min	Тур	Max	Unit	Note
	DCLK	fclk	63.0	63.0 72.4		MHz	
	Horizontal	fH	45	47.4	55	KHz	2
Frequency	Vertical	f∨	57 (47)	60 (50)	63 (53)	Hz	2 NTSC : 57~63Hz (PAL : 47~53Hz)

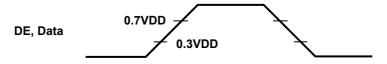
Note: 1. The input of HSYNC & VSYNC signal does not have an effect on normal operation (DE Only Mode). If you use spread spectrum of EMI, add some additional clock to minimum value for clock margin.

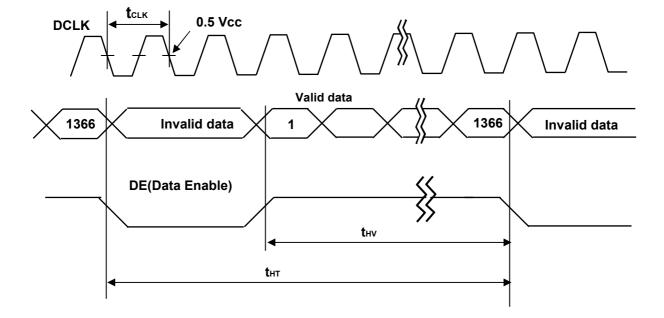
- 2. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate and the horizontal frequency
- \* Timing should be set based on clock frequency.

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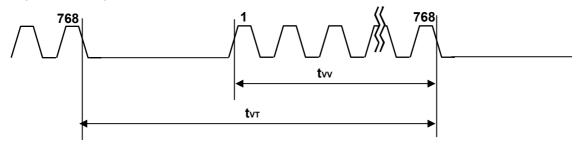
## 3-4. LVDS Signal Specification

## 3-4-1. LVDS Input Signal Timing Diagram





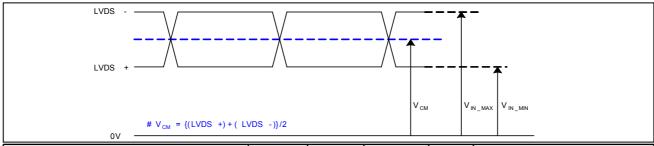
## DE(Data Enable)



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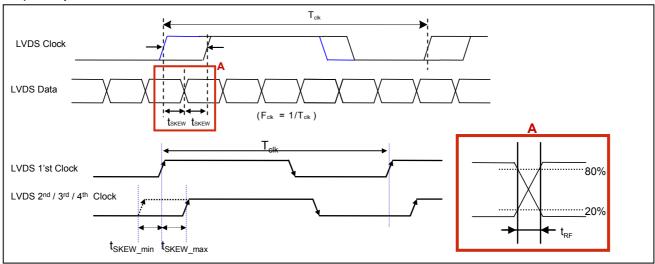
## 3-4-2. LVDS Input Signal Characteristics

## 1) DC Specification



Description	Symbol	Min	Max	Unit	Note
LVDS Common mode Voltage	V <sub>CM</sub>	1.0	1.5	V	-
LVDS Input Voltage Range	V <sub>IN</sub>	0.7	1.8	V	-
Change in common mode Voltage	△VCM	-	250	mV	-

## 2) AC Specification

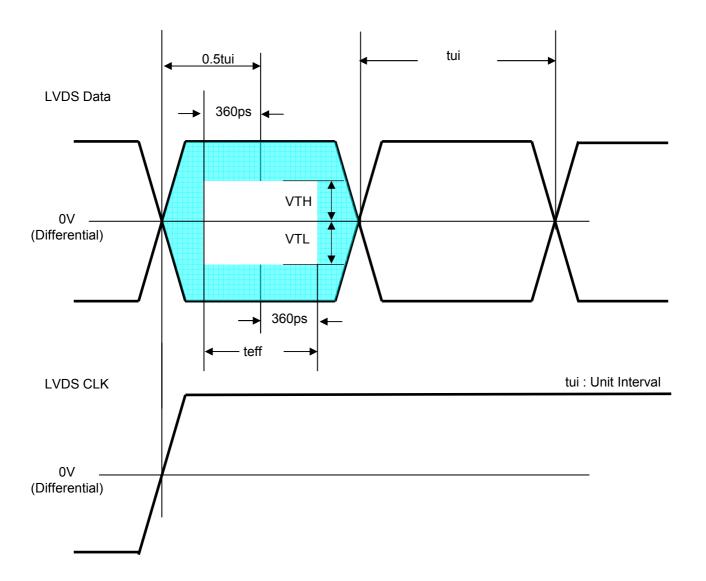


Description	Symbol	Min	Max	Unit	Note
LVDS Differential Voltage	V <sub>TH</sub>	100	600	mV	Tested with Differential Probe
LVDS Differential Voltage	V <sub>TL</sub>	-600	-100	mV	3
LVDS Clock to Data Skew	t <sub>SKEW</sub>	-	(0.20*T <sub>clk</sub> )/7	ps	-
LVDS Clock/DATA Rising/Falling time	t <sub>RF</sub>	260	(0.3*T <sub>clk</sub> )/7	ps	2
Effective time of LVDS	t <sub>eff</sub>	±360	-	ps	-
LVDS Clock to Clock Skew (Even to Odd)	t <sub>SKEW_EO</sub>	-	1/7* T <sub>clk</sub>	ps	-

notes 1. All Input levels of LVDS signals are based on the EIA 644 Standard.

- 2. If  $t_{RF}$  isn't enough,  $t_{eff}$  should be meet the range. 3. LVDS Differential Voltage is defined within  $t_{eff}$

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<sup>\*</sup> This accumulated waveform is tested with differential probe

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## 3-5. Color Data Reference

The brightness of each primary color(red,green,blue) is based on the 8bit gray scale data input for the color. The higher binary input, the brighter the color. Table 7 provides a reference for color versus data input.

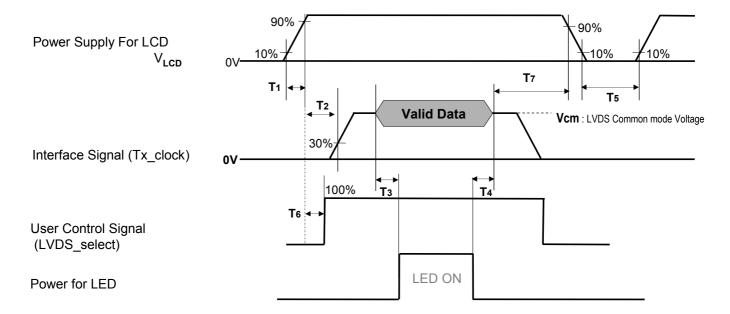
Table 7. COLOR DATA REFERENCE

								I	npu	t Col	lor E	Data													
	Color	MS	SB.		RE	ΕD		1 (	SB	MS	S.B.		GRE	EN		1	SB	MS	SB.		BL	UE			SB
			7 R	 S R5	R4	R3	R2 F				7 G6	G5	G4	G3	G2 (					B B 5	B4	B3	B2 E		
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Basic	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (001)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED																									
	RED (254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN																									
	GREEN (254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	BLUE (000)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE																									
	BLUE (254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

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## 3-6. Power Sequence

#### 3-6-1. LCD Driving circuit



**Table 8. POWER SEQUENCE** 

Doromotor		Unit	Note			
Parameter	Min	Тур	Max	Onit	Note	
T1	0.5	-	20	ms	1	
T2	0	-	-	ms	2	
Т3	400	-	-	ms	3	
T4	200	-	-	ms	3	
Т5	1.0	-	-	s	4	
T6	0	-	T2	ms	5	
Т7	0	-	-	ms	6	

#### Note:

- 1. Even though T1 is over the specified value, there is no problem if I2T spec of fuse is satisfied.
- 2. If T2 is satisfied with specification after removing LVDS Cable, there is no problem.
- 3. The T3 / T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
- 4. T5 should be measured after the Module has been fully discharged between power off and on period.
- 5. If the on time of signals (Interface signal and user control signals) precedes the on time of Power ( $V_{LCD}$ ), it will be happened abnormal display. When T6 is NC status, T6 doesn't need to be measured.
- 6. It is recommendation specification that T7 has to be 0ms as a minimum value.
- \* Please avoid floating state of interface signal at invalid period.
- \* When the power supply for LCD (VLCD) is off, be sure to pull down the valid and invalid data to 0V.

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## 4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at  $25\pm2^{\circ}$ C. The values are specified at 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to 0 °. FIG. 1 shows additional information concerning the measurement equipment and method.

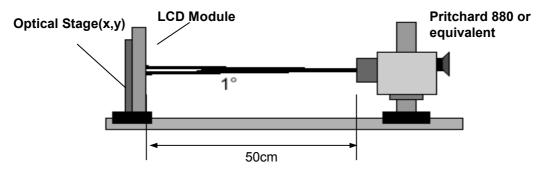


FIG. 1 Optical Characteristic Measurement Equipment and Method

**Table 10. OPTICAL CHARACTERISTICS** 

Ta= 25 $\pm$ 2°C, V<sub>LCD</sub>=12.0V, fv=60Hz, Dclk=72.4MHz, IF\_cathode = 400mA (Typ)

Parameter		Comple	Sumb ol		Value		l l mit	
"	arameter	Symbo	1	Min	Тур	Max	Unit	notes
Contrast Ratio		CR		850 (TBD)	1200	-		1
Surface Luminar	nce, white	$L_WH$		240	300	-	cd/m <sup>2</sup>	2
Luminance Varia	ation	$\delta_{\text{WHITE}}$	9P	50%(TBD)	60%(TBD)	1.3		3
Response Time	Variation	G to G	σ		6	9		5
Response fille	Gray to Gray (BW)	G to G B	W		9	13	ms	4
	RED	Rx		ļ	0.647(TBD)			
	KED	Ry			0.331(TBD)			
Color Coordinates	CDEEN	Gx			0.306(TBD)			
	GREEN	Gy		Тур	0.587(TBD)			
[CIE1931]	DLUE	Bx		-0.03	0.151(TBD)	+0.03		
	BLUE	Ву			0.063(TBD)			
	WHITE	Wx			0.281			
	VVIIIE	Wy			0.288			
Color Temperatu	re				10,000		K	
Color Gamut					68		%	
Viewing Angle (	CR>10)							
x a	axis, right(φ=0°)	θr		89	-	-		
x a	axis, left (φ=180°)	θΙ		89	-	-	dograa	6
у а	y axis, up (φ=90°)		θи		-	-	degree	°
y axis, down (φ=270°)		θd		89	-	-		
Gray Scale				-	2.2	-		7

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notes: 1. Contrast Ratio(CR) is defined mathematically as:

Contrast Ratio = Surface Luminance with all white pixels
Surface Luminance with all black pixels

It is measured at center 1-point.

- 2. Surface luminance are determined after the unit has been 'ON' and 1 Hour after lighting the backlight in a dark environment at 25±2°C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance ,  $\delta$  WHITE is defined as :  $\delta \, \text{WHITE(9P)} = \text{Minimum}(L_{on1}, L_{on2}, \cdots, L_{on8}, L_{on9}) \, / \, \text{Maximum}(L_{on1}, L_{on2}, \cdots, L_{on8}, L_{on9}) \, / \, \text{Where L}_{on1} \, \text{to L}_{on9} \, \text{are the luminance with all pixels displaying white at 9 locations} \, .$  For more information, see the FIG. 2.
- 4. Response time is the time required for the display to transit from any gray to white (Rise Time, Tr<sub>R</sub>) and from any gray to black (Decay time, Tr<sub>D</sub>). For additional information see the FIG. 3.
   ※ G to G<sub>BW</sub> Spec stands for average value of all measured points.

Photo Detector: RD-80S / Field: 2°

5. G to G  $_{\mbox{\tiny $\sigma$}}$  is Variation of Gray to Gray response time composing a picture

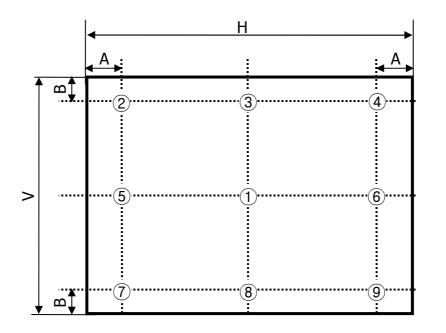
- 6. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 7. Gray scale specification
  Gamma Value is approximately 2.2. For more information, see the Table 11.

**Table 11. GRAY SCALE SPECIFICATION** 

Gray Level	Luminance [%] (Typ)
LO	0.08
L15	0.27
L31	1.04
L47	2.49
L63	4.68
L79	7.66
L95	11.5
L111	16.1
L127	21.6
L143	28.1
L159	35.4
L175	43.7
L191	53.0
L207	63.2
L223	74.5
L239	86.7
L255	100

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Measuring point for surface luminance & measuring point for luminance variation.



A:H/9 mm
B:V/9 mm
@H,V:Active Area

FIG. 2 5 Points for Luminance Measure

Response time is defined as the following figure and shall be measured by switching the input signal for "Gray(N)" and "Black or White".

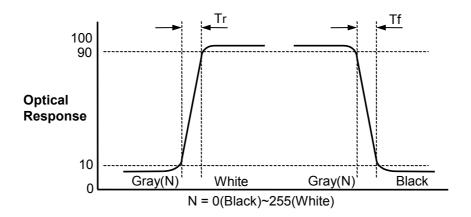


FIG. 3 Response Time

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## Dimension of viewing angle range

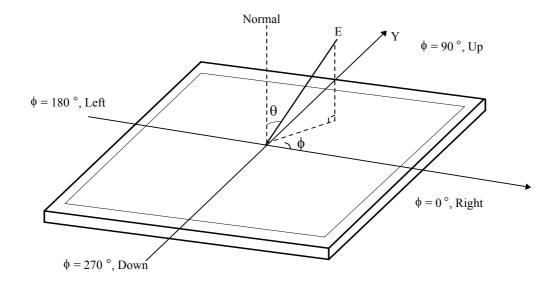


FIG. 4 Viewing Angle

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## 5. Mechanical Characteristics

Table 12 provides general mechanical characteristics.

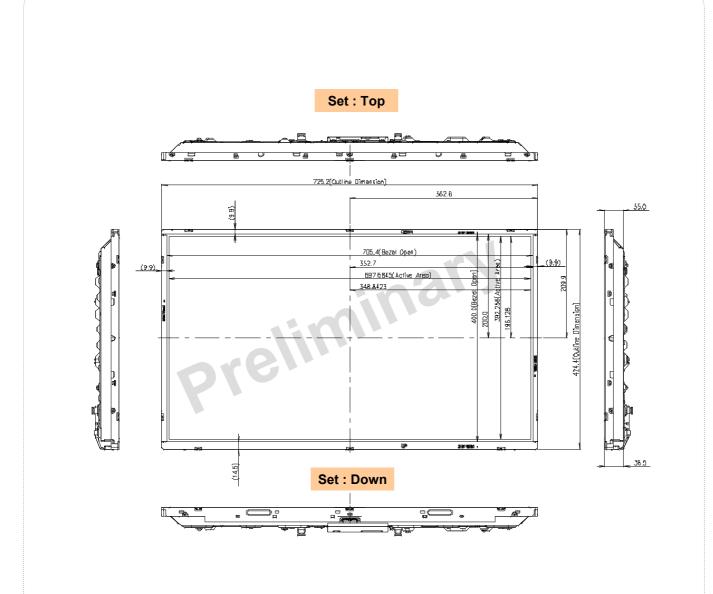
**Table 12. MECHANICAL CHARACTERISTICS** 

Item	Value		
	Horizontal	725.2 mm	
Outline Dimension	Vertical	424.40 mm	
	Depth	35.0 mm	
Bezel Area	Horizontal	705.4 mm	
Bezei Area	Vertical	400.0 mm	
Active Diapley Area	Horizontal	697.6845 mm	
Active Display Area	Vertical	392.256 mm	
Weight	3.8 Kg (Typ.), 4.0 kg (Max.) TBD		

notes: Please refer to a mechanical drawing in terms of tolerance at the next page.

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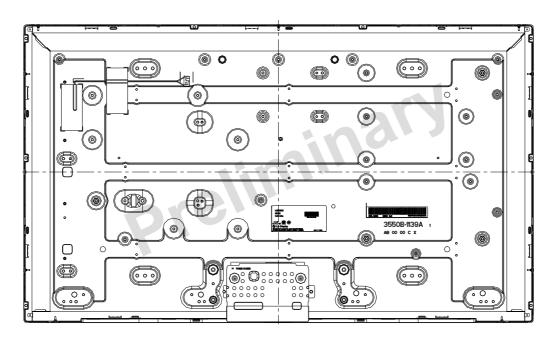
## <FRONT VIEW>



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### <REAR VIEW>

## Set : Top



Set : Down

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# 6. Reliability

**Table 13. ENVIRONMENT TEST CONDITION** 

No.	Test Item	Condition
1	High temperature storage test	Ta= 60°C 240h
2	Low temperature storage test	Ta= -20°C 240h
3	High temperature operation test	Ta= 50°C 50%RH 240h
4	Low temperature operation test	Ta= 0°C 240h
5	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, Each direction per 10 min
6	Shock test (non-operating)	Simplifievel: 50Grms  Waveform: half sine wave, 11ms  Direction: $\pm X$ , $\pm Y$ , $\pm Z$ One time each direction
7	Humidity condition Operation	Ta= 40 °C ,90%RH
8	Altitude operating storage / shipment	0 - 16,400 ft 0 - 40,000 ft

notes: Before and after Reliability test, LCM should be operated with normal function.

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#### 7. International Standards

## 7-1. Safety

- a) UL 60065, Underwriters Laboratories Inc.
  Audio, Video and Similar Electronic Apparatus Safety Requirements.
- b) CAN/CSA C22.2 No.60065:03, Canadian Standards Association. Audio, Video and Similar Electronic Apparatus Safety Requirements.
- c) EN 60065, European Committee for Electrotechnical Standardization (CENELEC). Audio, Video and Similar Electronic Apparatus Safety Requirements.
- d) IEC 60065, The International Electrotechnical Commission (IEC).
  Audio, Video and Similar Electronic Apparatus Safety Requirements.
  (Including report of IEC60825-1:2001 clause 8 and clause 9)

#### Notes

1. Laser (LED Backlight) Information

Class 1M LED Product IEC60825-1 : 2001 Embedded LED Power (Class1M)

#### 2. Caution

: LED inside.

Class 1M laser (LEDs) radiation when open. Do not open while operating.

#### 7-2. EMC

- a) ANSI C63.4 "American National Standard for Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electronic Equipment in the Range of 9 kHz to 40 GHz." American National Standards Institute (ANSI), 2003.
- b) CISPR 22 "Information technology equipment Radio disturbance characteristics Limit and methods of measurement." International Special Committee on Radio Interference (CISPR), 2005.
- c) CISPR 13 "Sound and television broadcast receivers and associated equipment Radio disturbance characteristics – Limits and method of measurement." International Special Committee on Radio Interference (CISPR), 2006.

#### 7-3. Environment

a) RoHS, Directive 2002/95/EC of the European Parliament and of the council of 27 January 2003

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## 8. Packing

## 8-1. Information of LCM Label

a) Lot Mark

Α	В	С	D	E	F	G	Н	I	J	К	L	М
---	---	---	---	---	---	---	---	---	---	---	---	---

A,B,C: SIZE(INCH) D: YEAR

E: MONTH  $F \sim M$ : SERIAL NO.

#### Note

#### 1. YEAR

Year	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Mark	Α	В	C	D	Е	F	G	Η	J	K

#### 2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	3	4	5	6	7	8	9	Α	В	С

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

## 8-2. Packing Form

a) Package quantity in one Pallet: 36 pcs

b) Pallet Size: 1140 mm(W) X 870 mm(D) X 1161 mm(H)

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#### 9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

### 9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment.
  Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

### 9-2. Operating Precautions

- (1) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (2) Brightness depends on the temperature. (In lower temperature, it becomes lower.)

  And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (3) Be careful for condensation at sudden temperature change. Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (4) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (5) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (6) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (7) A screw which is fastened up the steels should be a machine screw. (if not, it can causes conductive particles and deal LCM a fatal blow)
- (8) Please do not set LCD on its edge.
- (9) The conductive material and signal cables are kept away from LED driver inductor to prevent abnormal display, sound noise and temperature rising.

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### 9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

### 9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

### 9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5°C and 35°C at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.

  It is recommended that they be stored in the container in which they were shipped.
- (3) Storage condition is guaranteed under packing conditions.
- (4) The phase transition of Liquid Crystal in the condition of the low or high storage temperature will be recovered when the LCD module returns to the normal condition.

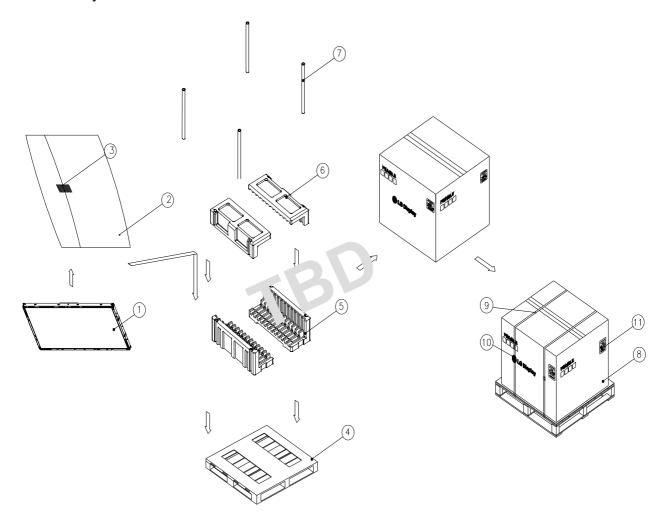
## 9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ion-blown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

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## # APPENDIX-I

# ■ Pallet Ass'y

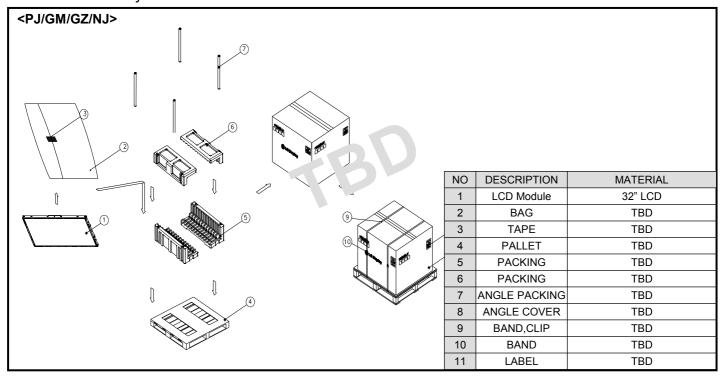


NO	DESCRIPTION	MATERIAL					
1	LCD Module	32" LCD					
2	BAG	TBD					
3	TAPE	TBD					
4	PALLET	TBD					
5	PACKING	TBD					
6	PACKING	TBD					
7	ANGLE PACKING	TBD					
8	ANGLE COVER	TBD					
9	BAND,CLIP	TBD					
10	BAND	TBD					
11	LABEL	TBD					

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## # APPENDIX-I

## ■ Pallet Ass'y



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### # APPENDIX- II-1

## ■ LCM Label



## ■ Production site

- LG Display (Paju) Co., LTD
- LG Display (Guangzhou) Co., LTD
- LG Display (Nanjing) Co., LTD
- LG Display (Poland) Co., LTD

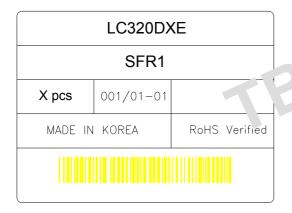
Note 1. The origin of LCM Label will be changed according to the production site.

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### # APPENDIX- II-2

■ Box Label

■ Pallet Label

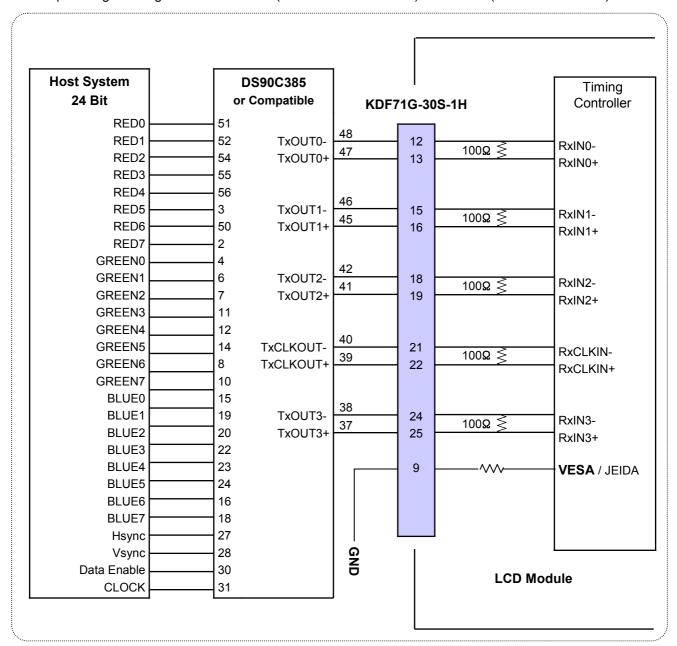




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#### # APPENDIX- III-1

■ Required signal assignment for Flat Link (Thine: THC63LVD103) Transmitter(Pin7= "L" or "NC")



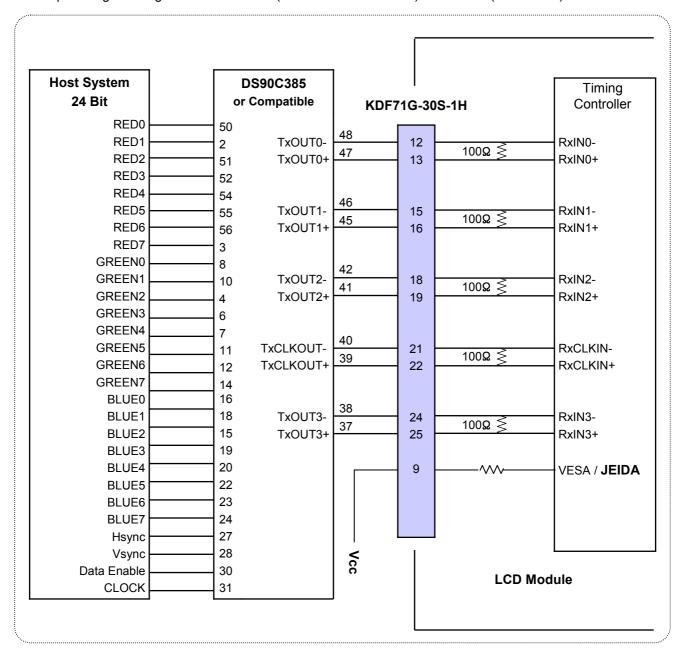
Note: 1. The LCD module uses a 100  $Ohm[\Omega]$  resistor between positive and negative lines of each receiver input

- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

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#### # APPENDIX- III-2

■ Required signal assignment for Flat Link (Thine: THC63LVD103) Transmitter(Pin7= "H")



Note :1. The LCD module uses a 100  $Ohm[\Omega]$  resistor between positive and negative lines of each receiver input

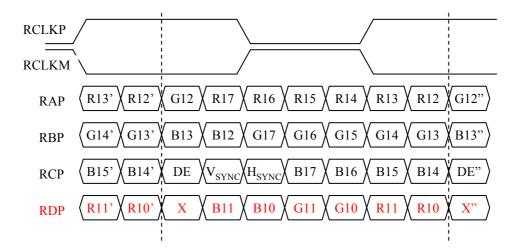
- 2. Refer to LVDS Transmitter Data Sheet for detail descriptions. (THC63LVD103 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

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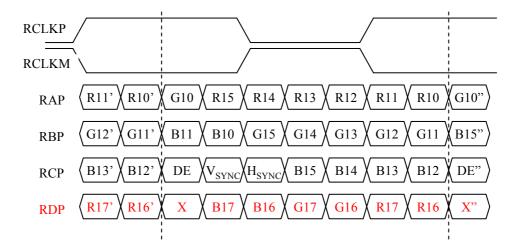
### # APPENDIX- IV

## ■ LVDS Data-Mapping Information (8 Bit )

## 1) LVDS Select: "H" Data-Mapping (JEIDA format)



### 2) LVDS Select: "L" Data-Mapping (VESA format)

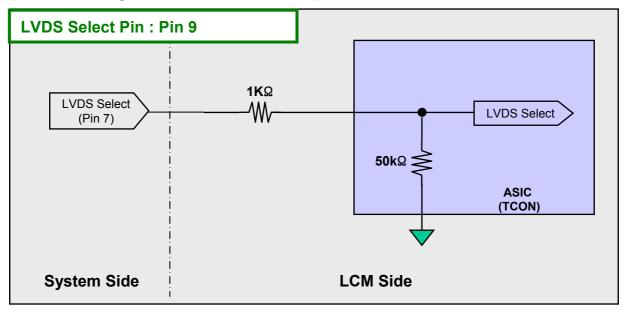


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## # APPENDIX- V-1

# ■ Option Pin Circuit Block Diagram

1) Circuit Block Diagram of LVDS Format Selection pin



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