

# SPECIFICATION FOR APPROVAL

(
 Preliminary Specification

) Final Specification

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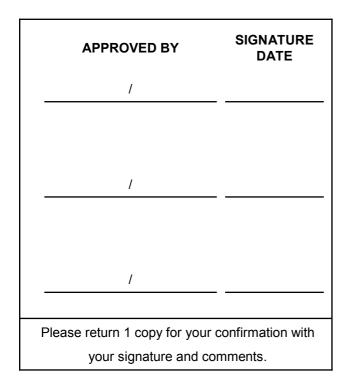
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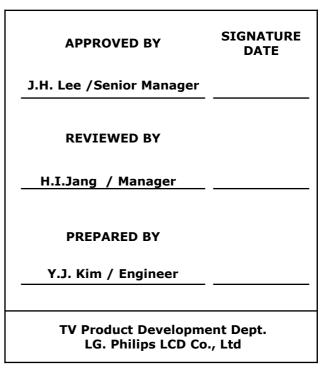
BUYER	
MODEL	

17" WXGA	TFT LCD
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SUPPLIER	LG.Philips LCD Co., Ltd.
*MODEL	LC170WXN
SUFFIX	SAA1 (RoHS Verified)

\*When you obtain standard approval, please use the above model name without suffix





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# RECORD OF REVISIONS

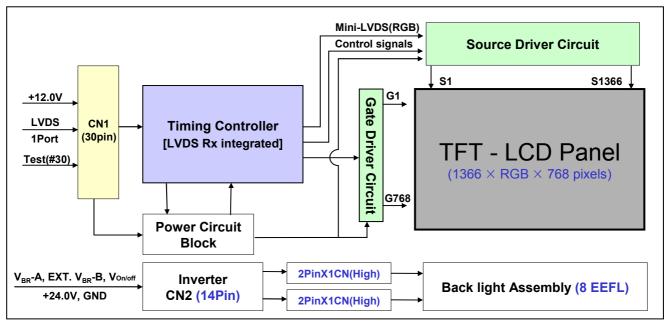
Revision No.	Revision Date	Page	Description
0.0	Nov, 22, 2007	-	Preliminary Specification(First Draft)
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#### **1. General Description**

The LC170WXN is a Color Active Matrix Liquid Crystal Display with an integral External Electrode Fluorescent Lamp(EEFL) backlight system. The matrix employs a-Si Thin Film Transistor as the active element. It is a transmissive display type which is operating in the normally black mode. It has a 16.8 inch diagonally measured active display area with WXGA resolution (768 vertical by 1366 horizontal pixel array). Each pixel is divided into Red, Green and Blue sub-pixels or dots which are arrayed in vertical stripes. Gray scale or the luminance of the sub-pixel color is determined with a 8-bit gray scale signal for each dot. Therefore, it can present a palette of more than 16.7M(true) colors.

It has been designed to apply the 8-bit 1-port LVDS interface.

It is intended to support LCD TV, PCTV where high brightness, super wide viewing angle, high color gamut, high color depth and fast response time are important.





#### **General Features**

Active Screen Size	16.84 inches(427.816mm) diagonal
Outline Dimension	413.0(H) x 252.0 (V) x 43.6 mm(D) (Typ.)
Pixel Pitch	0.091(H) x 0.273(V) mm x RGB
Pixel Format	1366 horiz. by 768 vert. Pixels, RGB stripe arrangement
Color Depth	8-bit, 16.7 M colors
Luminance, White	350 cd/m <sup>2</sup> (Center 1point ,Typ.)
Viewing Angle (CR>10)	Viewing angle free ( R/L 178 (Typ.), U/D 178 (Typ.))
Power Consumption	Total 36.1 W (Typ.) (Logic=2.5 W, Inverter=33.6W [VBR-A=1.65V] )
Weight	2100g (Typ.)
Display Mode	Transmissive mode, Normally black
Surface Treatment	Hard coating(3H), Anti-glare treatment of the front polarizer (Haze 13%)

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### 2. Absolute Maximum Ratings

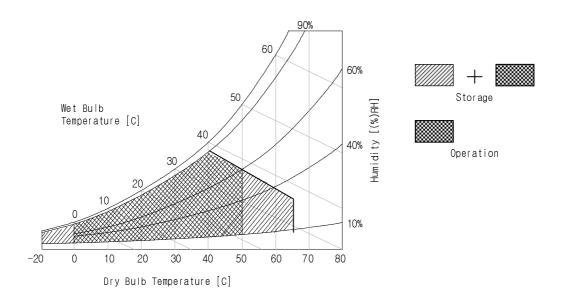
The following items are maximum values which, if exceeded, may cause faulty operation or damage to the LCD module.

Parameter		Symbol		ue	Unit	Remark	
F	arameter	Symbol	Min	Max	Offic	Reindik	
Power Input	LCM	VLCD	-0.3	+14.0	VDC	at 25 ± 2 ℃	
Voltage	Backlight inverter	VBL	-0.3	+27.0	VDC		
ON/OFF Co	ON/OFF Control Voltage		-0.3	+5. 5	VDC		
Brightness (	Brightness Control Voltage		0	+5.0	VDC		
Operating Temperature		Тор	0	+50	C		
Storage Temperature		Тѕт	-20	+65	°C	Note 1.2	
Operating Ambient Humidity		Нор	10	90	%RH	Note 1,2	
Storage Hur	nidity	Нѕт	10	90	%RH		

Table 1.	ABSOLUTE	MAXIMUM	RATINGS
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Notes :

- 1. Temperature and relative humidity range are shown in the figure below. Wet bulb temperature should be 39 ℃ Max, and no condensation.
- 2. Gravity mura can be guaranteed under 40 °C condition.
- 3. Abnormal visual problems by panel front side surface temperature can be occurred in specific range (60  $\degree$  ~ 65  $\degree$ ), But materials (exp : polarizer) are not damaged permanently in this range, TSUR.



### 3. Electrical Specifications

### 3-1. Electrical Characteristics

It requires two power inputs. One is employed to power for the LCD circuit. The other Is used for the EEFL backlight and inverter circuit.

#### **Table 2. ELECTRICAL CHARACTERISTICS**

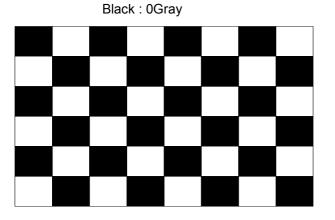
Parameter	Symbol		Value	Unit	Note		
	Gymbol	Min	Тур	Max	0 m		
Circuit :							
Power Input Voltage	VLCD	11.4	12.0	12.6	VDC		
	ILCD	-	205	250	mA	1	
Power Input Current		-	245	300	mA	2	
Power Consumption	PLCD	-	2.5	3.0	Watt	1	
Rush current	Irush	-	-	2.2	А	3	

Notes:

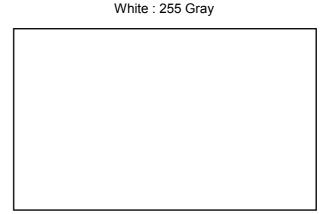
- 1. The specified current and power consumption are under the V<sub>LCD</sub>=12.0V,  $25 \pm 2$  °C, f<sub>V</sub>=60Hz condition whereas mosaic pattern(8 x 6) is displayed and f<sub>V</sub> is the frame frequency.
- 2. The current is specified at full white pattern.

White : 255Gray

3. The duration of rush current is about 2ms and rising time of power input is 1ms (min.).



Mosaic Pattern(8 x 6)



Full White pattern

#### Table 3. ELECTRICAL CHARACTERISTICS (Continue)

Parameter		Symbol		Values		Unit	Notes		
			Symbol	Min	Тур	Max	Unit	NOLES	
Inverter :									
Power Supply Inp	ut Voltage		VBL	21.6	24.0	25.2	Vdc	1	
Power Supply Inp	ut Voltage R	ipple		-	-	0.5	Vp-р	1	
	Aftor Aging			-	1.4	1.6	А	VBR-A = 1.65V 1	
Power Supply	After Aging		IBL_A	-	1.55	1.75	А	V <sub>BR</sub> -A = 3.3V 1	
Input Current	Boforo Agir			-	1.5	1.7	А	V <sub>BR</sub> -A = 1.65V 2	
	Before Agir	ig	IBL_B	-	1.65	1.85	А	V <sub>BR</sub> -A = 3.3V 2	
Power Supply Input Current(In-Rush)		Irush	-	-	4	A	VBL = 24V EXTVbr-B=100% V <sub>BR</sub> -A = 1.65V		
Power Consumpt	ion		PBL	-	33.6	38.4	W	1	
	On/Off	On	V on	2.5	-	5.0	Vdc		
Input signal for		Off	V off	-0.3	0.0	0.8	Vdc		
Inverter control	Brightness	∆diust	VBR-A	0.0	1.65	3.3	Vdc		
			EXT VBR-B	20		100	%	On duty	
PWM Frequency for	or NTSC & P	AL	NTSC/PAL	115/95	120/100	125/105	Hz	3	
Pulse Duty Level(F	WM)		High Level	2.5	-	5.0	Vdc	HIGH: Lamp on	
(Burst mode)			Low Level	0.0	-	0.8	Vdc	LOW:Lamp off	
Lamp :									
Lamp Voltage (Ex	Lamp Voltage (ExtVbr-B = 100%)		Vout	680/650	820/790	970/940	V(rms)	V <sub>BR</sub> -A = Typ	
			IO-MAX	53/55	58/60	63/65	mA(rms)	V <sub>BR</sub> -A = Max	
Lamp Current (Ext	Vbr-B = 100	%)	IO-TYP	48/50	53/55	58/60	mA(rms)	VBR-A = TYP	
		IO-MIN	43/45	48/50	53/55	mA(rms)	V <sub>BR</sub> -A = Min		
Life Time			Boost(0V~3.3V)	50,000			Hrs	4	

Notes :

- Electrical characteristics are determined after the unit has been 'ON' and stable for approximately 120 minutes at 25± 2℃. The specified current and power consumption are under the typical supply Input voltage 24V and Vbr 1.65V, it is total power consumption.
  - The ripple voltage of the power supply input voltage is under 0.5 Vp-p. LPL recommend Input Voltage is  $24.0V \pm 5\%$ .
- 2. Electrical characteristics are determined within 30 minutes at 25± 2  ${}^{\circ}\!\!{\rm C}.$
- The specified currents are under the typical supply Input voltage 24V.
- 3. LPL recommend that the PWM freq. is synchronized with double harmonic of Vsync signal of system.
- 4. Specified Values are for a single lamp which is aligned horizontally. The life time is determined as the time which luminance of the lamp is 50% compared to that of initial value at the typical/ maximum lamp current on condition of continuous operating at 25 ± 2 ℃
- 5. The duration of rush current is about 5ms.

#### 3-2. Interface Connections

This LCD employs Two interface connections, a 30 pin connector is used for the module electronics and 14Pin Connector is used for the backlight system.

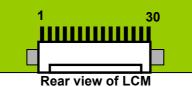
### 3-2-1. LCD Module

- LCD Connector(CN1) : FI-X30SSL-HF (Manufactured by JAE).

- Mating Connector : FI-30C2L(Manufactured by JAE).

#### Table 4. MODULE CONNECTOR(CN1) PIN CONFIGURATION

	Symbol	Description	Note
1	VCC	Power Supply +12.0V	
2	VCC	Power Supply +12.0V	
3	VCC	Power Supply +12.0V	
4	VCC	Power Supply +12.0V	
5	GND	Ground	
6	GND	Ground	
7	GND	Ground	
8	GND	Ground	
9	Select	Select LVDS Data format	1
10	NC	No Connection	
11	GND	Ground	
12	RA-	LVDS Receiver Signal(-)	
13	RA+	LVDS Receiver Signal(+)	
14	GND	Ground	
15	RB-	LVDS Receiver Signal(-)	
16	RB+	LVDS Receiver Signal(+)	
17	GND	Ground	
18	RC-	LVDS Receiver Signal(-)	
19	RC+	LVDS Receiver Signal(+)	
20	GND	Ground	
21	RCLK-	LVDS Receiver Clock Signal(-)	
22	RCLK+	LVDS Receiver Clock Signal(+)	
23	GND	Ground	
24	RD-	LVDS Receiver Signal(-)	
25	RD+	LVDS Receiver Signal(+)	
26	GND	Ground	
27	NC	No Connection	
28	NC	No Connection	
29	GND	Ground	
30	RBF	No Signal Processing select	2



#### Notes:

1. Interface data format is "LG Format, VESA", please Refer to Appendix for further details.

- 2. Pin30 should be ground, this pin is necessary for LCD test.
- Vcc(3.3V) : AGP(Auto Generation Pattern), Ground : Black Display 3. All GND(ground) pins should be connected together, which should also be connected to the LCD's metal frame. 4. All VLCD (power input) pins should be connected together.
- 5. Input Level of LVDS signal is based on the IEA 664 Standard.

#### 3-2-2. Backlight Inverter

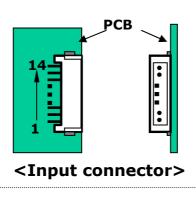
- -Inverter Connector : S14B-PH-SM3(manufactured by JST)
- Mating Connector : PHR-14

#### Table 5. INVERTER CONNECTOR PIN CONFIGULATION

Pin No	Symbol	Description	Note
1	VBL	Power Supply +24.0V	
2	VBL	Power Supply +24.0V	
3	VBL	Power Supply +24.0V	
4	VBL	Power Supply +24.0V	
5	VBL	Power Supply +24.0V	
6	GND	Backlight Ground	
7	GND	Backlight Ground	
8	GND	Backlight Ground	1
9	GND	Backlight Ground	
10	GND	Backlight Ground	
11	Vbr-A	DC(0.0V ~ 3.3V) input	2
12	Von/off	0.0V ~ 5.0V	3, Open/High for B/L on as default
13	EXTVBR-B	PWM signal (0.0V ~ 3.3V) input	4
14	Status	Normal : Under 0.7V Abnormal : External Pull up	

Notes :

- 1. GND should be connected to the LCD module's metal frame.
- 2. Minimum Brightness : Boost = 0.0V Maximum Brightness : Boost = 3.3V "OPEN" : Boost = 1.65V
- 3. Rising Edge : Lamp "ON" / Falling Edge : Lamp "OFF"
- 4. Pin#13 can be opened. (if Pin #13 is open, EXTVBR-B is 100%)
- 5. Pin#14 can be opened. (GND or NC)
- 6. Each impedance of pin #11, 12 and 13 is TBD [K $\Omega$ ], TBD [K $\Omega$ ] and TBD [K $\Omega$ ].



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### 3-3. Signal Timing Specifications

This is the signal timing required at the input of the LVDS transmitter. All of the interface signal timing should be satisfied with the following specifications for it's proper operation.

#### Table 6. TIMING TABLE for NTSC & PAL

[ DE (Data Enable) Only ]

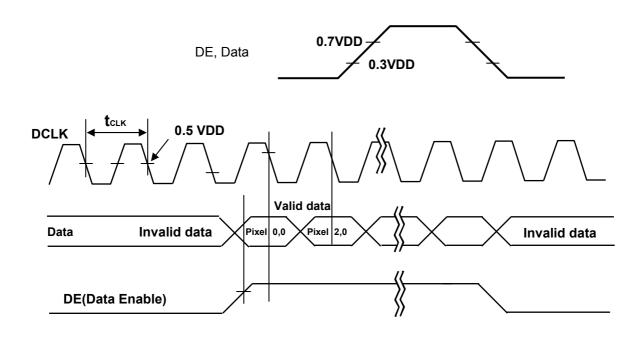
ITEM	Symbol		Min	Тур	Мах	Unit	Note
DCLK	Period	tськ	12.5	13.8	15.8	ns	
DCLK	Frequency	-	63	72.4	80	MHz	
	Period	tн⊤	1456	1528	1920	<b>t</b> CLK	
	Horizontal Valid	tн∨	1366	1366	1366	<b>t</b> CLK	
	Horizontal Blank	-	tHP- tH∨	162	thp- thv		
Hsync	Frequency	fн	45	47.4	50	KHz	
	Width	twн	-	32	-	tськ	
	Horizontal Back Porch	tнвр	24	48	-		
	Horizontal Front Porch	thfp	40	80	-		
	Period	t∨⊤	776	790	1063	tHP	
	Vertical Valid	tvv	768	768	768	tHP	
	Vertical Blank	-	tvp-tvv	22	tvp- tvv	tHP	
Vsync	Frequency	f∨	47	60	63	Hz	Note 1) PAL : 47~53Hz
	Width	tw∨	-	5	-	tHP	NTSC : 57~63Hz
	Vertical Back Porch	tvвр	5	15	-	Hz	
	Vertical Front Porch	tvfp	1	2	-	tHP	

Note :

1. The performance of the electro-optical characteristics may be influenced by variance of the vertical refresh rate.

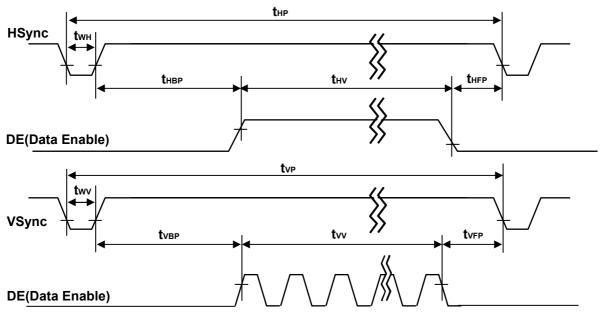
2. Above Timing Tables are only valid for DE Mode.

### 3-4. Signal Timing Waveforms



\* Reference : Sync. Relation

<sup>\*</sup> tvb = tvfp + twv +tvbp



<sup>\*</sup> the = thee + twh +thee

### 3-5. Color Data Reference

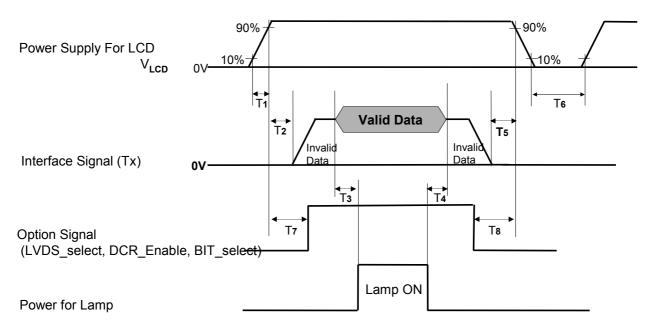
The brightness of each primary color(red,green,blue) is based on the 8-bit gray scale data input for the color. The higher binary input, the brighter the color. Table 10 provides a reference for color versus data input.

#### Table 7. COLOR DATA REFERENCE

												Inpu	ut Co	olor	Data	a									
	Color				RE	D							GRE	EEN			0.0				BL	UE			
		MS		DE		<b>D</b> 2	<b>D</b> 2	R1 F	_	MS		05	G4	<u> </u>	<u> </u>			MS		DE		<b>D</b> 2	<b>D</b> 2	L B1	SB
	Black	0	0	0	0	к <u>э</u> 0	0	0	0	0	0	0	0	0	0	0	0	Б7 0	0	0	0	вз 0	Б2 0	<u>о</u>	<u>о</u>
	Red (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Basic Color	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1		1	1	. 1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	RED (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	RED (001)	0	0	0	0	0	0	0		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RED			0	0			0	0	-	0	0	0			0	•	0		0	0			0		_
	 RED (254)	1	1	1		1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	 0	0	0	0
	RED (255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000) Dark	0	0	0	0	0	0	0	$\rightarrow$	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	GREEN (000) Dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
GREEN	. ,		0	0			0	0		0	0	0			0	0	-		0	0			0		
GREEN	 GREEN (254)	0	0	0	0	0	0	0	0	1	1	1		. 1	1	1	0	0	0	0	0	 0	0	0	0
	GREEN (255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1		0	0	0	0	0	0	0
	BLUE (000) Dark	0	0	0	0	0	0	0	$\rightarrow$	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	BLUE (001)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
BLUE					••																	••			
	BLUE (254)	0	0	0	0	0	0	0		0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	BLUE (255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

### 3-6. Power Sequence

#### 3-6-1. LCD Driving circuit

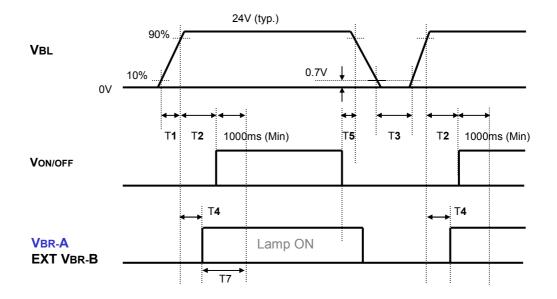


#### Table 8. POWER SEQUENCE

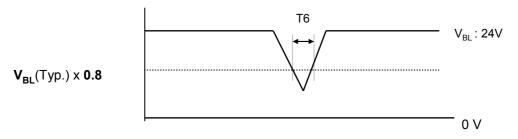
Deremeter		Value		1.1	Notos
Parameter	Min	Тур	Max	Unit	Notes
T1	0.5	-	20	ms	
T2	0	-	-	ms	4
Т3	3 x (1/f <sub>V</sub> )	-	-	ms	3
T4	200	-	-	ms	3
T5	0	-	-	ms	
Т6	2.0	-	-	S	5
T7	0	-	T2	ms	4
Т8	0	-	-	ms	4

- Note: 1. Please avoid floating state of interface signal at invalid period.
  - 2. When the interface signal is invalid, be sure to pull down the power supply  $V_{LCD}$  to 0V.
  - 3. The T3/T4 is recommended value, the case when failed to meet a minimum specification, abnormal display would be shown. There is no reliability problem.
  - 4. If the on time of signals(Interface signal and Option signals) precedes the on time of Power(V<sub>LCD</sub>), check the LCD logic Power(Vcc) is under 0.8V, otherwise it will be happened abnormal display.
  - 5. T6 should be measured after the Module has been fully discharged between power off and on period.

#### 3-6-2. Sequence for Inverter



#### 3-6-3. Deep condition for Inverter



#### Table 9. Power Sequence for Inverter

Parameter		Values		Units	Remarks
Falameter	Min	Тур	Max	Units	Remarks
T1	20	-	-	ms	1
T2	500	-	-	ms	
Т3	200	-	-	ms	
T4	0		-	ms	2
T5	10	-	-	ms	
Т6	-	-	10	ms	<b>V<sub>BL</sub></b> (Тур) х <b>0.8</b>
T7	1000	-	-	ms	3

Notes : 1. T1 describes rising time of 0V to 24V and this parameter does not applied at restarting time.

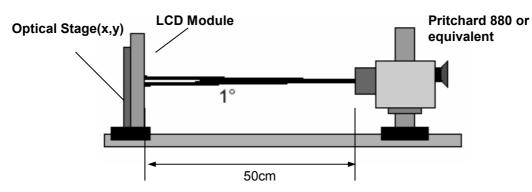
2. T4(max) is less than T2.

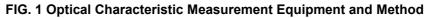
3. For 1 second after lamp on(T7),  $V_{BR-A}$  should be more than 1.65V and  $V_{BR-B}$  should be 3.3V.

### 4. Optical Specification

Optical characteristics are determined after the unit has been 'ON' and stable in a dark environment at  $25\pm 2$  °C. The values specified are at an approximate distance 50cm from the LCD surface at a viewing angle of  $\Phi$  and  $\theta$  equal to 0 °.

FIG. 1 shows additional information concerning the measurement equipment and method.





#### Table 10. OPTICAL CHARACTERISTICS

```
Ta= 25± 2 °C, V<sub>LCD</sub>=12.0V, fv=60Hz, Dclk=72.3MHz VBR_A=1.65V, EXT. VBR_B=100%
```

Dem		Quest			Value		11-24	Nete
Para	ameter	Symb	01	Min	Тур	Max	Unit	Note
Contrast Ratio		CR		680	900	-		1
Surface Lumina	nce, white	L <sub>WH</sub>		280	350	-	cd/m <sup>2</sup>	2
Luminance Varia	ation	$\delta_{\text{WHITE}}$	5P	-	-	1.3		3
Response Time	Rise Time	Tr <sub>R</sub>		-	8	12	ms	4,5
Response nine	Decay Time	Tr <sub>D</sub>		-	9	14	1115	4,5
	RED	Rx			0.635			
	RED	Ry			0.340			
	GREEN	Gx			0.292	]		
Color Coordinates	GREEN	Gy		Тур	0.612	Тур		
[CIE1931]	BLUE	Bx		-0.03	0.145	+0.03		
		Ву			0.063	]		
	WHITE	Wx			0.279			
		Wy			0.292			
Viewing Angle (	CR>10)							
xa	axis, right(φ=0°)	θr		89	-	-		
Xa	axis, left (థ=180°)	θI		89	-	-		
y a	axis, up (¢=90°)	θu		89	-	-	degree	6
y a	axis, down (థ=270°)	θd		89	-	-		
Gray Scale				-	-	-		7

Ver. 0.0

#### Notes :

1. Contrast Ratio(CR) is defined mathematically as :

CRn = -Surface Luminance at all black pixels It is measured at center 1-point.

- 2. Surface luminance are determined after the unit has been 'ON' and 30min after lighting the backlight in a dark environment at 25±2 °C. Surface luminance is the luminance value at center 1-point across the LCD surface 50cm from the surface with all pixels displaying white. For more information see the FIG. 2.
- 3. The variation in surface luminance ,  $\delta$  WHITE and  $\delta$  BLACK are defined as : -  $\delta$  WHITE(5P) = Maximum(L<sub>on1</sub>, L<sub>on2</sub>, L<sub>on3</sub>, L<sub>on4</sub>, L<sub>on5</sub>) / Minimum(L<sub>on1</sub>, L<sub>on2</sub>, L<sub>on3</sub>, L<sub>on4</sub>, L<sub>on5</sub>) Where Lon1 to Lon5 are the luminance with all pixels displaying white at 5 locations . For more information, see the FIG. 2.
- 4. Response time is the time required for the display to transition from black to white (Rise Time,  $Tr_{p}$ ) and from white to black (Decay Time,  $Tr_D$ ). For additional information, see FIG 3.
- 5. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD module surface. For more information, see the FIG. 4.
- 6. Gray scale specification Gamma Value is approximately 2.2. For more information, see the Table 11.
- 7. Image sticking When it changes into pattern-B after a 1-hour drive by pattern-A, it disappears within 10 minutes. For more information, see the FIG. 4.

Grout avai		Luminance [%]	
Gray Level	Minimum	Typical	Maximum
LO	0	0.113	0.65
L15	0	0.27	1.4
L31	0	1.00	2.7
L47	0	2.40	5
L63	0.5	4.60	9.1
L79	1.7	7.60	13.7
L95	4.0	11.4	19.1
L111	7.5	16.0	25.1
L127	11.8	21.6	32.9
L143	16.3	28.0	42
L159	22.0	35.4	50.5
L175	29.7	43.7	59.7
L191	39.4	53.0	69.5
L207	48.8	63.2	80.3
L223	61.5	74.5	90.6
L239	76.9	88.0	96.6
L255	100	100	100

#### Table 11. GRAY SCALE SPECIFICATION

Measuring point for surface luminance & measuring point for luminance variation.

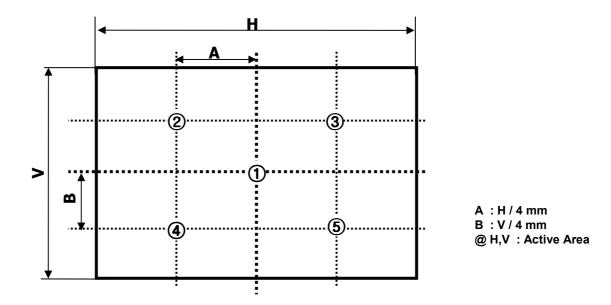


FIG. 2 5 Points for Luminance Measure

The response time is defined as the following figure and shall be measured by switching the input signal for Black to White and White to Black.

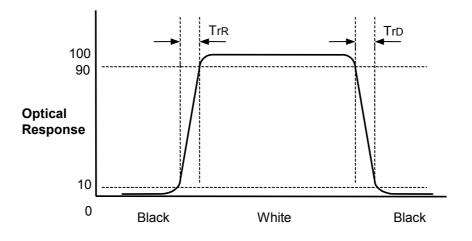


FIG. 3 Response Time

Dimension of viewing angle range

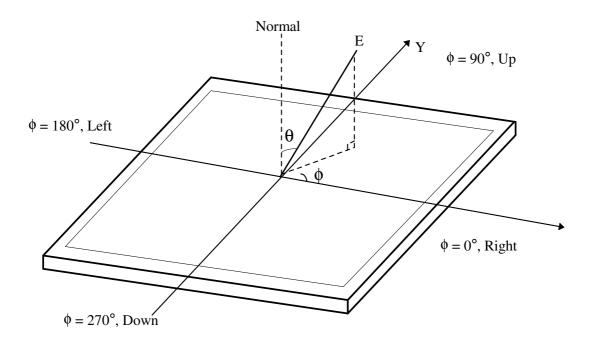
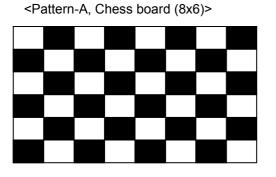


FIG. 4 Viewing Angle

7. Image sticking



<Pattern-B, Mid-gray(127 gray)>

FIG. 6 Image sticking Test pattern

### **5. Mechanical Characteristics**

Table 15 provides general mechanical characteristics.

#### Table 12. MECHANICAL CHARACTERISTICS

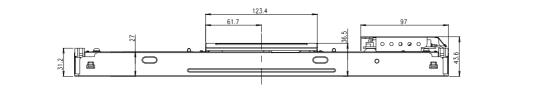
Item	Value	
	Horizontal	413.0 mm
Outline Dimension	Vertical	252.0 mm
	Depth	43.6 mm
Densil Area	Horizontal	377.0 mm
Bezel Area	Vertical	213.8 mm
	Horizontal	372.918 mm
Active Display Area	Vertical	209.664 mm
Weight	2100g (Typ.) , 2210g (Max.)	

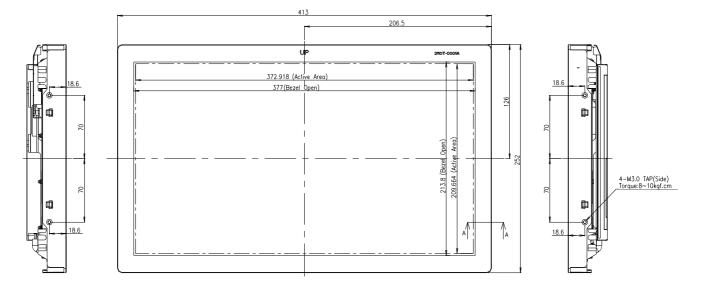
Note : Please refer to a mechanic drawing in terms of tolerance at the next page.

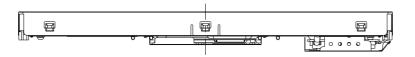
#### LC170WXN

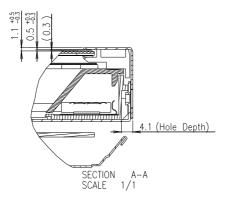
### **Product Specification**

### <FRONT VIEW>

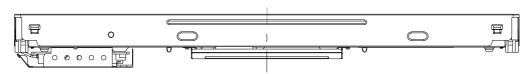


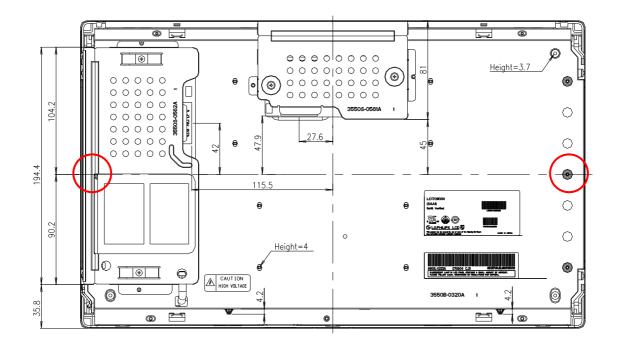


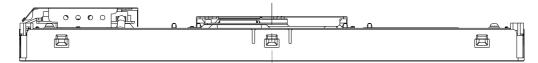




#### <REAR VIEW>

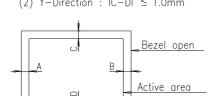






NOTES

- Unspecified tolerances are to be ±0.5mm.
   Tilt and partial disposition tolerance of display area are as following. (1) X-Direction :  $|A-B| \le 1.0$ mm (2) Y-Direction :  $|C-D| \le 1.0$ mm



### 6. Reliability

#### Table 13. ENVIRONMENT TEST CONDITION

No.	Test Item	Condition
1	High temperature storage test	Ta= 60 ℃, 500h
2	Low temperature storage test	Ta= -20℃, 500h
3	High temperature operation test	Ta= 50 ℃, 80%RH, 500h Ta= 60 ℃, 500h(2000h)
4	Low temperature operation test	Ta= 0 ℃, 500h(1000h)
5	Heat cycle test	Ta= -20 ℃ ~ 60 ℃, 30min/5min/30min, 100cycles
6	Soldering heat cycle test	Ta= -40 ℃ ~ 80 ℃, 30min/5min/30min, 200cycles
7	Vibration test (non-operating)	Wave form : random Vibration level : 1.0Grms Bandwidth : 10-300Hz Duration : X,Y,Z, 30 min One time each direction
8	Shock test (non-operating)	Shock level : 120Grms Waveform : half sine wave, 2ms Direction : ±X, ±Y, ±Z One time each direction
9	ESD test	Condition : 150pF, 330 ohm Case , air Evaluation : ± 15kV
10	Humidity storage test	Ta= 40 ℃, 70%RH, 240h

Note : Before and after Reliability test, LCM should be operated with normal function.

### 7. International Standards

### 7-1. Safety

- a) UL 60065, 7<sup>th</sup> Edition, dated June 30, 2003, Underwriters Laboratories, Inc., Standard for Audio, Video and Similar Electronic Apparatus.
- b) CAN/CSA C22.2, No. 60065:03, Canadian Standards Association, Standard for Audio, Video and Similar Electronic Apparatus.
- c) IEC60065:2001, 7<sup>th</sup> Edition CB-scheme and EN 60065:2002, Safety requirements for Audio, Video and Similar Electronic Apparatus.

### 7-2. EMC

- a) ANSI C63.4 "Methods of Measurement of Radio-Noise Emissions from Low-Voltage Electrical and Electrical Equipment in the Range of 9kHZ to 40GHz. "American National Standards Institute(ANSI), 1992
- b) CISPR13 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
   CISPR22 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" International Special Committee on Radio Interference.
- c) EN55013 "Limits and Methods of Measurement of Radio interference characteristics of Sound and Television broadcast receivers and associated equipment"
   EN55022 "Limits and Methods of Measurement of Radio interference characteristics of Information Technology Equipment" European Committee for Electro Technical Standardization.(CENELEC), 1988(Including A1:2000)

### 8. Packing

### 8-1. Designation of Lot Mark

a) Lot Mark



A,B,C : SIZE(INCH)

D : YEAR

- F : PANEL CODE
- H : ASSEMBLY CODE

E : MONTH G : FACTORY CODE I,J,K,L,M : SERIAL NO.

Note

1. YEAR										
Year	2001	2002	2003	2004	2005	2006	2007	2008	2009	2010
Mark	1	2	3	4	5	6	7	8	9	0

#### 2. MONTH

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Mark	1	2	4	4	5	6	7	8	9	А	В	С

b) Location of Lot Mark

Serial NO. is printed on the label. The label is attached to the backside of the LCD module. This is subject to change without prior notice.

### 8-2. Packing Form

a) Package quantity in one Pallet : 7 pcs

b) box Size : 375 mm X 320 mm X 484 mm.

### 9. Precautions

Please pay attention to the followings when you use this TFT LCD module.

### 9-1. Mounting Precautions

- (1) You must mount a module using specified mounting holes (Details refer to the drawings).
- (2) You should consider the mounting structure so that uneven force (ex. Twisted stress) is not applied to the module. And the case on which a module is mounted should have sufficient strength so that external force is not transmitted directly to the module.
- (3) Please attach the surface transparent protective plate to the surface in order to protect the polarizer. Transparent protective plate should have sufficient strength in order to the resist external force.
- I ransparent protective plate should have sufficient strength in order to the resist externa
- (4) You should adopt radiation structure to satisfy the temperature specification.
- (5) Acetic acid type and chlorine type materials for the cover case are not desirable because the former generates corrosive gas of attacking the polarizer at high temperature and the latter causes circuit break by electro-chemical reaction.
- (6) Do not touch, push or rub the exposed polarizers with glass, tweezers or anything harder than HB pencil lead. And please do not rub with dust clothes with chemical treatment. Do not touch the surface of polarizer for bare hand or greasy cloth.(Some cosmetics are detrimental to the polarizer.)
- (7) When the surface becomes dusty, please wipe gently with absorbent cotton or other soft materials like chamois soaks with petroleum benzine. Normal-hexane is recommended for cleaning the adhesives used to attach front / rear polarizers. Do not use acetone, toluene and alcohol because they cause chemical damage to the polarizer. \* Panel push force can be guaranteed under 5kgf / \$\phi10mm\$
- (8) Wipe off saliva or water drops as soon as possible. Their long time contact with polarizer causes deformations and color fading.
- (9) Do not open the case because inside circuits do not have sufficient strength.

### 9-2. Operating Precautions

- The spike noise causes the mis-operation of circuits. It should be lower than following voltage : V=± 200mV(Over and under shoot voltage)
- (2) Response time depends on the temperature.(In lower temperature, it becomes longer.)
- (3) Brightness depends on the temperature. (In lower temperature, it becomes lower.)
- And in lower temperature, response time(required time that brightness is stable after turned on) becomes longer
- (4) Be careful for condensation at sudden temperature change.Condensation makes damage to polarizer or electrical contacted parts. And after fading condensation, smear or spot will occur.
- (5) When fixed patterns are displayed for a long time, remnant image is likely to occur.
- (6) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference shall be done by system manufacturers. Grounding and shielding methods may be important to minimized the interference.
- (7) Please do not give any mechanical and/or acoustical impact to LCM. Otherwise, LCM can't be operated its full characteristics perfectly.
- (8) A screw which is fastened up the steels should be a machine screw.
- (if not, it can causes conductive particles and deal LCM a fatal blow)
- (9) Please do not set LCD on its edge.
- (10) It is recommended to avoid the signal cable and conductive material over the inverter transformer for it can cause the abnormal display and temperature rising.
- (11) Partial darkness may happen during 3~5 minutes when LCM is operated initially in condition that luminance is under 40% at low temperature (under 5 °C). This phenomenon is not a problem about reliability but LCD characteristic which disappears naturally after 3~5 minutes

### 9-3. Electrostatic Discharge Control

Since a module is composed of electronic circuits, it is not strong to electrostatic discharge. Make certain that treatment persons are connected to ground through wrist band etc. And don't touch interface pin directly.

### 9-4. Precautions for Strong Light Exposure

Strong light exposure causes degradation of polarizer and color filter.

### 9-5. Storage

When storing modules as spares for a long time, the following precautions are necessary.

- (1) Store them in a dark place. Do not expose the module to sunlight or fluorescent light. Keep the temperature between 5℃ and 35℃ at normal humidity.
- (2) The polarizer surface should not come in contact with any other object.It is recommended that they be stored in the container in which they were shipped.

### 9-6. Handling Precautions for Protection Film

- (1) The protection film is attached to the bezel with a small masking tape. When the protection film is peeled off, static electricity is generated between the film and polarizer. This should be peeled off slowly and carefully by people who are electrically grounded and with well ionblown equipment or in such a condition, etc.
- (2) When the module with protection film attached is stored for a long time, sometimes there remains a very small amount of glue still on the bezel after the protection film is peeled off.
- (3) You can remove the glue easily. When the glue remains on the bezel surface or its vestige is recognized, please wipe them off with absorbent cotton waste or other soft material like chamois soaked with normal-hexane.

#### **# APPENDIX-I-1**

Required signal assignment for Flat Link (DS90C385) Transmitter(Pin9="L")

Host System		DS90C385				Timing
24 Bit		Compatible	FI-X	30SSL	-HF	Controller
RED0	51					
RED1	52	TxOUT0-	48	12		RxIN0-
RED2	<u>54</u>	TxOUT0+	47	13	100 <b>Ω</b> ≶	RxIN0+
RED3						
RED4	56					
RED5	3	TxOUT1-	46	15	~	RxIN1-
RED6	50	TxOUT1+	45	16	100 <b>Ω</b> ≶	RxIN1+
RED7	2					
GREEN0	4					
GREEN1	6	TxOUT2-	42	18	100 - 2	RxIN2-
GREEN2	7	TxOUT2+	41	19	100 <b>Ω</b> ≶	RxIN2+
GREEN3	11					
GREEN4	12					
GREEN5	14	TxCLKOUT-	40	21		RxCLKIN-
GREEN6	8	TxCLKOUT+	39	22	100 <b>Ω</b> ≶	RxCLKIN+
GREEN7	10					
BLUE0	15					
BLUE1	19	TxOUT3-	38	24	100 <b>Ω</b> ≶	RxIN3-
BLUE2	20	TxOUT3+	37	25	10022 5	RxIN3+
BLUE3	22					
BLUE4	23			9 -		
BLUE5	24			30		LCD Test
BLUE6	16					
BLUE7	18					
Hsync	27					
Vsync	28		GND	1		
Data Enable	30		55		LCD M	lodulo
CLOCK	31					louule

#### Notes:

- 1. The LCD module uses a 100  $Ohm(\Omega)$  resistor between positive and negative lines of each receiver input.
- 2. Refer to LVDS transmitter data sheet for detail descriptions. (THC63LVD823 or Compatible)
- 3. '7' means MSB and '0' means LSB at R,G,B pixel data.

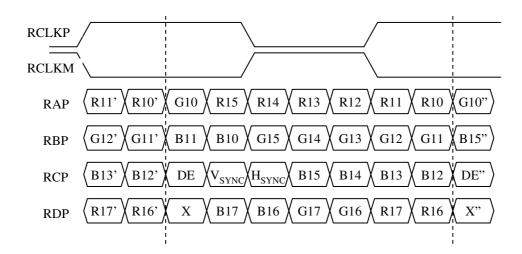
# APPENDIX- -I-2

## LVDS Data-Mapping info. (8bit)

RCLKP RCLKM G12 G12" (R13') R12' R17 ) R15 R14 R13 R12 R16 RAP G14' X G13' B13 B12 G17 G16 G15 G14 G13 B13" RBP B15' X B14' DE (V<sub>SYNC</sub>/H<sub>SYNC</sub>/ B17 B16 B15 B14 DE" RCP R11' R10' Х B11 B10 G11 G10 R11 R10 Х" RDP

LVDS Select : "H" Data-Mapping (JEIDA format)

■ LVDS Select : "L" Data-Mapping (LG format, VESA)

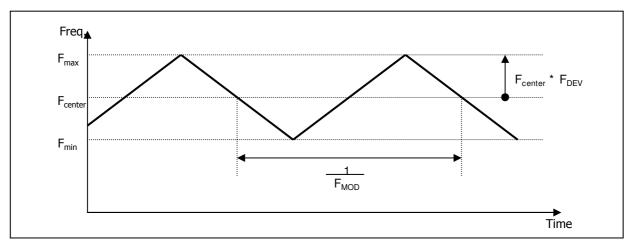


### # APPENDIX- -I-3

# **LVDS** Input characteristics

### 1. AC Specification

1) Spread Spectrum

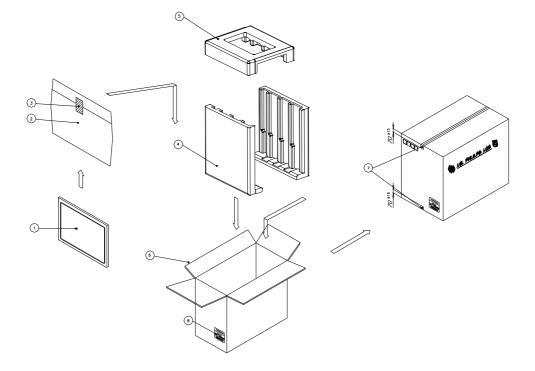


< Spread Spectrum >

Description	Symbol	Min	Max	Unit	Notes
Maximum deviation of input clock frequency during SSC	F <sub>DEV</sub>	-	± 2	%	-
Maximum modulation frequency of input clock during SSC	F <sub>MOD</sub>	-	300	KHz	-

### # APPENDIX- || -1

Packing Ass'y

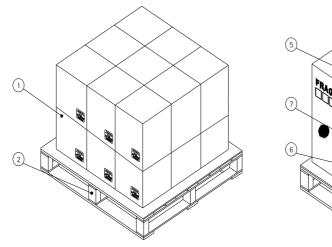


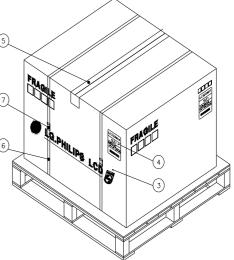
NO.	DESCRIPTION	MATERIAL	
1	LCD MODULE		
2	BAG	AL	
3	TAPE	MASKING 20MM X 50M	
4	PACKING, BOTTOM	EPS	
5	PACKING, TOP	EPS	
6	BOX	PAPER_SW3	
7	TAPE	OPP 70MMX300M	
8	LABEL	YUPO PAPER 100X100	

#### LC170WXN

### # APPENDIX- II -2

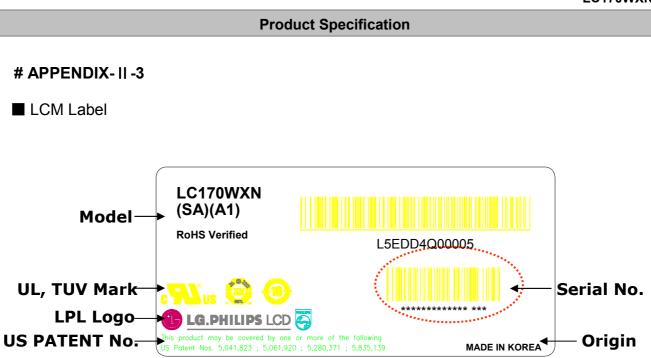
Pallet Ass'y





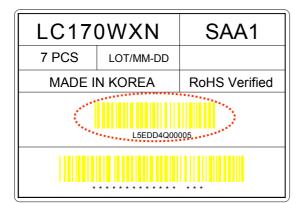
NO.	DESCRIPTION	MATERIAL
1	PACKING ASS'Y	
2	PALLET	Plywood_1140X990X117.5
3	ANGLE, PACKING	SWR4
4	LABEL	YUPO PAPER
5	TAPE	OPP
6	BAND	РР
7	BAND, CLIP	CLIP 18MM

#### LC170WXN



### # APPENDIX- || -4

### Box Label



Pallet Label

