Specification of FUJITSU TFT-LCD module

FLC43XWC6V-02

(Preliminary)

Approval										
	Approval									

This Product is designed, developed and manufactured as contemplated for general use, including without limitation, general office use, personal use, household use, and ordinary industrial use, but is not designed, developed and manufactured as contemplated for use accompanying fatal risks or dangers that, unless extremely high safety is secured, could lead directly to death, personal injury, severe physical damage or other loss (hereinafter "High Safety Required Use"), including without limitation, nuclear reaction control in nuclear facility, aircraft flight control, air traffic control, mass transport control, medical life support system, missile launch control in weapon system. If customer's product possibly falls under the category of High Safety Required Use, please consult with our sales representatives in charge before such use. In addition, Fujitsu shall not be liable against the Customer and/or any third party for any claims or damages arising in connection with the High Safety Required Use of the Product without permission.

Specification No.: Tech Bes LCD-00087

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Issued by:

T. Naka

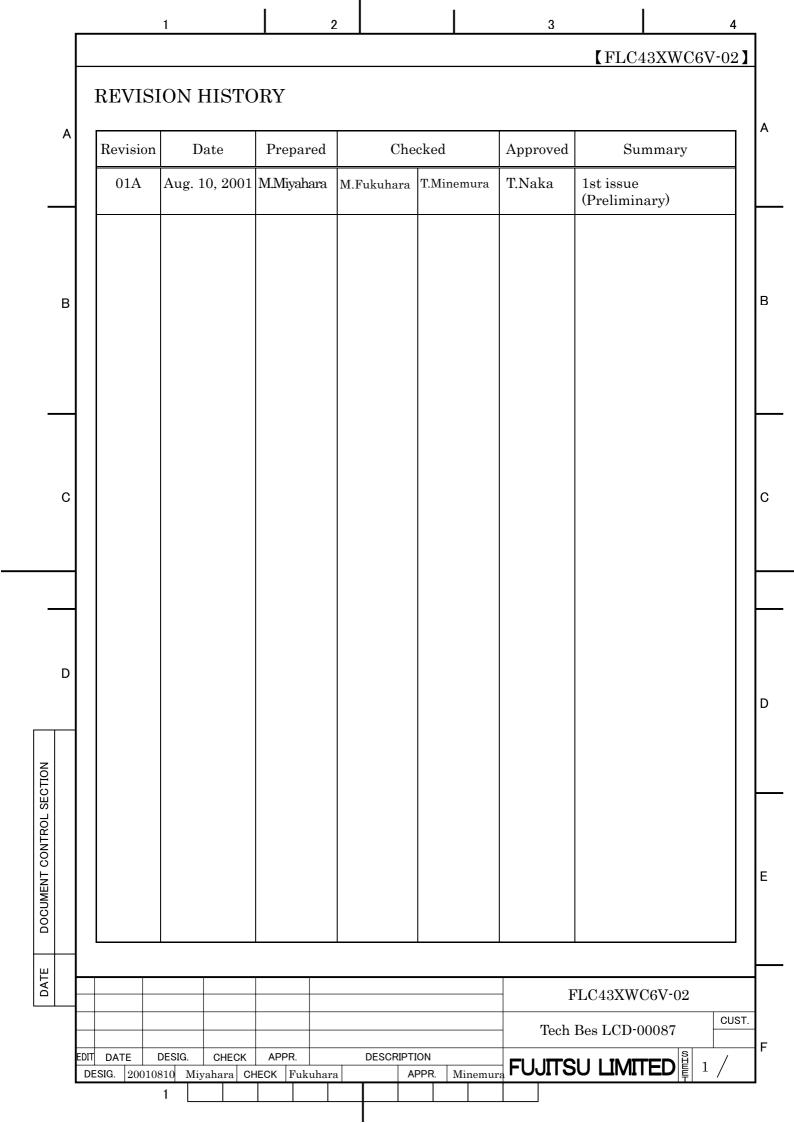
Director

LCD Design Dep.

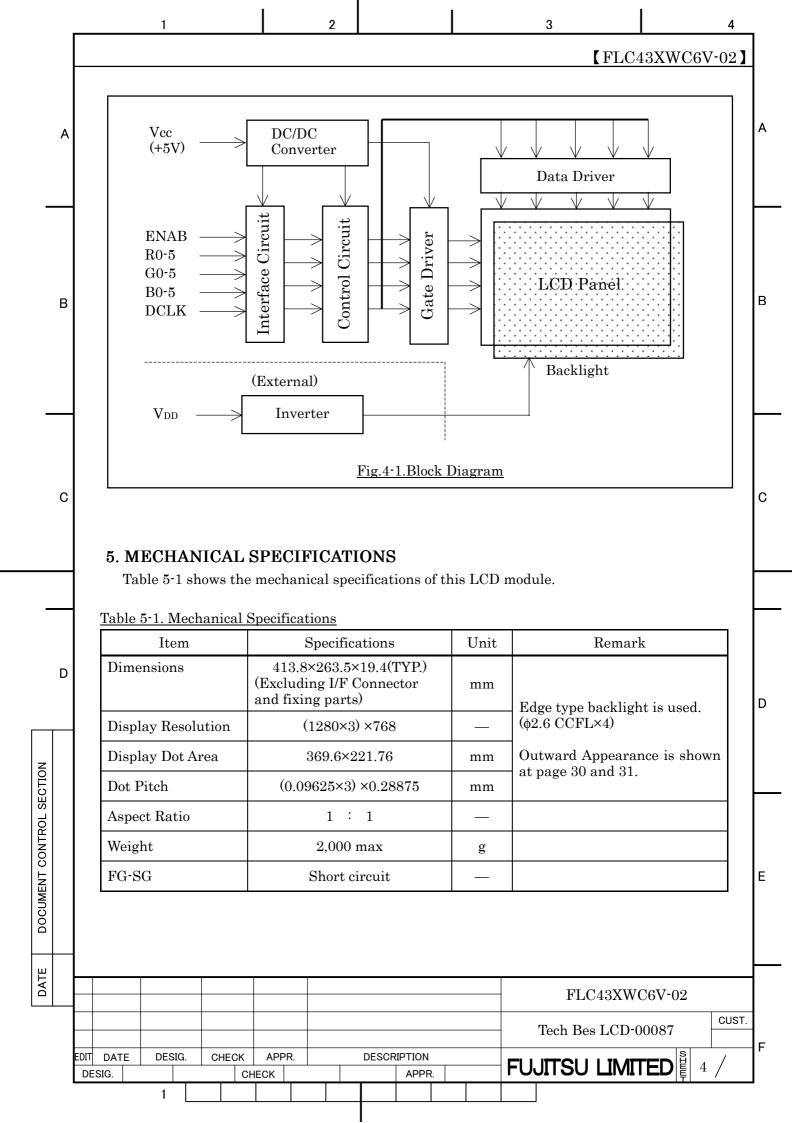
LCD Technology Div.

LCD Group

FUJITSU LIMITED

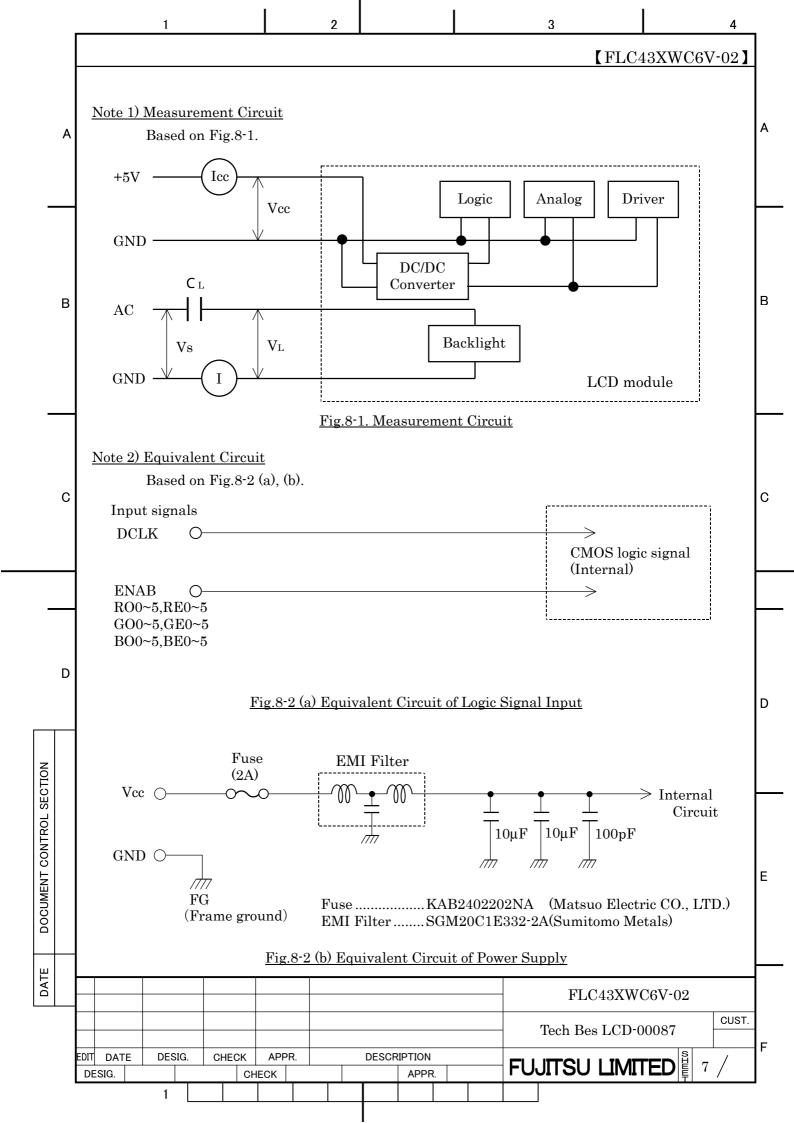


[FLC43XWC6V-02] 1. APPLICATIONS This specification is applied to the 17.0 in. XGA-WIDE supported TFT-LCD module. 2. PRODUCT NAME AND MODEL NUMBER 2-1. Product Name: LCD Module 2-2. Model Name : FLC43XWC6V-02 В В 3. OVERVIEW This LCD module has a TFT active matrix type liquid crystal panel 1280×768 pixels, and diagonal size of 43cm (17.0-inch). This module supports 1280×768 XGA-WIDE mode (Non-interlace). This LCD has a digital RGB interface and can display 262,144 colors. Timing control signal is "Data enable signal: ENAB" only. (Data enable mode) Even and odd data are transmitted at the same timing in the interface, so data lines are 36. (R, G, B each 6 bit ×2) The signal level of this interface is +3.3V CMOS level or 5V TTL level. С The power supply of this LCD module is +5v DC single. 4. CONFIGURATION This LCD module consists of a LCD panel, LCD driving circuit, control circuit, interface circuit and backlight unit. The LCD panel is active matrix TFT type and Fujitsu's unique MVA (Multi-domain Vertical D Alignment) liquid crystal technology is adopted in it. The LCD driving circuit is integrated in IC chips, which are bonded on plastic wiring film (hereinafter TAB driver-IC), and the output terminals of the IC chips are connected to the LCD panel. The control circuit and the interface circuit are mounted on three kinds of printed circuit board (hereinafter PCB) and the input of DOCUMENT CONTROL SECTION the TAB driver-ICs are connected to the PCBs. With such circuit construction, the image data received by the interface circuit is forwarded to the control circuit and the control circuit modulates the image data to LCD driving signals. The TAB driver ICs buffer the LCD driving signals and output driving voltages to the LCD panel. Ε These LCD parts such as the LCD panel, the TAB-ICs and the PCBs are assembled together with the backlight module in a plastic case and a metal frame. Fig.4-1 shows a block diagram of this LCD module. FLC43XWC6V-02 CUST. Tech Bes LCD-00087 EDIT DESIG. CHECK APPR. DESCRIPTION DATE FUJITSU LIMITED DESIG. CHECK APPR.

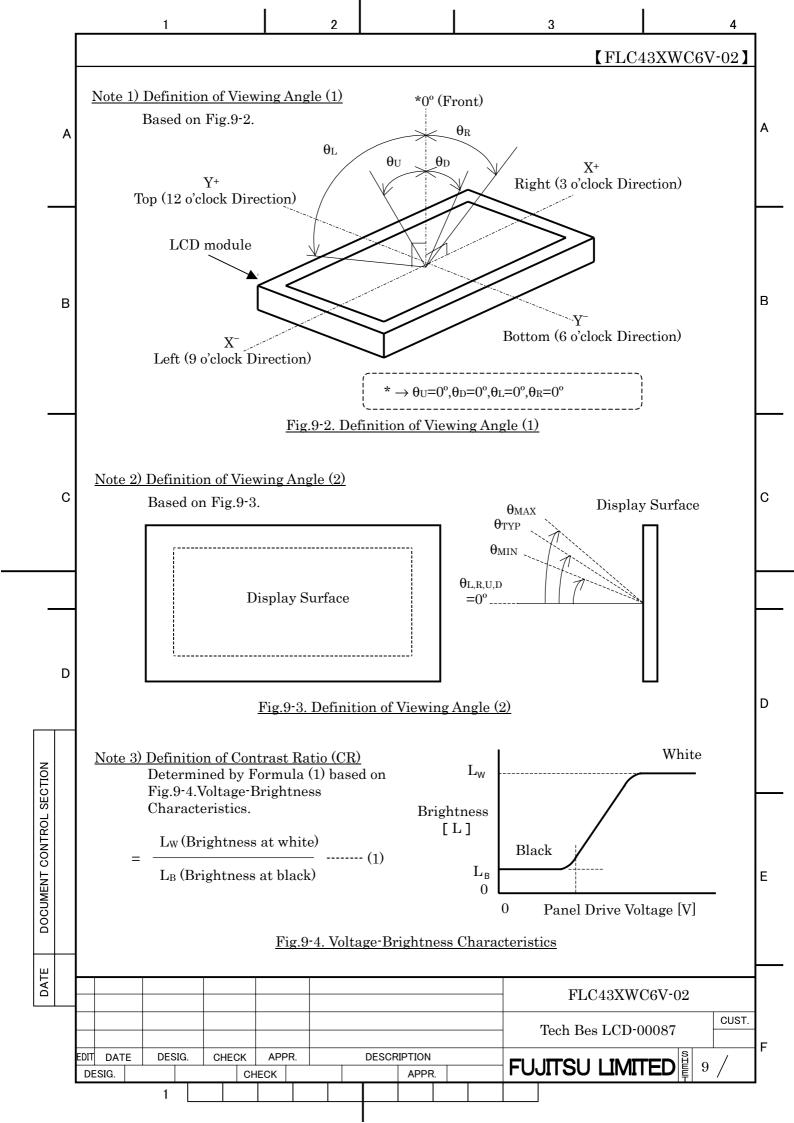


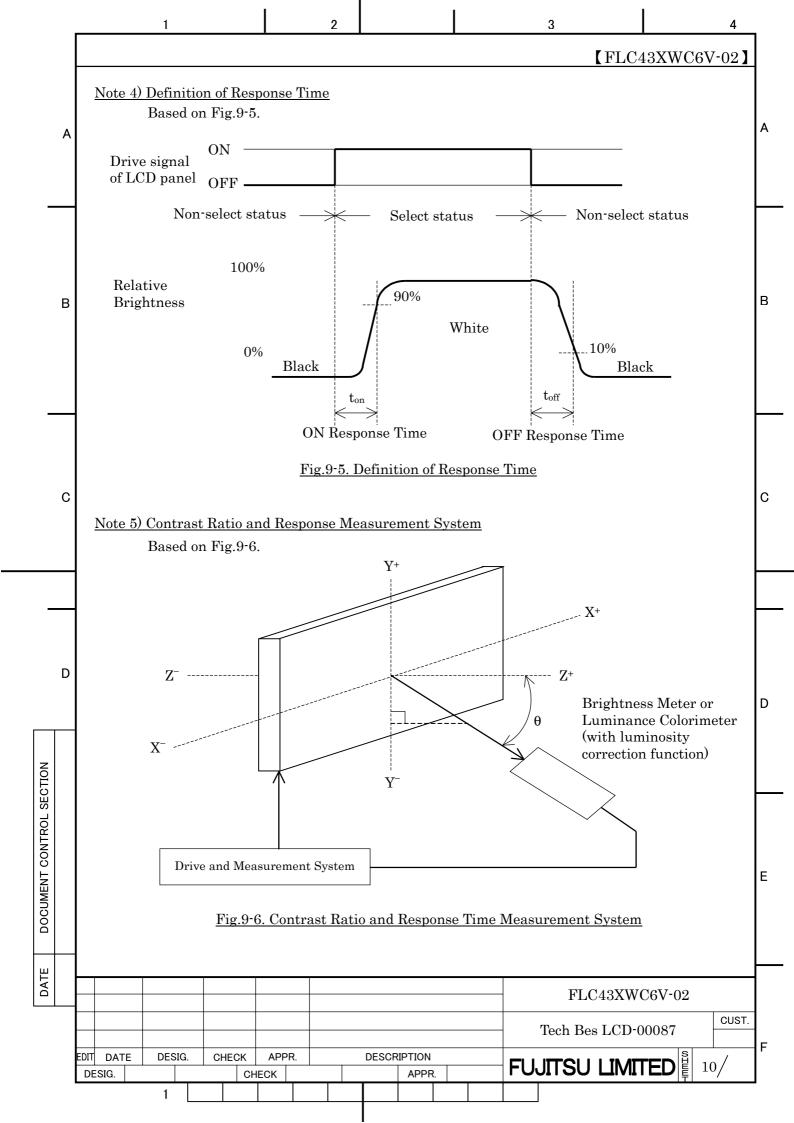
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Α	T	ABSOLUTE able 6-1 show Table 6-1. Abs	s the absolu	ite maxim	um rating o						Α
	-	Item	Sym	İ	ondition	MIN.	TYP.	MAX.	Unit		
		Supply Volta			25°C	-0.3		6.0	V		
В	7. F	Input Voltage RECOMME able 7-1 show	NDED OP	ERATIN				Vcc+0.3	V		В
	-	able 7-1 snow	s tne recom	menaea o	perating co	naitions o	I this LCL	module.			
		Table 7-1. Re		Operatin							
		G 1 77 1	Item		Symbol	MIN.	TYP.	MAX.	Unit		
С		Supply Volta			Vcc	4.75	5.0	5.25	V		С
		Ripple Volta	ige	$V_{\rm CC}$	V_{RP}		_	100	mV		
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CTION	_										D
E DOCUMENT CONTROL SECTION	_										E
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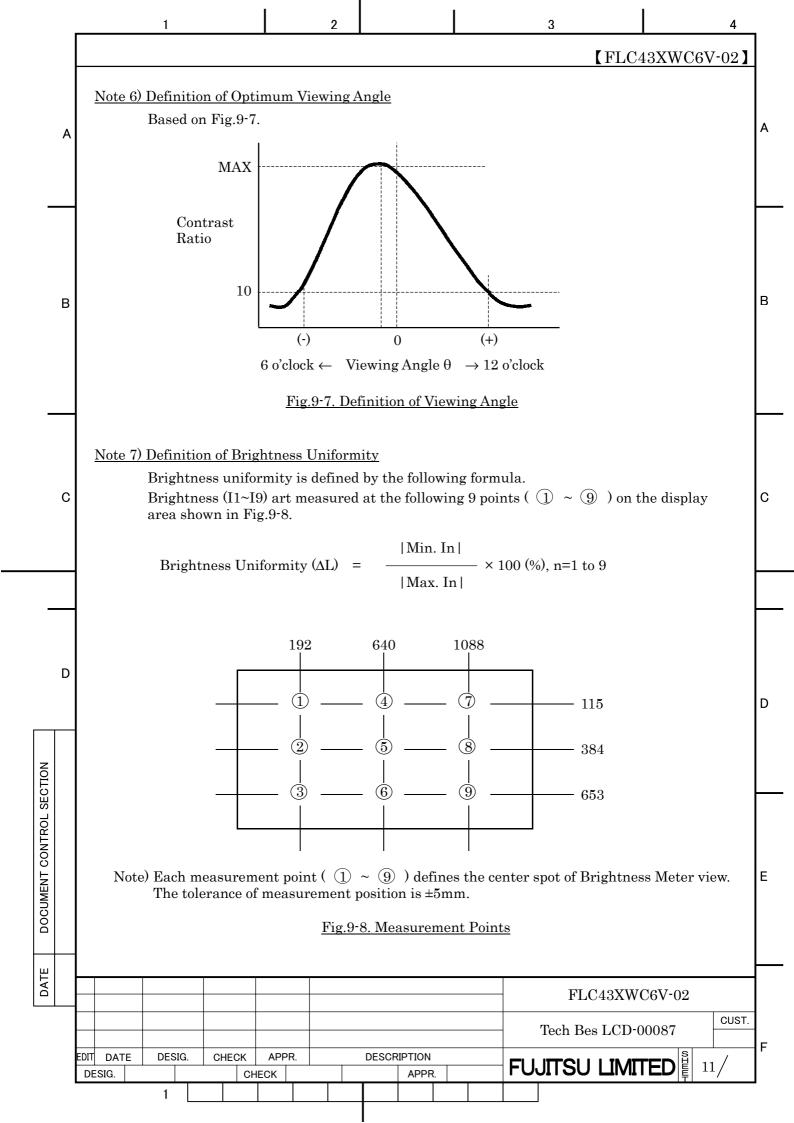
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	Α		ELECTRICAL SP Table 8-1 shows the e e 8-1. Electrical Speci	electrical s	pecification		LCD m		MAX.	Unit	Remark	A		
-		Con					WIIIN		<u> </u> 		*1			
		"H"	oply Current Carrent Logic Input tage	I _{CC}	V _{CC=} +5.0 V _{SS} =0V DCLK=3	±0.25V 2.505MHz	2.3	380	800 V _{CC}	mA V	"1			
	В	"L"	Level Logic Input tage	V _{IL}			$ m V_{SS}$	_	0.9	V		В		
		Sup	oply Rush Current	Iscc				5.5	7	A	*2			
			oply Rush Current ration (1A excess)	Tscc			_	0.4	0.15	ms	-			
_		L	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$											
	•	LIGH												
	С	BACK	Lighting Voltage	V_{L}	f_L =50kH I_L =7mA	Z	590	630	670	Vrms	*4	С		
			Lighting Frequency	f_L	V_L =580 V_L	/rms	40	50	60	kHz		L		
		*3	Tube Current	I_{L}	$f_{L}=50kH$ $V_{L}=580V$		9.5	10.5	11.0	mA	*4			
SECTION	D	(*2) (*3) (*4)	 (*1) Typical current value is measured when color bar pattern is displayed at Vcc=5.0V. Maximum current value is measured when 55/63 and 63/63 gray scale pattern every 2 pixel is displayed at Vcc=4.75V. Without rush current. (*2) These items prescribe the rush current for starting internal DC/DC. Charging current to capacitors of Vcc is not prescribed. (*3) Backlight specifications are valid when using a suitable inverter such as the "CA05951-3894" of Fujitsu Limited. (*4) Tube current (I_L) shows the value of the current that is consumed at one lamp. 									D		
DOCUMENT CONTROL S			This LCD module has 4 lamps. Each 2 lamps are placed at upper and lower side of the display. 2 lamps are connected in parallel. Each low voltage terminals are bound into 1 line cable, which connected to the backlight connector.											
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	Α	Tab	le 9-1 sł	nows 1	ECIFIC.	al specifi	S cations of th	ais LCD	module.			7	∵a=25°C	А
				ar ope				Sı	pecificatio	ns		Ren		
_			Item		Symbol	Cor	ndition	MIN.	TYP.	MAX	Unit		Note	
		Visual	Horizo	ntal	$\theta_{ m L,R}$	CR≥10	$\theta_{\rm U,D}=0^{\rm o}$	80	_	_	deg		(1)(2)	
		Angle	Vertica	al	$\theta_{\mathrm{U,D}}$	01,210	$\theta_{\rm L,R}=0^{\rm o}$	80	_	_	deg	•	(3)(5) (6)	
	В	Contra	st Ratio		CR	$\theta_{L,R,U,D}$ =	=0°	210	500	_	_	White/ Black	(1)(2) (3)(5)	В
		Respon Time(O			,	$\theta_{ m L,R,}$	Ta=25°C	_	15	30	ms	Bitten	(1)	
			(B W) $U,D=0^{\circ}$ $Ta=0^{\circ}$						50	100	ms		(4) (5)	
		Respon Time(O			${ m t}_{ m off}$	$\theta_{\mathrm{L,R,}}$	Ta=25°C	_	10	25	ms			
_		(W B)			Loff	U,D=0°	Ta=0°C		50	100	ms			
		Bright	ness		I	$\theta_{ m L,R,U,D}$ =	=0°	(340)	400	_	cd/m ²	White	(1)(5)	
	С		$\begin{array}{c ccccccccccccccccccccccccccccccccccc$					70	_	_	%	*1	(1)(5) (7)	C
		Chroma	aticity	W	X	(at max	imum orightness)	T.B.D.	T.B.D.	T.B.D.	_		(1) (5)	
					Y	,	Jiigiitiiess/	T.B.D.	T.B.D.	T.B.D.	_		(0)	
				R			Red		T.B.			1		\vdash
_				G B	(x, y)		Green White		T.B.					
		LCD Pa	anel Typ				William	TFT C		D				
	D	Display						Normally Black VA						
		Wide V	iewing A	Angle	Technolo	gy		MVA						
		Optimu	ım View	ring A	ngle			_	- (sy	mmetry)		(6)	D
		Display	Color					262,14	4 (6-	bit color	·)			
TION		Color o	f non-di	splay	area			Black						
SEC		Surface	e Treatm	nent				Anti-g	lare (Haze	e value:	25%, 2H	H)		
DOCUMENT CONTROL SECTION		(Note)	 (*1) Value at 20~30 minutes after lighting on. (Note) •CS-1000 (MINOLTA Co., Ltd.) , BM-5A(Topcon) or equivalent luminance colorimeter should be used for the measurement. Field=2°, L=500mm •The specified value of viewing angle, contrast, brightness, brightness uniformity and chromaticity are under the dark room condition (1lux or less). 											
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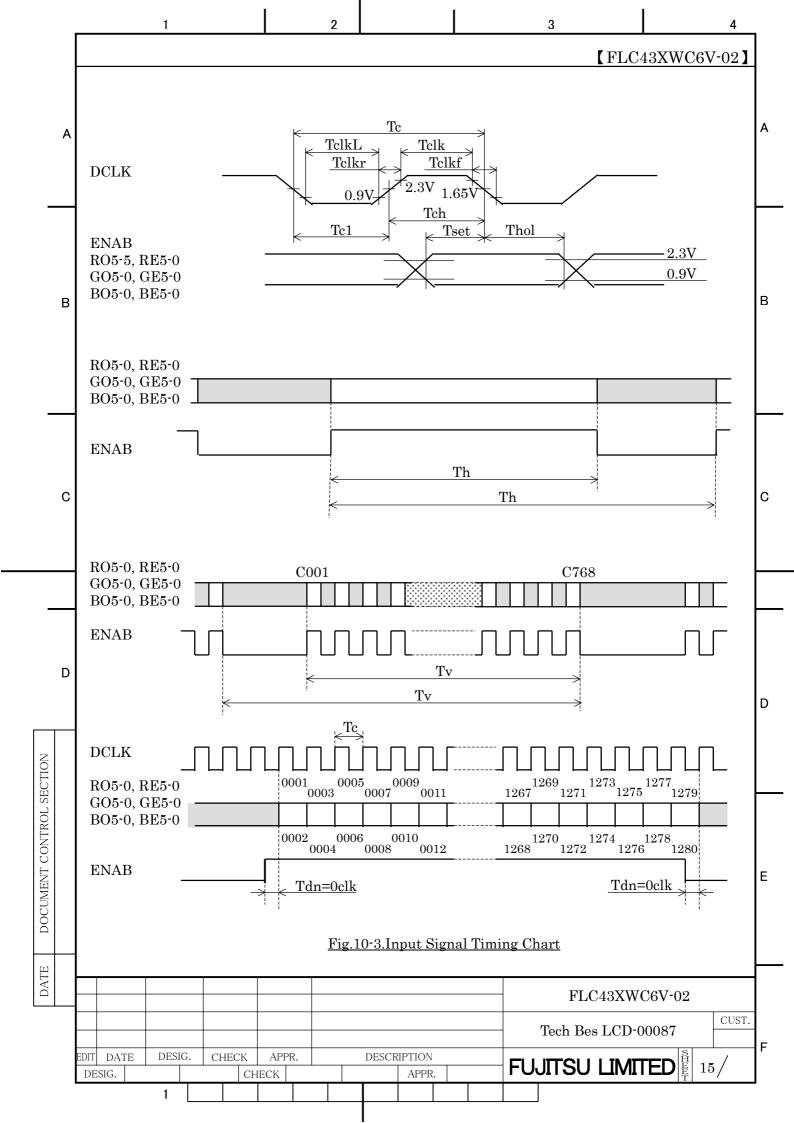


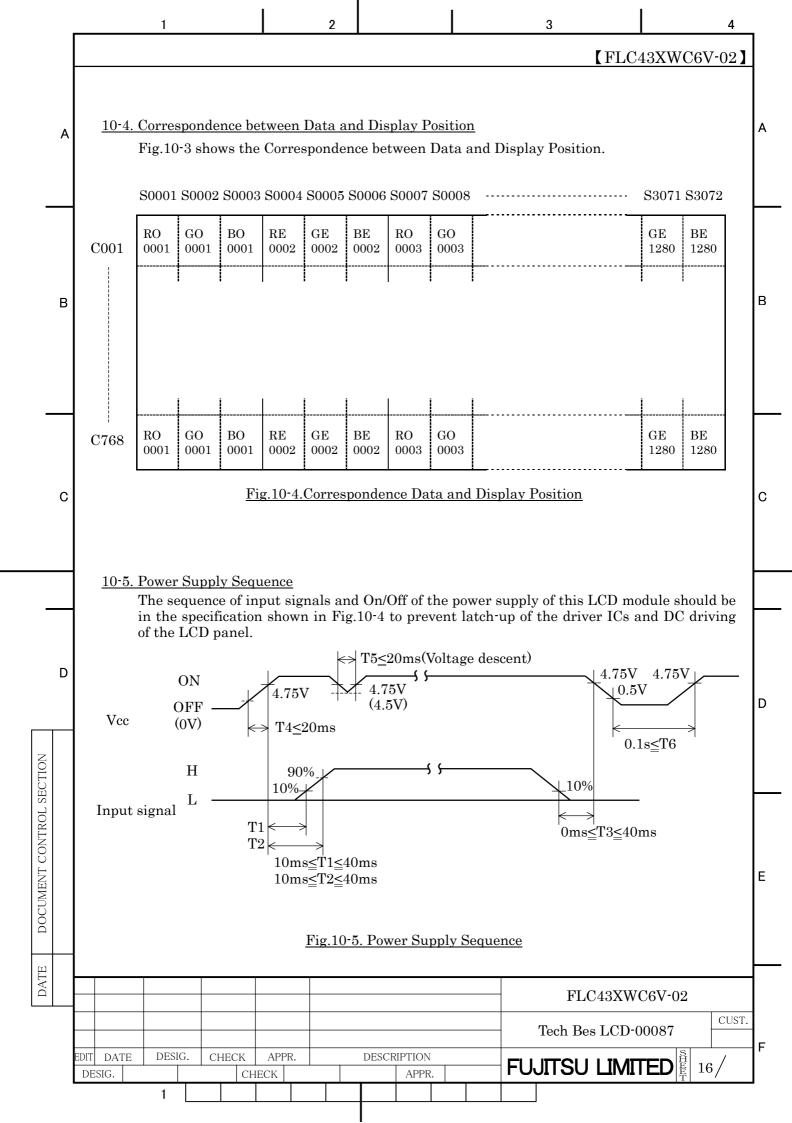




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	10. I	NTERFA	CE SI	PECIFICATIONS	3				
	10-1	. Signal de	acrinti	one					
	10 1	_	_		nd cont	figuration	of Int	erface signals (CN1).	
A	Tabla			ignals (CN1)	iu com	iiguratioi	1 01 1110	errace signals (CN1).	
	Pin				Pin		7.0	T .	
	No.	Symbol	I/O	Function	No.	Symbol	I/O	Function	
-	1	GND		Ground	31	GO1	I	Green odd data 1	
	$\frac{2}{3}$	RE0 RE1	1	Red even data 0 Red even data 1	32 33	GO2 GO3	1 T	Green odd data 2 Green odd data 3	
	4	RE2	I	Red even data 2	34	GO4	I	Green odd data 4	
	5	RE3	1	Red even data 3	35	GO5	I	Green odd data 5	
в	<u>6</u> 7	RE4 RE5	1	Red even data 4 Red even data 5	$\frac{36}{37}$	GND BO0	 -	Ground Blue odd data 0	
ا	8	GND		Ground	38	BO1	I	Blue odd data 1	
	9	GE0	1	Green even data 0	39	BO2	I	Blue odd data 2	
	10 11	GE1 GE2	1	Green even data 1 Green even data 2	40	BO3 BO4	<u> </u>	Blue odd data 3 Blue odd data 4	
	12	GE3		Green even data 3	42	BO5	I	Blue odd data 5	
	13	GE4		Green even data 4	43	GND		Ground	
	$\begin{array}{r} 14 \\ 15 \end{array}$	GE5 GND		Green even data 5 Ground	$\begin{array}{r} 44 \\ 45 \end{array}$	GND GND	+=	Ground Ground	
	16	BE0		Blue even data 0	46	ENAB	I	Data enable signal	
	17	BE1	1	Blue even data 1	47	GND	<u> </u>	Ground	
С	18 19	BE2 BE3	1	Blue even data 2 Blue even data 3	48	GND DCLK		Ground Dot clock signal	
٦	$\frac{10}{20}$	BE4	1	Blue even data 4	50	GND	<u> </u>	Ground	
	21	BE5		Blue even data 5	51	GND		Ground	
	$\frac{22}{23}$	GND RO0		Ground Red odd data 0	52 53	SS GND	 -	SS function ON/OFF (*1) Ground	
-	$\frac{23}{24}$	RO1	1	Red odd data 1	54	GND		Ground	
	25	RO2		Red odd data 2	55	GND		Ground	
\neg	$\frac{26}{27}$	RO3 RO4	1	Red odd data 3 Red odd data 4	56 57	GND VDD	+=	Ground +5V Power supply	
	28	RO5	1	Red odd data 5	58	VDD	_	+5V Power supply	
	29	GND		Ground	59	VDD	<u> </u>	+5V Power supply	
D	(*1) 6	GO0		Green odd data 0	60 ON 1	VDD	111.	+5V Power supply	M C
	("1) 8	os (<u>s</u> pread	<u>S</u> pectr					is high or N.C(generally set up	N.C.,
				SS function is C	JFF wl	hen signa	al level	is low.	
- -			TT.	1 .					
			Up	per side					
DOCUMENT CONTROL SECTION		<u> </u>	Ir	nterface connector					
		,	CD M.	31 30		Connec	aton	: 52760-0600 (Molex)	
			CD Mo Rear sid	odule					
		1	icai sic	$_{60}$ $_{1}$		User s	connec	tor: 53475-0600 (Molex)	
→			т	• 1					
			Lov	ver side					
T)								FLC43XWC6V-02	
DAIE			1	1 1					OI IOM
DAIE								Table Desired CD 00007	CUST.
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,	Table 10	10-3. Input Signal Timing Table 10-3 and Fig.10-3 shows the input signal timing. Table 10-3. Timing Characteristics (T=0~50°C, Vcc=5±0.25V)										
		<u>ing Characteristic</u> Item	Symbo	ol Min.	Тур.	Max.	$\frac{(1-0\sim50)}{\text{Unit}}$	Remark				
	DCLK signal	Period	Tc	30.764	30.764	40.000	ns	Itemark				
	(Clock)	Frequency Duty High time Low time	fc Tch/Tc TclkH TclkL	5.0	32.505 50 —	32.505 55 — —	MHz % ns ns	fc=1/Tc *1				
E		Rise time	Telkr			5.0	ns		В			
L		Fall time	Tclkf			5.0	ns	40MII				
	DCLK-Data Timing	Setup time Hold time	Tset Thold	$\begin{array}{c c} 4.5 \\ 6.5 \end{array}$	_		ns ns	40MHz 40MHz				
	Horizontal	Period	Th	672	672	1566	DCLK	fh=1/Th				
	timing	Frequency	fh	46.6	48.3	48.3	kHz	111 1/111				
	tim	Display period	Thd	640	640	640	DCLK	*2,3				
	Vertical Vertical	Period	Tv	776	806	806	Th	16.67ms				
	a-E)	Frequency	fv	60	60	60	Hz					
	Data	Display period	Tvd	768	768	768	Th	*2,3				
(Data-ENAl	B timing	Tdn	0	0	0	DCLK	*4	c			
	*2) Display pos	*1) DCLK signal input must be valid while power supply is applied. *2) Display position is specified by the ENAB signal. •Horizontal display position is specified by the rise of ENAB signal. The data of a horizontal line, which is latched by the falling edge of 1st DCLK right after the rise of ENAB, is										
					ist DCL	ix rigiit ai	ter the r	ise of ENAD, is				
	Vertica equival line aft *3) If a period displays bl *4) The display	 displayed on the left edge of the screen. •Vertical display position is specified by the rise of ENAB after a "Low" level period equivalent to eight times of horizontal period. The 1st data corresponding to one horizontal line after the rise of ENAB is displayed at the top line of screen. *3) If a period of ENAB "High" is less than 640 DCLK or less than 768 lines, the rest of the screen displays black. *4) The display position does not fit to the screen if the ENAB period and the effective data period 										
SECTION	do not sync	hronize with each	other.									
DOCUMENT CONTROL SECTION									E			
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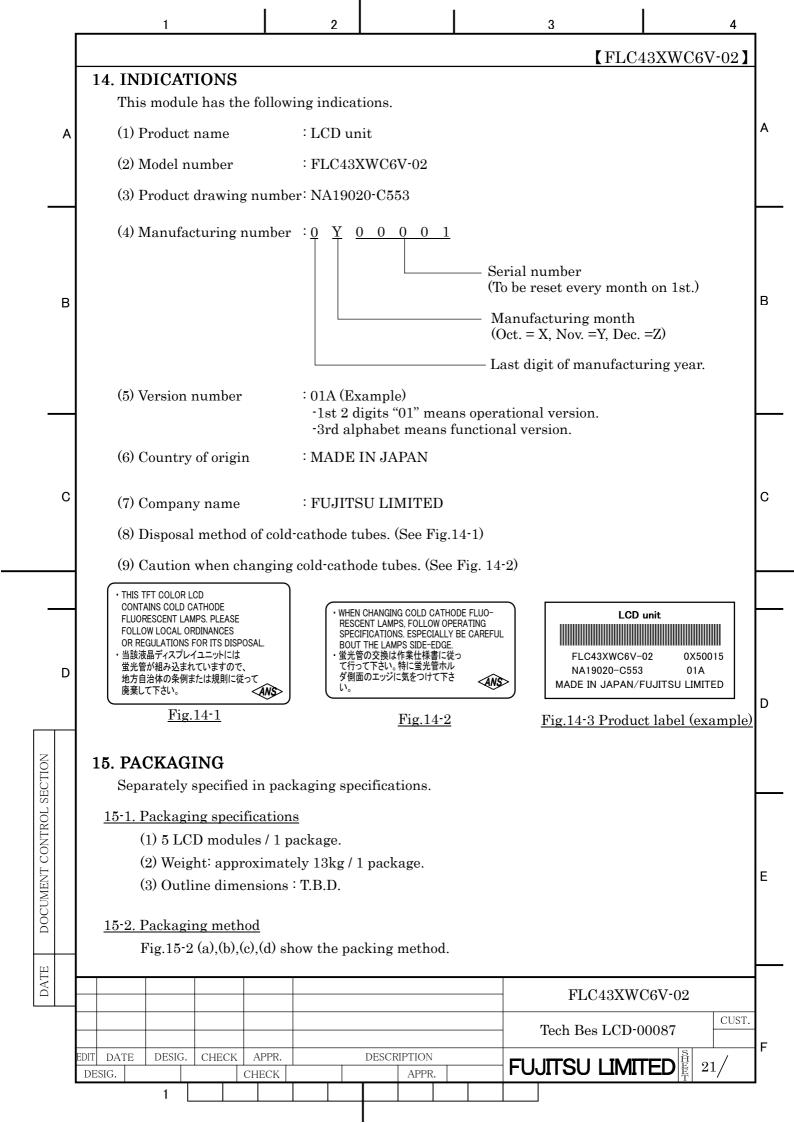


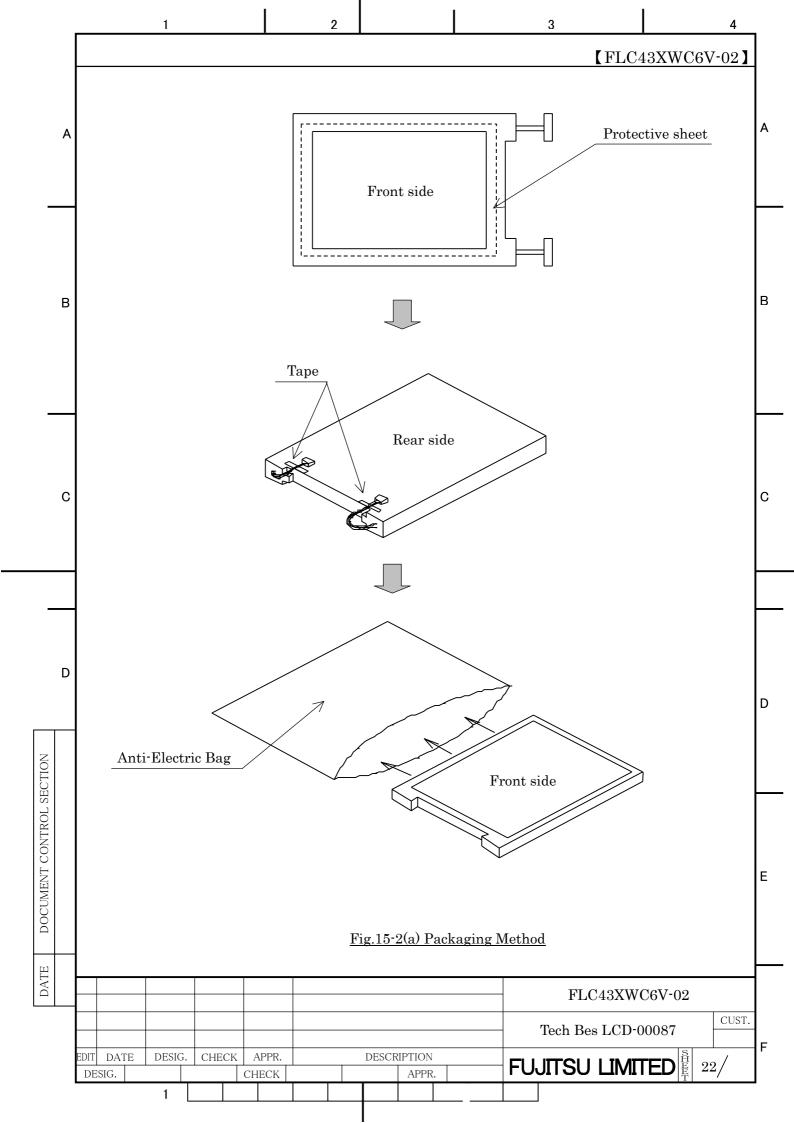
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Α		. Pin Confi Table 11-	guration for 1 (a) and 1	CIFICATION or Backlight 1-1(b) shows th light of this LO	ne description a	ınd pin assi	ignment of	the connectors	(CN-A	A
	<u>Table</u>			ent of CN-A		e11-1(b) Pir	n Assignme	ent of CN-B		
	Pin	Signal		Function	Pin	Signal		Function		
	1	$V_{\rm L}1$	Power s	supply for CCF	L 1 1	$V_{\rm L}3$	Power s	supply for CCFI	3	
В	2	$V_{\rm L}2$	Power s	supply for CCF	`L 2 2	$V_{\rm L}4$	Power s	supply for CCFI	L 4	В
	3	NC			3	NC				
	4	GND	Grou	and (for $V_{\rm L}1,2$)) 4	GND	Grou	and (for $V_{\rm L}3,4$)		
		Cable col	or (CN-A a	and B) : White	at GND, Pink	at $ m V_L1,2,3$ a	nd 4			
С	<u>11-2</u>	Supplier	: (onnector :] : ,		: BHR-04VS : SBH-001T : SM04(4.0) : ss Terminal Tr	-P0.5 B-BHS-1-T		(J.S.T.)		С
D) Working Ambie	conditions	vature $:25\pm5^\circ$	mum of 25,000 PC nA or less)	hours at th	e following	g conditions.		D
	(2	9-1. The la	tness beco	t be lit by the l	low 50% of the			value shown in	Table	
	<u>11-4</u>	Lamp Ass Lamp As This set o	sembly set	(for replacement) (with charge) an upper lamp	ent) is prepared for assembly and			ly.		E
							FLC	C43XWC6V-02		1
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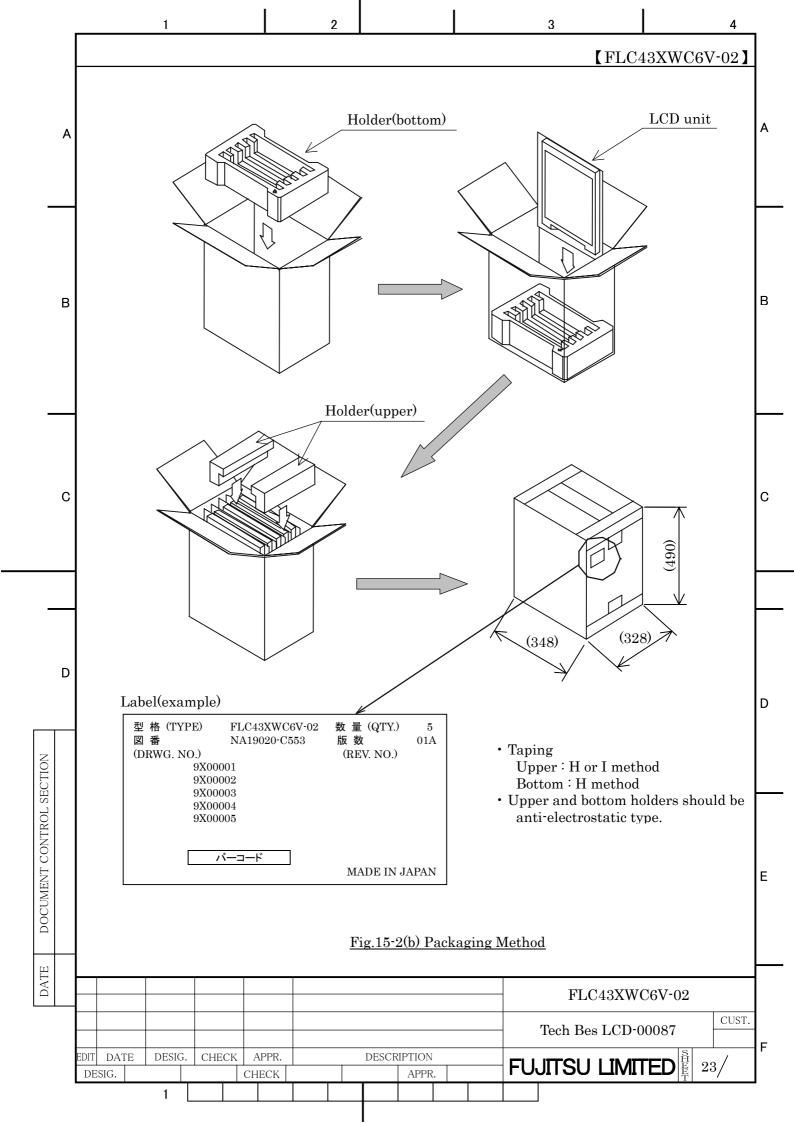
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А	12-1. Zone •Insid •Disp	le display dot lay dot area n	area (369.6× 2 neans active an of 3 dots (red,	21.8mm) ea.	e).				Α		
В	• Fore: unde modu <u>12-2. Brigh</u> (1) Brig • Vi • Vi • In (2) Brig	ign particle as r polarizer filule or polarized t spots ght spots by the sible under be sible under 5 visible under ght spots by the sp	and scratch walm but outside er film out of the defect of TF ias of 2% ND find bias of 5% ND he light passing half dot	nharmful to don't he display area display area T. Iter	isplay ir area an , etc., ard filter	d scratch one not counted. High bright Low bright Not counted	metal bezel, k l. spot R•G spot R•G filter.	-	В		
	•A] (3) Brig •Ex	half dot or les ght spots by th sceed 50µm	he light passin	g through tear	s, break	. Not counted s, etc in chro . High bright	mium mask. spot		C		
	·Inspe by a shou The	 12-3. Test condition Inspector must observe the LCD screen from the normal direction under the illumination by a single 20W fluorescent lamp. The distance between the LCD screen and the inspector should be a height of 50cm above the worktable. The vertical illuminance is 300 to 600lux (reference value). Bright spot should be counted under entire black screen. 									
	•Dark	spot should	t be counted und be counted und should be 60Hz	ler entire white					_		
D	12-4. Specif Table (Note:	fications 12-4 shows th 1) Please do Cs(suppler 2) If a pixel is with follow (a) S<	ne appearance not mistake a nental capacita s dark partiall	standard. a single bright ance) line at th y, it connects i . Only one of 4	e center nto the	of each dot. number of d	ark spots in a		D		
DOCUMENT CONTROL SECTION		— (C) 2/3 <u>≤</u> S	: Considere		е)				E		
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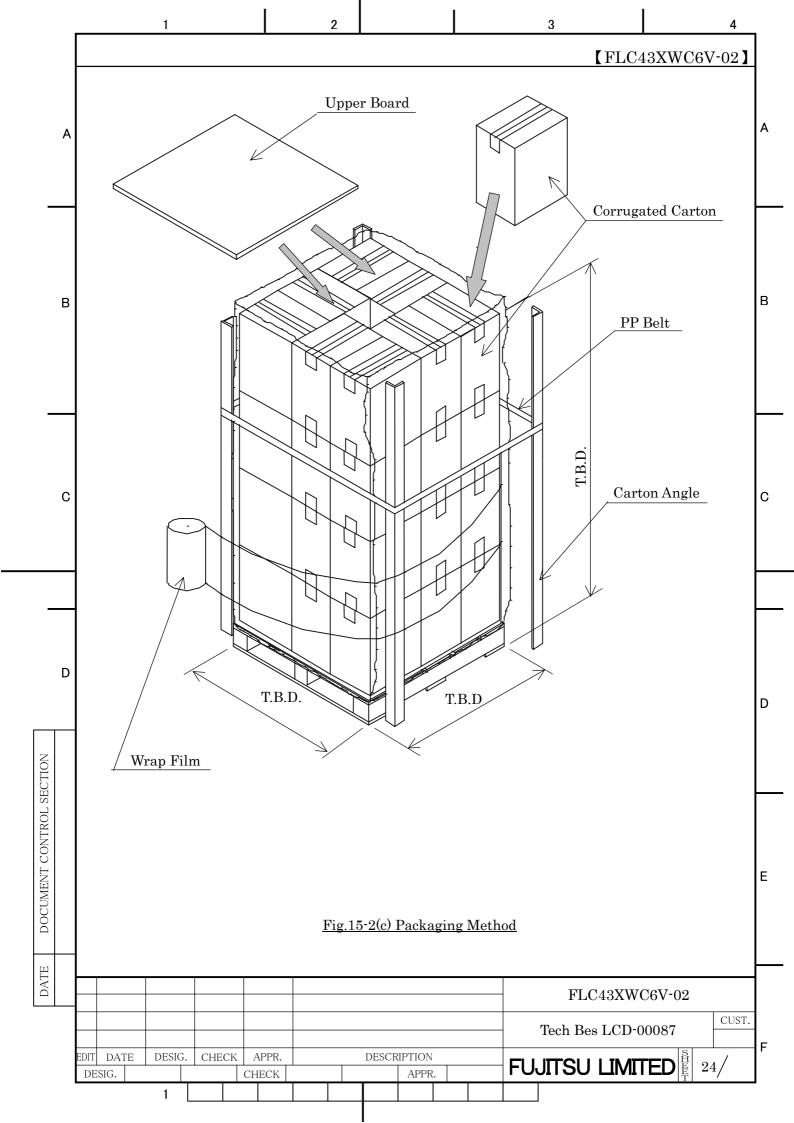
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٨			4. Appearance spe							J	A
Α	`	No.	Item			Judgment method and standard					
		1	Bright spot (high		\leq 10 dots (Note 1)						
		3	Bright spot conn (high and low)		≤2 pair (2 dot connect <7 dots	tion in l	norizontal)		(Note 1)	_	
		$\frac{3}{4}$	Total of bright sp Dark spot	oot	≤1 dots				(Note 2)		
		5	Dark spot connec	ction	≤5 pairs (2 dot connect	tion in h	norizontal)		(Note 2)		
_		6	Total of dark spo	t	≤12 dots				(Note 2)		В
В		7	Total of dot defect (bright and dark))	$\leq 12 \text{ dots}$						
		8	Distance of	high-hgh	≥10mm						
		9	bright spot	others	<u>≥</u> 3mm						
		10	Distance of dark Scratch on polari	_	<u>≥</u> 3mm	/ <u>≤</u> 0.5, L	<0.5	<u>≤</u> 5			
	-	10	line shape	.201,		0.5 <w< td=""><td><u>=0.0</u></td><td>0</td><td></td><td></td><td></td></w<>	<u>=0.0</u>	0			
						W<0.3		Ignore			
		11	Dent on polarized dot shape			D<0.5		<u>≤</u> 6			
C	;	12	Nick on polarizer line shape	c,		L<10.0		≦ 6			С
		13	Black spot			D<0.5	<u> </u>	<u>≤</u> 5			
			(Foreign circular	matter)		5 <u>≤D≤</u> 0.6 6 <d<1.6< td=""><td></td><td><u>≤</u>1 <u>≤</u>1</td><td></td><td></td><td></td></d<1.6<>		<u>≤</u> 1 <u>≤</u> 1			
	1					D<0.5	<i>.</i>	<u>≅</u> ¹ <5			
			White spot (Foreign circular	mattan)		5 ≤ D ≤ 0.8	3	<u>=</u> -			
]			matter)	0.	8 <d<u>≤1.0</d<u>)	<u>≤</u> 1			
		14	Lints, black line			0.1, L<3		<u>≤</u> 5			
D			Lints,			5, 0.1 <i< td=""><td></td><td><u>≤</u>6</td><td></td><td></td><td></td></i<>		<u>≤</u> 6			
		D:4	white spot Average diameter	[mm] W:Widt]		5, 0.5 <i< td=""><td></td><td>≤ 2</td><td>(dot size)</td><td></td><td>D</td></i<>		≤ 2	(dot size)		D
ECTION		D .1	werage diameter	[iiiii], \(\daggar\)	ir (iiiii), D'Deng	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	ij, b vorigire v	3 9 00 3120/	(dot size)		
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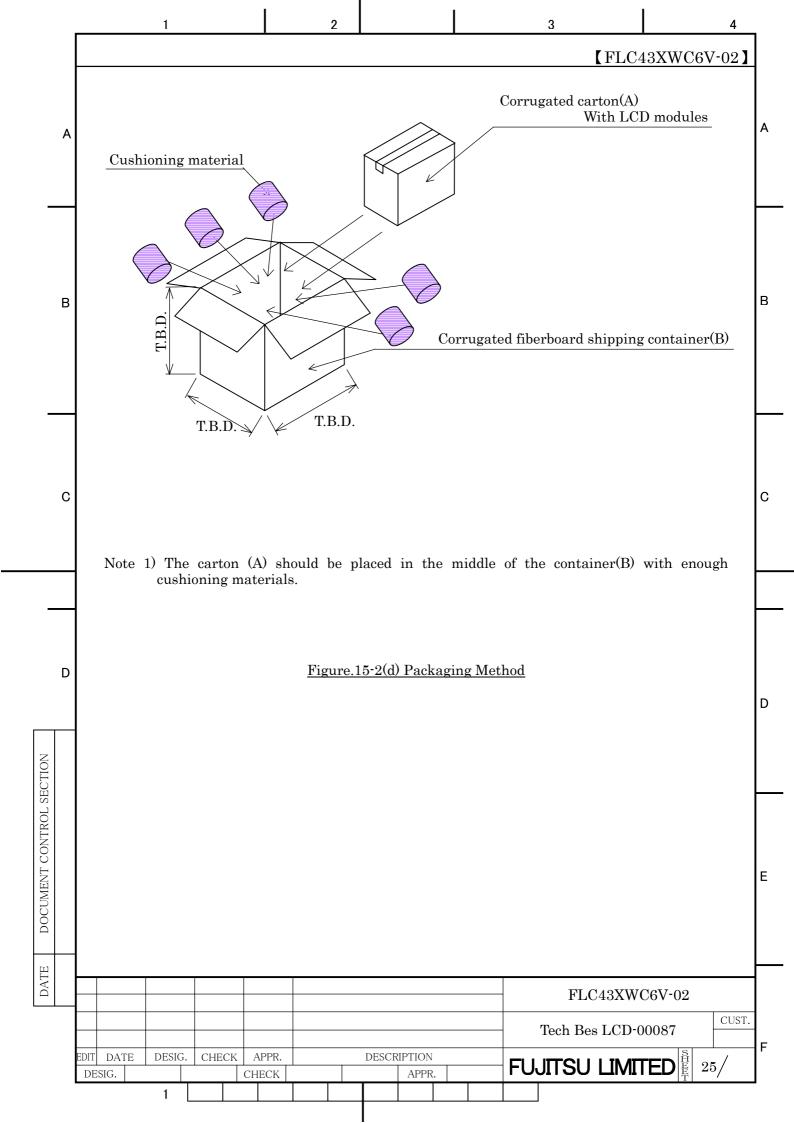
13. ENVIRONMENTAL SPECIFICATIONS Table 13:1 shows the environmental specifications. Table 13:1 shows the environmental specifications. Table 13:1 however the environmental specifications Table 13:1 however the specifications Temperature of Storage Condition Temperature of Storage Temperature of Storage Condition Condition Temperature of Storage Temper			1			2				3	4	
Table 13-1 shows the environmental specifications. Table 13-1 Environmental Specifications Remark										【FLC	43XWC6V-02	1
Hom	,	Table										A
Temperature Operation 0-50°C Temperature on surface of LCD panel (display area.) Humidity Operation 20-8596RH Maximum wet-bulb temperature should not exceed 29°C. No condensation. Non-operation 10-500Hz, 1 cycle/20minute, 2G, 1.5mm max, 2hour each X, Y and Z directions. Shock Non-operation 30G, 6ms, 1time each ± X, ± Y and ± Z directions. NOTE: Table 13-2 and Fig. 13-1 show the shock resistance standard when module is packaged. C Table 13-2. Shock Resistance Standard when Module is Packaged Dropping location Dropping height Count A - J 60cm 1 time G (Top face) J (Rear face) Fig. 13-1. Direction to apply shock to package E (Side face) Fig. 13-1. Direction to apply shock to package E FLC 43XWC6V-02 Tech Bes LCD-00087 FLC 43XWC6V-02 Tech Bes LCD-00087 FLC 43XWC6V-02 Factor Appr. Description Full Immed Package 20 Cust. Full Standard Package Full Immed Package 20 Cust. Full Standard Package Full Immed Package 20 Cust. Flc 43XWC6V-02 Tech Bes LCD-00087 Flc 43XWC6V-02 Flc 43X		Table 13-	1. Env	rironmenta	l Specific		="					1
B Storage -20-60°C LCD panel (display area.)		Item	1			Condi						
Humidity Operation 20-85%RH Maximum wet-bulb temperature should not exceed 29°C. No condensation. Non-operation 10-500Hz, 1 cycle/20minute, 2C1, 15mm max, 2hour each X, Y and Z directions. For single module without package.		Tempera	iture		ı							
Storage 5-85%RH Non-operation 10~500Hz, 1 cycle/20minute, 2G, 1,5mm max, 2hour each X, Y and Z directions Shock Non-operation 30G, 6ms, 1time each ±X, ±Y and ±Z directions.										Maximum wet-hu	lh temperature	
Vibration Non-operation 10~500Hz, 1 cycle/20minutc, 2G, 1.5mm max, 2hour each X V and Z directions. Shock Non-operation 30G, 6ms, 1 time each ± X, ± Y and ± Z directions. NOTE: Table 13-2 and Fig. 13-1 show the shock resistance standard when module is packaged. C Table 13-2. Shock Resistance Standard when Module is Packaged Dropping location Dropping height Count A ~ J 60cm 1 time G (Top face) J (Rear face) F (Side face) F (Side face) F (Side face) F (Side face) Fig. 13-1. Direction to apply shock to package FIG. 13-1. Direction to apply shock to package FLC43XWC6V-02 Tech Bes LCD-00087 F DESIG CHECK APPR DESIGN FUJITSU LIMITED \$\frac{3}{6}\$ 20/	F		у	_	1					should not exceed		В
NOTE: Table 13-2 and Fig. 13-1 show the shock resistance standard when module is packaged. C Table 13-2. Shock Resistance Standard when Module is Packaged Dropping location A ~ J G0cm 1 time C (Fidge) F (Side face) F (Side face) D (Edge) F (Side face) Fig. 13-1. Direction to apply shock to package Fig. 13-1. Direction to apply shock to package F (Side face) F (Side f			n		ation 2	G, 1.51	Hz, 1 c	ycle/20 ax, 2ho		For single module	e without	
Table 13-2. Shock Resistance Standard when Module is Packaged Dropping location Dropping height Count A - J 60cm 1 time	_	Shock		Non-opera								
G (Top face) F (Side face) F (Side face) F (Side face) D T (Front face) D (Edge) F (Side face) Fig.13-1. Direction to apply shock to package FLC43XWC6V-02 Tech Bes LCD-00087 FUJITSU LIMITED	(is packaged.	C
G (Top face) J (Rear face) C (Edge) F (Side face) E (Side face) A (Corner) Fig. 13-1. Direction to apply shock to package E FLC43XWC6V-02 Tech Bes LCD-00087 F DESIG. CHECK APPR. DESCRIPTION FUITSU LIMITED 1 20/												
Fig. 13-1. Direction to apply shock to package Fig. 13-1. Direction to apply shock to package FLC43XWC6V-02 Tech Bes LCD-00087 Tech Bes LCD-00087 FUJITSU LIMITED				A ~ J			(30cm		1 time		
Fig. 13-1. Direction to apply shock to package Fig. 13-1. Direction to apply shock to package FLC43XWC6V-02 Tech Bes LCD-00087 Tech Bes LCD-00087 FUJITSU LIMITED												
FLC43XWC6V-02 Tech Bes LCD-00087 FUJITSU LIMITED 20/			C (Edge)								D	
FLC43XWC6V-02 Tech Bes LCD-00087 FUJITSU LIMITED 20/	NOI.					/				∕ B (Edge)		
FLC43XWC6V-02 Tech Bes LCD-00087 FUJITSU LIMITED 20/	CONTROL SECT		A (Corner)									
Tech Bes LCD-00087 EDIT DATE DESIG. CHECK APPR. DESCRIPTION FUJITSU LIMITED 20/	DOCUMENT		Fig.13 1. Direction to apply snock to package									
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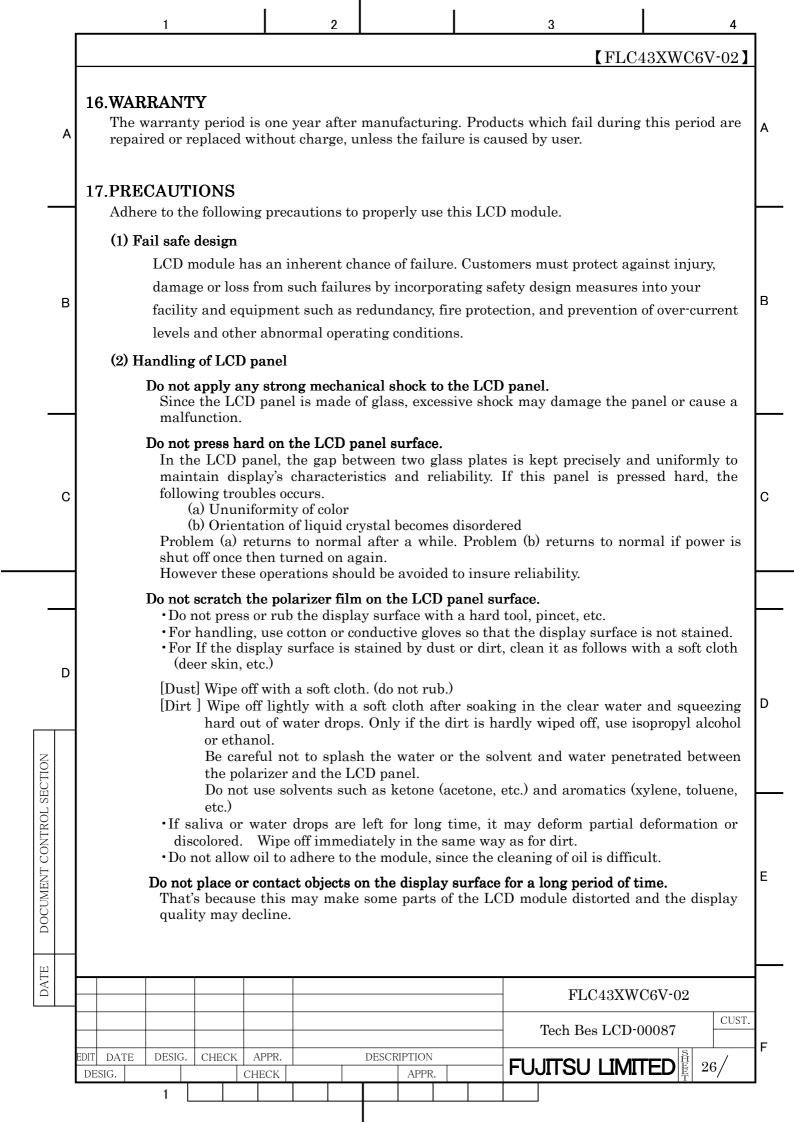


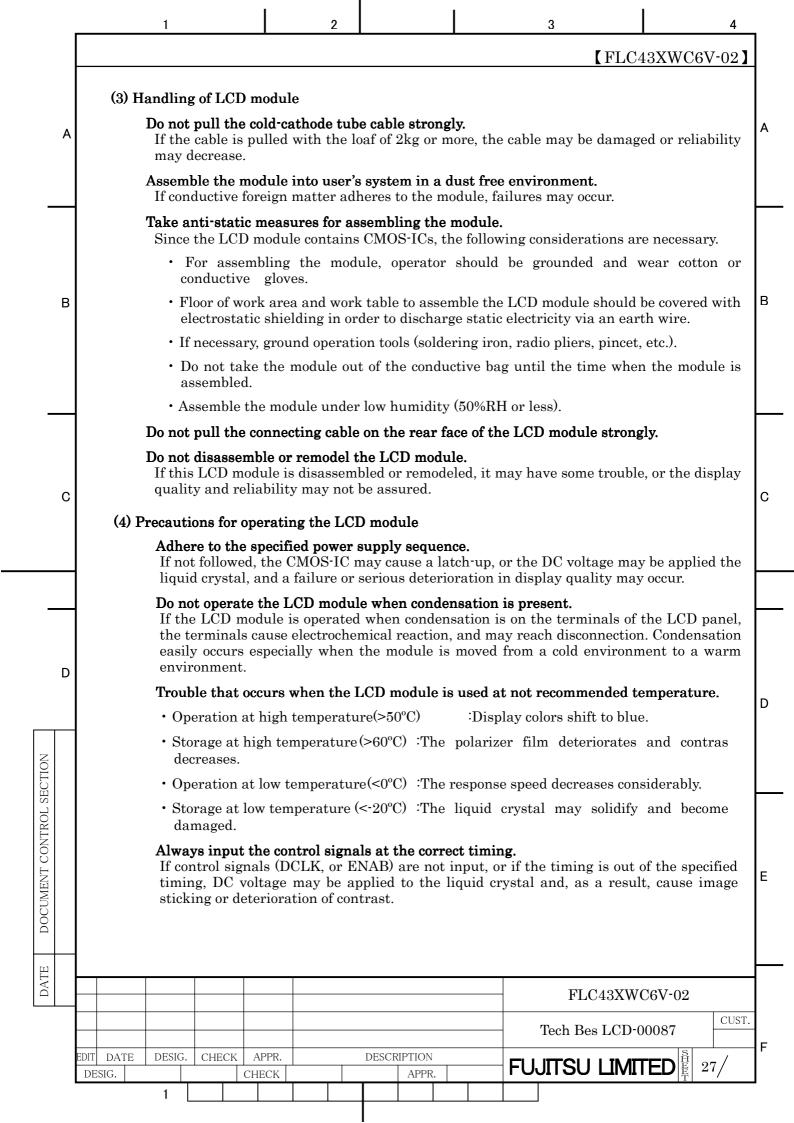


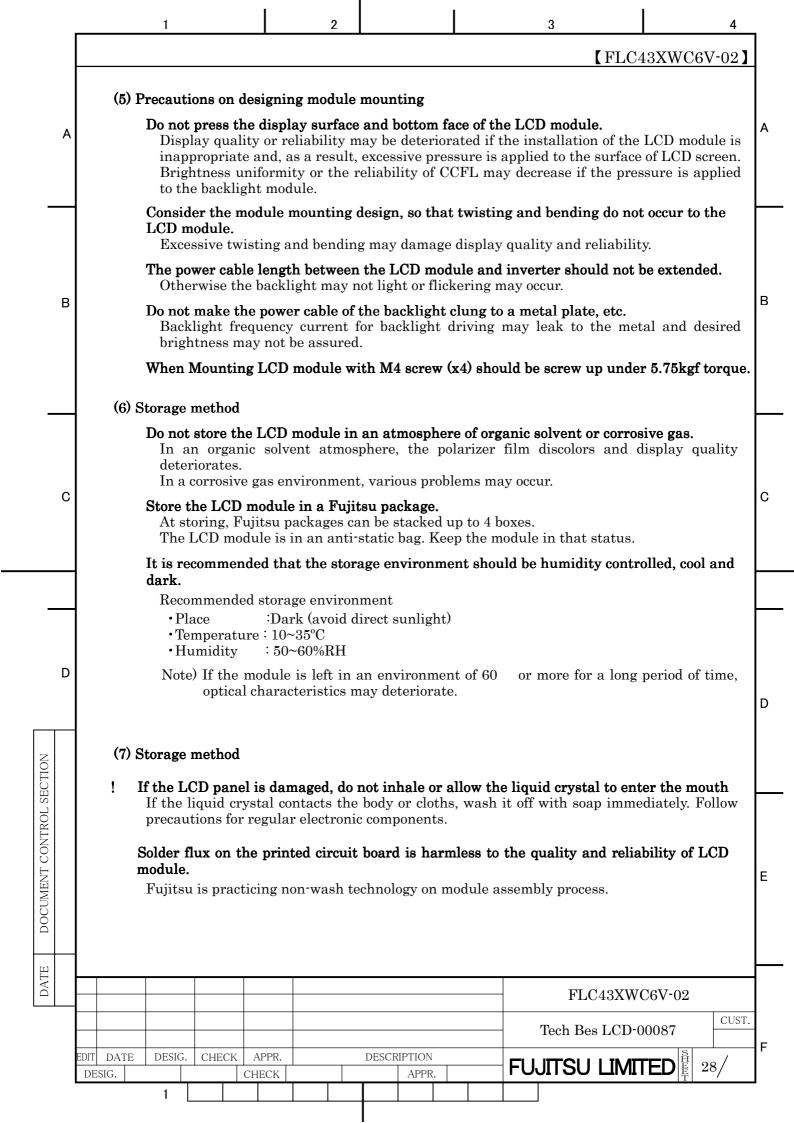




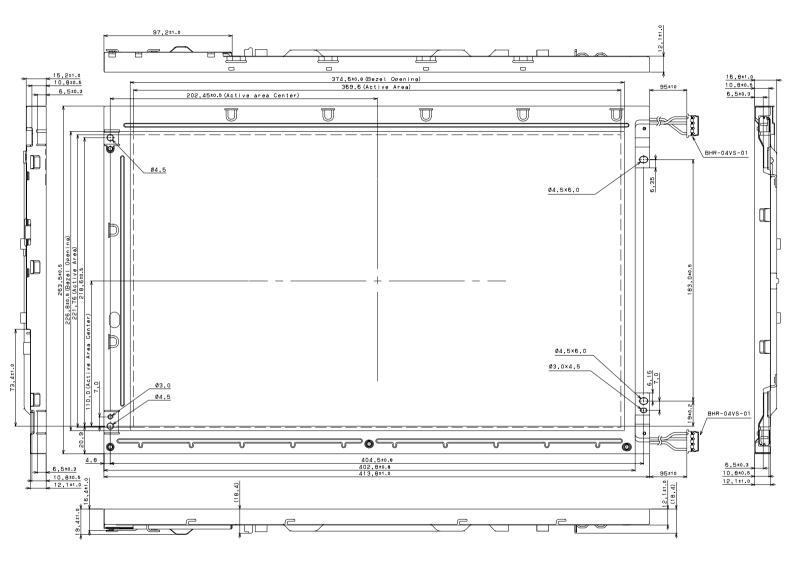








[FLC43XWC6V-02] 18. PRECAUTIONS FOR USE This Product is designed, developed and manufactured as contemplated for general use, including without limitation, general office use, personal use, household use, and ordinary industrial use, but is not designed, developed and manufactured as contemplated for use accompanying fatal risks or dangers that, unless extremely high safety is secured, could lead directly to death, personal injury, severe physical damage or other loss (hereinafter "High Safety Required Use"), including without limitation, nuclear reaction control in nuclear facility, aircraft flight control, air traffic control, mass transport control, medical life support system, missile launch control in weapon system. If customer's product possibly falls under the В category of High Safety Required Use, please consult with our sales representatives in charge before such use. In addition, Fujitsu shall not be liable against the Customer and/or any third party for any claims or damages arising in connection with the High Safety Required Use of the Product without permission. 19. MISCELLANEOUS Specifications of the TFT-LCD panel and other components used in the LCD module are subject to change. Both parties shall discuss together before change. If any doubt is raised in the content of the specifications, both parties shall discuss and make С С best effort for the agreement. D DOCUMENT CONTROL SECTION Ε DATE FLC43XWC6V-02 CUST. Tech Bes LCD-00087 EDIT DATE DESIG. CHECK APPR. DESCRIPTION FUJITSU LIMITED 29/ DESIG. CHECK APPR.



Front View

