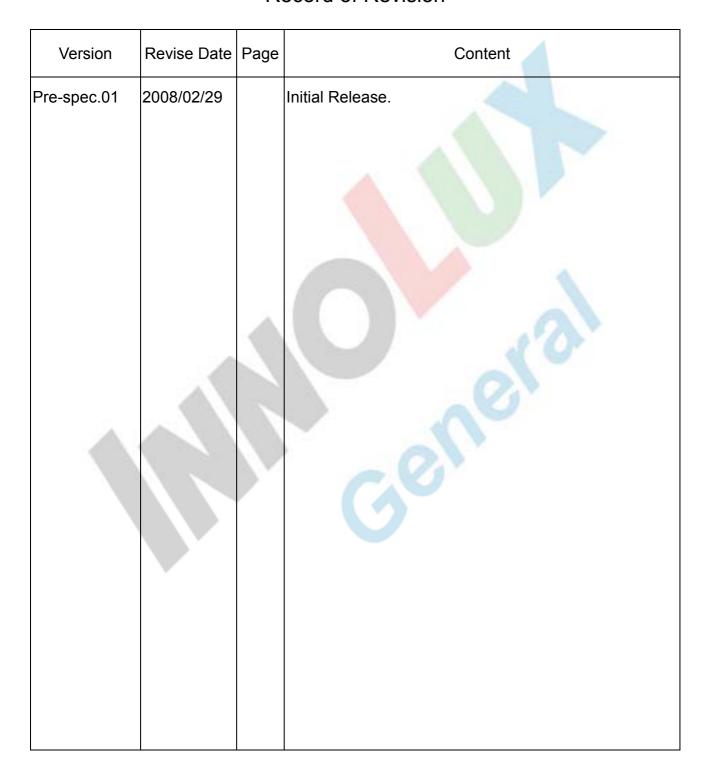
INNOLUX DISPLAY CORPORATION LCD MODULE SPECIFICATION

Customer:		<u></u>					
Model Name	: <u>ATO</u>	AT050TN33					
SPEC NO.:	<u>A05</u>	<u>0-33-TT-0</u>	<u>1</u>				
Date:	2008	2008/02/29					
Version:	<u>01</u> _						
■ Preliminary Sp □ Final Specifica		tion <ten< th=""><th>tative></th></ten<>	tative>				
Option	· 	_					
■AT050TN33 LC	М						
■Touch screen	panel						
For Customer's Acc	eptance		•				
Approved by	,	(Comment				
		·- <u>-</u> -					
Approved by	Revi	ewed by	Prepared by				
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Record of Revision





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1. General Specifications

No.	Item	Specification	Remark
1	LCD size	5.0 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	480XRGBX272	
4	Display mode	Normally White, Transmissive	
5	Dot pitch	0.077(W) X 0.231(H) mm	
6	Active area	110.88 (W)X62.832(H) mm	
7	Module size	121.8 (W)X77.5 (H)X4.85(D) mm	
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight Power consumption	TBD	
12	Panel Power consumption	TBD	
13	Weight	TBD	

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2. Pin Assignment

2.1.TFT LCD Panel Driving Section

FPC Connector is used for the module electronics interface. The recommended model

is FH19SC-40S-0.5SH(51) manufactured by HIROSE.

Pin No.	Symbol	I/O	Function	Remark
1	V_{LED}	Р	Power voltage for LED circuit	
2	V _{LED+}	Р	Power voltage for LED circuit	
3	GND	Р	Power ground	
4	V_{DD}	Р	Power voltage for analog circuit	
5	R0	I	Red data (LSB)	
6	R1	_	Red data	
7	R2	1	Red data	
8	R3	7	Red data	
9	R4	4	Red data	
10	R5	P	Red data	
11	R6		Red data	
12	R7	-	Red data (MSB)	
13	G0	I	Green data (LSB)	
14	G1	_	Green data	
15	G2	I	Green data	
16	G3	I	Green data	
17	G4	I	Green data	
18	G5	I	Green data	
19	G6	I	Green data	



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20 G7					Page:3/23
22 B1 I Blue data 23 B2 I Blue data 24 B3 I Blue data 25 B4 I Blue data 26 B5 I Blue data 27 B6 I Blue data 28 B7 I Blue data (MSB) 29 GND P Power ground 30 PCLK I Pixel clock 31 DISP I Display on/off 32 HSYNC I Horizontal Sync Signal 33 VSYNC I Vertical Sync Signal 34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	20	G7	-	Green data (MSB)	
23 B2 I Blue data 24 B3 I Blue data 25 B4 I Blue data 26 B5 I Blue data 27 B6 I Blue data 28 B7 I Blue data (MSB) 29 GND P Power ground 30 PCLK I Pixel clock 31 DISP I Display on/off 32 HSYNC I Horizontal Sync Signal 33 VSYNC I Vertical Sync Signal 34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	21	В0	I	Blue data (LSB)	
24 B3 I Blue data 25 B4 I Blue data 26 B5 I Blue data 27 B6 I Blue data 28 B7 I Blue data (MSB) 29 GND P Power ground 30 PCLK I Pixel clock 31 DISP I Display on/off 32 HSYNC I Horizontal Sync Signal 33 VSYNC I Vertical Sync Signal 34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	22	B1	-	Blue data	
25 B4 I Blue data 26 B5 I Blue data 27 B6 I Blue data 28 B7 I Blue data (MSB) 29 GND P Power ground 30 PCLK I Pixel clock 31 DISP I Display on/off 32 HSYNC I Horizontal Sync Signal 33 VSYNC I Vertical Sync Signal 34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Left side of touch panel	23	B2	_	Blue data	
26 B5 I Blue data 27 B6 I Blue data 28 B7 I Blue data (MSB) 29 GND P Power ground 30 PCLK I Pixel clock 31 DISP I Display on/off 32 HSYNC I Horizontal Sync Signal 33 VSYNC I Vertical Sync Signal 34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	24	В3		Blue data	
27 B6 I Blue data 28 B7 I Blue data (MSB) 29 GND P Power ground 30 PCLK I Pixel clock 31 DISP I Display on/off 32 HSYNC I Horizontal Sync Signal 33 VSYNC I Vertical Sync Signal 34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	25	B4	I	Blue data	
28 B7 I Blue data (MSB) 29 GND P Power ground 30 PCLK I Pixel clock 31 DISP I Display on/off 32 HSYNC I Horizontal Sync Signal 33 VSYNC I Vertical Sync Signal 34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	26	B5	I	Blue data	
29 GND P Power ground 30 PCLK I Pixel clock 31 DISP I Display on/off 32 HSYNC I Horizontal Sync Signal 33 VSYNC I Vertical Sync Signal 34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	27	B6	_	Blue data	
30 PCLK I Pixel clock 31 DISP I Display on/off 32 HSYNC I Horizontal Sync Signal 33 VSYNC I Vertical Sync Signal 34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	28	B7	_	Blue data (MSB)	
31 DISP I Display on/off 32 HSYNC I Horizontal Sync Signal 33 VSYNC I Vertical Sync Signal 34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	29	GND	Р	Power ground	
32 HSYNC I Horizontal Sync Signal 33 VSYNC I Vertical Sync Signal 34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	30	PCLK	I	Pixel clock	
33 VSYNC I Vertical Sync Signal 34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	31	DISP	¥	Display on/off	
34 DE I Data Enable 35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	32	HSYNC	7	Horizontal Sync Signal	
35 NC - No connection 36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	33	VSYNC	4	Vertical Sync Signal	
36 GND P Power ground 37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	34	DE		Data Enable	
37 X1 I/O Right side of touch panel 38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	35	NC		No connection	
38 Y1 I/O Bottom side of touch panel 39 X2 I/O Left side of touch panel	36	GND	Р	Power ground	
39 X2 I/O Left side of touch panel	37	X1	I/O	Right side of touch panel	
	38	Y1	I/O	Bottom side of touch panel	
Y2 I/O Top side of touch panel	39	X2	I/O	Left side of touch panel	
	40	Y2	I/O	Top side of touch panel	

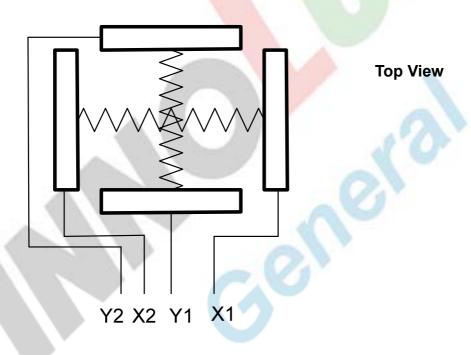
I: input, O: output, P: Power

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2.2. Touch Screen Panel Section

Symbol	I/O	Function	Remark
X1	Right	Right electrode – differential analog	
Y1	Bottom	Bottom electrode – differential analog	
X2	Left	Left electrode – differential analog	2 (0)
Y2	Тор	Top electrode – differential analog	

Note: Touch Screen Panel Block



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3. Operation Specifications

3.1. Absolute Maximum Ratings

(Note 1)

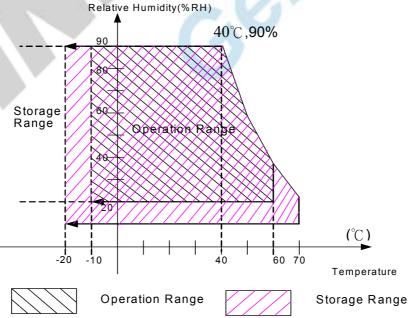
(Note 1)							
Item	Symbol	Val	Unit	Remark			
Rem	Cymbol	Min.	Max.	Onic	Koman		
Power voltage	V_{DD}	-0.5	5.0	V			
Input signal voltage	Logic input	-0.5	5.0	V			
Operation temperature	T _{OP}	-10	60	$^{\circ}\mathbb{C}$	Note 3, 4		
Storage temperature	T _{ST}	-20	70	$^{\circ}\!\mathbb{C}$	Note 3, 4		
LED Reverse Voltage	VR	-	1.2	V	Each LED Note 2		
LED Forward Current	lF	-	25	mA	Each LED		

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. A module should be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme condition, the module may be permanently destroyed.

Note 2: VR Conditions: Zener Diode 20mA

Note 3 : 90% RH Max.(Max wet temp. is 40° C)

Maximum wet-bulb temperature is at 38°C or less. And No condensation (no drops of dew)



Note 4: In case of temperature below 0°C, the response time of liquid crystal (LC) becomes slower and the color of panel darker than normal one.

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3.2. Typical operation conditions

Item	Symbol		Values	Unit	Remark	
item	Symbol	Min.	Тур.	Max.	Onit	Remark
Power voltage	V_{DD}	3.0	3.3	3.6	V	
Current for Driver	I _{DD}	-	TBD	7	mA	V _{DD} = 3.3V
Input logic high voltage	V _{IH}	0.7V _{DD}	- 1	V_{DD}	V	Note 1
Input logic low voltage	V _{IL}	GND	F.	0.3V _{DD}	V	INUIC I

Note1: PCLK,DE, HS, VS, R0~ R5,G0~ G5,B0~ B5.

3.3 Backlight Driving Conditions

Item	Symbol		Values		Unit	Remark
item	Symbol	Min.	Тур.	Max.	Offic	
LED forward voltage	V _L	18.6	19.8	21.0	V	Note 1
LED forward current	l _L	162	180	198	mA	
LED life time	-	20,000	8	_	Hr	Note 2

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25 $^{\circ}$ C and I₁ =180mA.

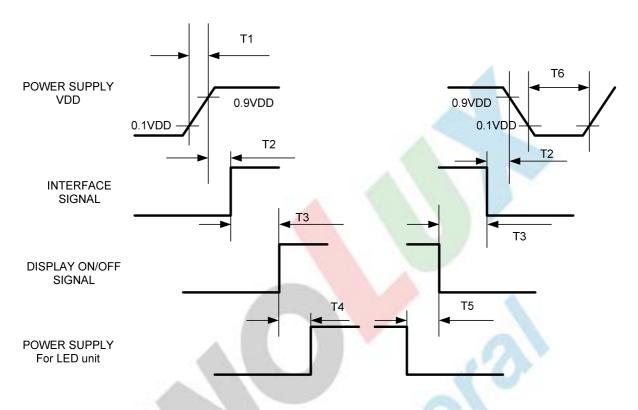
Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25 $^{\circ}$ C and I_L =180mA. The LED lifetime could be decreased if operating I_L is lager than 180 mA.



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3.4. Power Sequence

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.



Symbol	Symbol Specification		Specification
T1	0≦T1≦10 msec	T4	160 msec ≦T4
T2	0≦T2≦100 msec	T5	160 msec ≦T4
Т3	0≦T3≦200 msec	Т6	1 msec ≦T6



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3.5. Timing Characteristics

3.5.1. Timing Conditions

 $(TA = 25^{\circ}C, V_{DD} = 3.3V)$

Itam	Value			alues		Downsells
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Clock cycle	1/ t c	5	9.00	12	MHz	
Hsync cycle	1/ f н	-	17.14	-	KHz	
Vsync cycle	1/f∨	59.94	()	-	Hz	
Horizontal signal	th	- 1	525	N- 1	CLK	Note 1
Horizontal display period	t hd	1	480	<u>y-</u>	CLK	
Horizontal Front porch	t hf	2		-	CLK	Note 2
Horizontal Pulse width	thp	2	41	-	CLK	Note 2,Note3
Horizontal Back porch	t hb	2	_		CLK	Note 2,Note3
Vertical cycle	tv	-	286	Pa	Н	
Vertical display period	t vd	-	272	2	Н	
Vertical Front porch	t vf	2	2	-	Н	
Vertical Pulse width	tvp	2	10	-	Н	Note3
Vertical Back porch	t vb	2	2	-	Н	Note3
DISP Setup Time	tdiss	10	-	-	ns	
DISP Hold Time	t dish	10	-	-	ns	
Clock Period	PW CLK	66.7	-	-	ns	
Clock Pulse High Period	PWH	26.7	-	-	ns	
Clock Pulse Low Period	PWL	26.7	-	-	ns	
Hsync Setup Time	ths	10	-	-	ns	
Hsync Hold Time	thh	10	-	-	ns	



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						1 ago.o/20
Data Setup Time	t ds	10	-	ı	ns	
Data Hold Time	t dh	10	-	-	ns	
DE Setup Time	t des	10	-	-	ns	
DE Hold Time	t deh	10	-	-	ns	
Vsync Setup Time	tvhs	10	-	-4	ns	
Vsync Hold Time	tvhh	10	- , 4		ns	

Note 1: thd=480CLK, thf= 2CLK, thp= 41CLK, thb= 2CLK 525CLK=480CLK + 2CLK + 41CLK + 2CLK

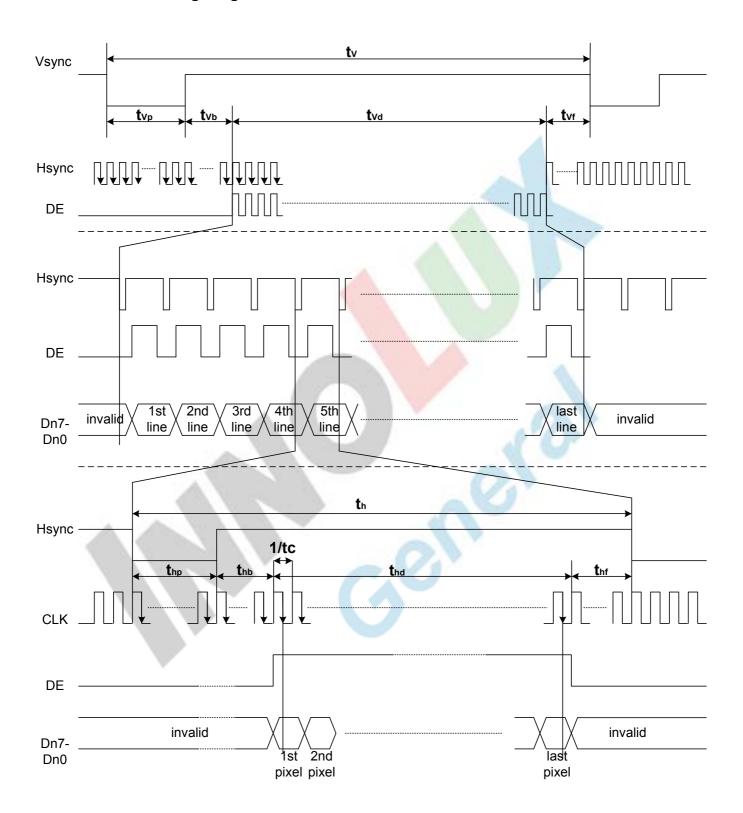
Note 2: thf+ thp+ thb> 44 CLK

Note3: It's necessary to keep $t_{Vp} + t_{Vb} = 12$ and $t_{hp} + t_{hb} = 43$ in sync mode. DE mode is unnecessary to keep.



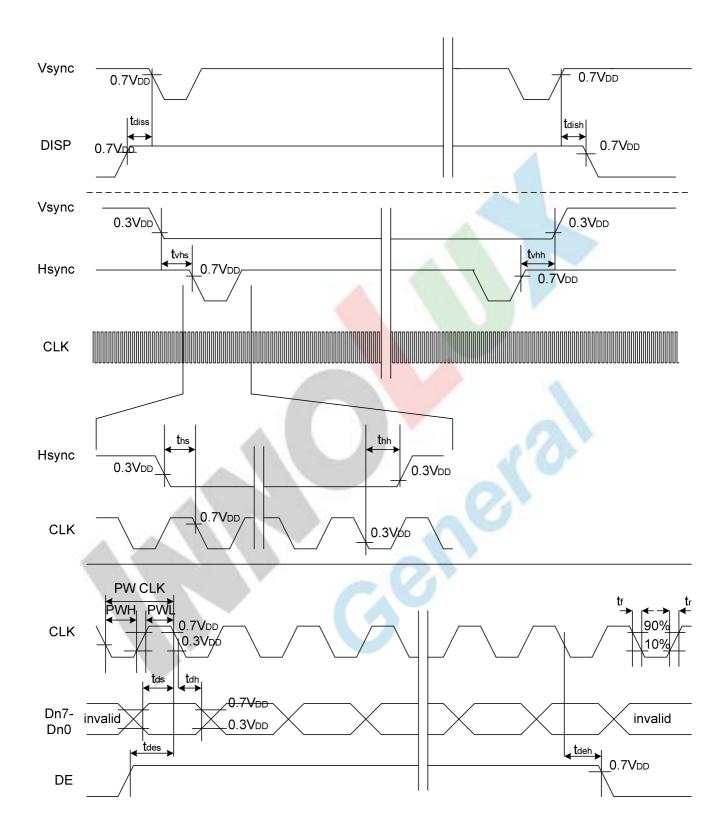
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3.5.2. Timing Diagram





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4. Touch Screen Panel Specifications

4.1. Electrical Characteristics

Item	Value			Unit	Remark	
	Min.	Тур.	Max.	Offic	Nemark	
Terminal Resistance	100	-	900	Ω	X(Film side)	
	100	-	900	Ω	Y(Glass side)	
Insulation resistance	25	-	- 1	ΜΩ	DC 25V	
Voltage	-	5	7	V	DC	
Chattering	-	-	10	ms	100kΩ pull-up	
Transparency	80	1		%	JIS K7105	

Note: Avoid operating with hard or sharp material such as a ball point pen or a mechanical pencil except a polyacetal pen (tip R0.8mm or less) or a finger.

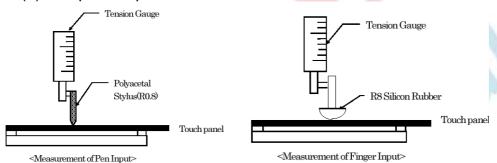
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4.2. Mechanical & Reliability Characteristics

Item	Value			Unit	Remark	
	Min.	Тур.	Max.	Offic	Kemark	
Activation force	150	-	-	gf	Note 1	
Durability-surface scratching	Write 100,000	-	-	characters	Note 2	
Durability-surface pitting	1,000,000	-	-	touches	Note 3	
Surface hardness	3	-	-	Н	JIS K5400	

Note 1: Activation force test condition

- (1) Input DC 5V on X direction, Drop off Polyacetal Stylus (R0.8), until output voltage stabilize ,then get the activation force •
- (2) R8.0mm Silicon rubber for finger Activation force test
- (3) Test point: 9 points

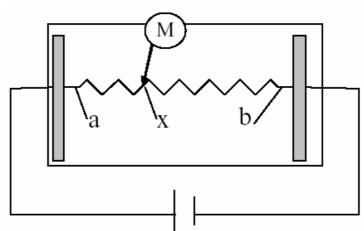


Note 2: Measurement for surface area.

- -Scratch 100,000 times straight line on the film with a stylus change every 20,000 times.
- -Force: 250gf.
- -Speed: 60mm/sec.
- -Stylus: R0.8 polyacetal tip.
- Note 3: Pit 1,000,000 times on the film with a R0.8 silicon rubber.
 - -Force: 250gf.
 - -Speed: 2times/sec.

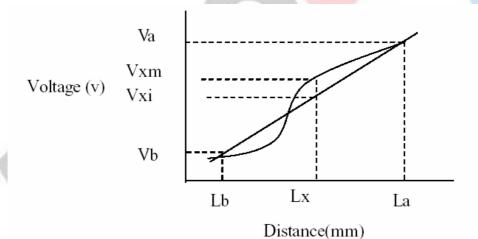
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4.3. Linearity Definition



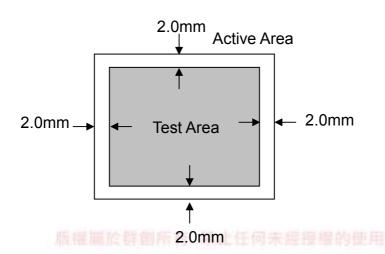
Va: maximum voltage in the active area of touch panel Vb: minimum voltage in the active area of touch panel

X: random measuring point Vxm: actual voltage of Lx point Vxi: theoretical voltage of Lx point



Linearity = [|Vxi-Vxm |/(Va-Vb)]*100%

Note: Test area is as follows and operation force is 150gf.



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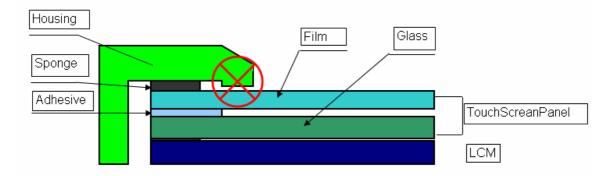


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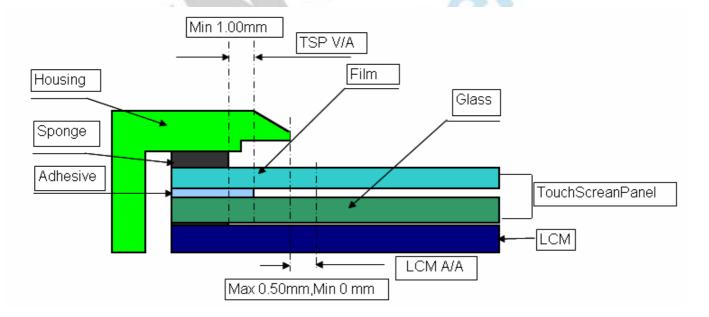
4.4. Housing design guide

Housing design follow as below

- 1) Avoid the design that housing overlap and press on the active area of the LCM
- 2) Give enough gap(over 0.5mm at compressed) between the housing and TSP to protect wrong operating.



- 3) Use a buffer material(Gasket) between the TSP and housing to protect damage and wrong operating
- 4) Avoid the design that buffer material overlap and press on the inside of TSP view area





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5. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
item		Condition	Min.	Тур.	Max.	Unit	Remark
Viewing angle (CR≥ 10)	θ_{L}	Φ=180°(9 o'clock)	60	70	4-	dograd	Note 1
	θ_{R}	Φ=0°(3 o'clock)	60	70			
	θ_{T}	Φ=90°(12 o'clock)	40	50	1-	degree	touch screen)
	θ_{B}	Φ=270°(6 o'clock)	60	70			
Response time	T _{ON}		-/	10	20	msec	Note 3
	T _{OFF}		1	15	30	msec	Note 3
Contrast ratio	CR		400	500	- 1	-	Note 4
Color chromaticity	W _X	Normal θ=Φ=0°	0.26	0.31	0.36	-	Note 2 Note 5 Note 6 (Without touch screen)
	W _Y		0.28	0.33	0.38	-	
Luminance	L ₁		230	280	-	cd/m²	Note 6 (With touch screen)
Luminance uniformity	Yu		(70)	(75)	-	%	Note 7

Test Conditions:

- 1. V_{DD}=3.3V, I_L=180mA (Backlight current), the ambient temperature is 25°C.
- 2. The test systems refer to Note 2.

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Note 1: Definition of viewing angle range

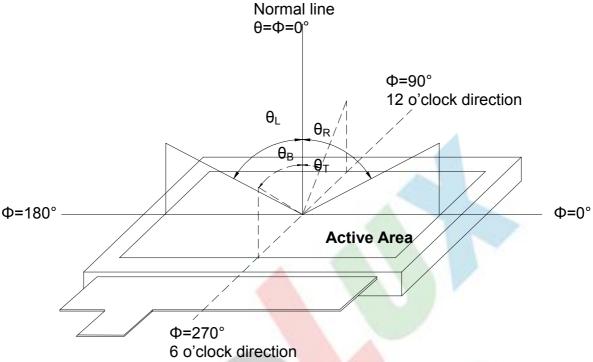


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

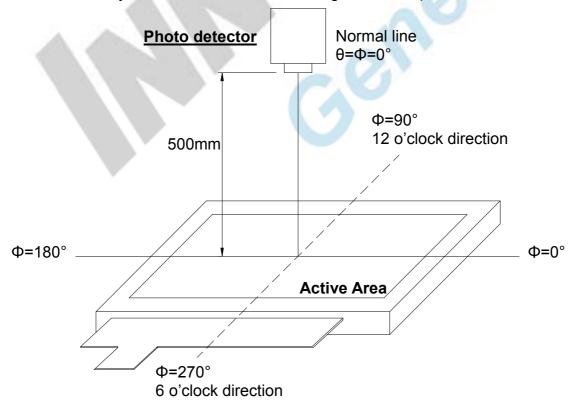


Fig. 4-2 Optical measurement system setup

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Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

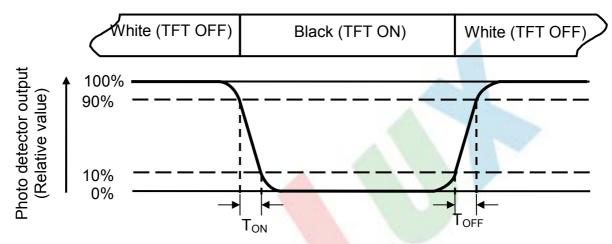


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

Contrast ratio (CR) = Luminance measured when LCD on the "White" state

Luminance measured when LCD on the "Black" state

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is I_L=180mA of which each LED module is 6 LED serial.

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Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4). Every measuring point is placed at the center of each measuring area.

Luminance Uniformity (Yu) =
$$\frac{B_{min}}{B_{max}}$$

L----- Active area length W---- Active area width

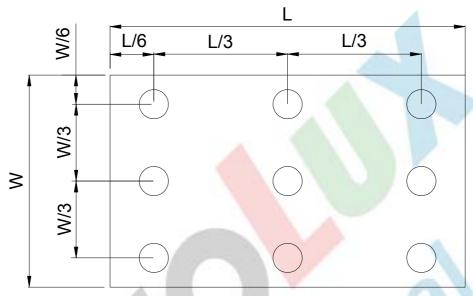


Fig. 4-4 Definition of measuring points

 \mathbf{B}_{max} : The measured maximum luminance of all measurement position. \mathbf{B}_{min} : The measured minimum luminance of all measurement position.

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6. Reliability Test Items

(Note3)

Item	Test Conditio	Remark		
High Temperature Storage	Ta = 80°C 240 hrs		Note 1,Note 4,Note 6	
Low Temperature Storage	Ta = -30°C 240hrs		Note 1,Note 4,Note 6	
High Temperature Operation	Ts = 70°C	240hrs	Note 2,Note 4,Note 6	
Low Temperature Operation	Ta = -20°℃	240hrs	Note 1,Note 4,Note 6	
Operate at High Temperature and Humidity	+40℃, 90%RH	240 hrs	Note 5,Note 6	
Thermal Shock	-30°C/30 min ~ +80°C/30 m 100 cycles, Start with cold and end with high tempera	Note 4,Note 6		
Vibration Test	Frequency range:10~55Hz Stroke:1.5mm Sweep:10Hz~55Hz~10Hz 2 hours for each direction (6 hours for total)			
Mechanical Shock	100G 6ms,±X, ±Y, ±Z 3 time direction			
Package Vibration Test	Random Vibration: 0.015G*G/Hz from 5-200H -6dB/Octave from 200-500 2 hours for each direction (6 hours for total)			
Package Drop Test	Height:60 cm 1 corner, 3 edges, 6 surfac			
Electro Static Discharge	± 2KV, Human Body Mod 100pF/1500Ω			

- Note 1: Ta is the ambient temperature of samples.
- Note 2: Ts is the temperature of panel's surface.
- Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but doesn't guarantee all the cosmetic specification.
- Note 4: Before cosmetic and function tests , the product must have enough recovery time, at least 2 hours at room temperature.
- Note 5: Before cosmetic and function tests , the product must have enough recovery time,
 - at least 24 hours at room temperature.
- Note 6:These temperature values apply to the test condition without touch screen,and with touch screen,the temperature values should be changed to:

$$T_{OP}$$
 : -10°C ~ +60°C , T_{ST} : -20°C ~ +70°C.



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7. General Precautions

7.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

7.2. Handling

- 1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- 2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
- 3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
 - 4. Keep a space so that the LCD panels do not touch other components.
- 5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
- 6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
 - 7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

7.3. Static Electricity

- 1. Be sure to ground module before turning on power or operating module.
- 2. Do not apply voltage which exceeds the absolute maximum rating value.

7.4. Storage

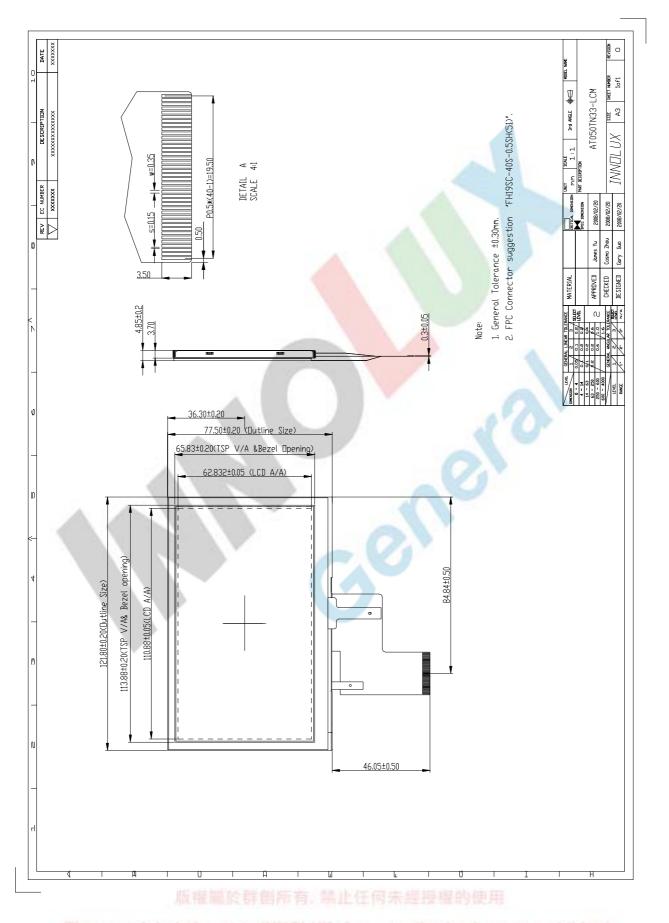
- 1. Store the module in a dark room where must keep at 25±10℃ and 65%RH or less.
- 2. Do not store the module in surroundings containing organic solvent or corrosive gas.
 - 3. Store the module in an anti-electrostatic container or bag.

7.5. Cleaning

- 1. Do not wipe the polarizer with dry cloth. It might cause scratch.
- 2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

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8. Mechanical Drawing



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9. Package Drawing

TBD

