


INNOLUX DISPLAY CORPORATION LCD MODULE SPECIFICATION

Customer: _____
 Model Name: AT043TN13
 SPEC NO.: A043-13-TT-01
 Date: 2006/11/03
 Version: 01

- Preliminary Specification
 Final Specification

For Customer's Acceptance

Approved by	Comment

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Record of Revision

Version	Revise Date	Page	Content
Pre. Spec			
01	2006/6/3		Initial Release.
02	2006/7/21		Modify Model Name from AT043TN01 to AT043TN13.
		2	Modify the definition of Pin.3,Pin.4
		5	Delete V _{GH} , V _{GL} of Power Voltage.
		6	Delete V _{COM} of Input Signal Voltage Modify V _{DD} of Input Signal Voltage
		7	Delete I _{GH} , I _{GL} of Current for Driver
		8	Modify Power Sequence
		13	Modify the Viewing Angle
		5,17	Modify the Operation Temperature.
03	2006/9/18	2	Add FPC connector type.
		19	Modify the Mechanical Drawing.
Final Spec.			
01	2006/11/3	1	Add Backlight power consumption, Panel power consumption, Weight
		5	Add the LED reverse voltage and LED forward current.
		7	Add values of current for driver Add the Min. & Max. values of LED forward voltage and LED forward current. Add note2 Modify “LED voltage” to “LED forward voltage”, and “ LED current” to “ LED forward current”
		5 17	Modify Storage temperature from “-40~95°C” to “-30~85°C”
		16	Modify note 6
		20	Add Package drawing

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1. General Specifications

No.	Item	Specification	Remark
1	LCD size	4.3 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	480X3(RGB)X272	
4	Display mode	Normally White, Transmissive	
5	Dot pitch	0.066(W)X0.198(H) mm	
6	Active area	95.04(W)X53.856(H) mm	
7	Module size	105.5(W)X67.2(H)X3.95(D) mm	Note 1
8	Surface treatment	Anti-Glare	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight Power consumption	633.6mW(Typ.)	
12	Panel Power consumption	103.2mW(Typ.)	
13	Weight	52g(Typ.)	

Note 1: Refer to Mechanical Drawing.

2. Pin Assignment

2.1. TFT LCD Panel Driving Section

1. Main-FPC Connector is used for the module electronics interface. The recommended model is FH19-40S-0.5SH(51) manufactured by HIROSE.
2. LED-FPC Connector is used for the integral backlight system. The recommended model is FH19-4S-0.5SH manufactured by HIROSE.

Pin No.	Symbol	I/O	Function	Remark
1	GND	P	Power ground	
2	GND	P	Power ground	
3	V _{DD}	P	Power supply	
4	V _{DD}	P	Power supply	
5	R0	I	Red data (LSB)	
6	R1	I	Red data	
7	R2	I	Red data	
8	R3	I	Red data	
9	R4	I	Red data	
10	R5	I	Red data	
11	R6	I	Red data	
12	R7	I	Red data (MSB)	
13	G0	I	Green data (LSB)	
14	G1	I	Green data	
15	G2	I	Green data	
16	G3	I	Green data	
17	G4	I	Green data	
18	G5	I	Green data	

19	G6	I	Green data	
20	G7	I	Green data (MSB)	
21	B0	I	Blue data (LSB)	
22	B1	I	Blue data	
23	B2	I	Blue data	
24	B3	I	Blue data	
25	B4	I	Blue data	
26	B5	I	Blue data	
27	B6	I	Blue data	
28	B7	I	Blue data (MSB)	
29	GND	P	Power ground	
30	PCLK	P	Pixel clock	
31	DISP	I	Display on/off	
32	HSYNC	I	Horizontal Sync Signal	
33	VSYNC	I	Vertical Sync Signal	
34	DE	I	Data Enable	
35	AV _{DD}	P	Power supply (+5V)	
36	AV _{DD}	P	Power supply (+5V)	
37	NC	-	No connection	
38	NC	-	No connection	
39	GND	P	Power ground	
40	GND	P	Power ground	

I: input, O: output, P: Power

2.2. Backlight Unit Section

Pin No.	Symbol	I/O	Function	Remark
1	VLED 1-	P	Ground (cathode)	
2	VLED 2-	P	Ground (cathode)	
3	VLED 2+	P	LED Input terminal (anode)	
4	VLED 1+	P	LED Input terminal (anode)	

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3. Operation Specifications

3.1. Absolute Maximum Rating

(GND=AV_{SS}=0V, Note 1)

Item	Symbol	Values		Unit	Remark
		Min.	Max.		
Power voltage	V _{DD}	-0.3	6.0	V	
	AV _{DD}	-0.3	6.0	V	
Input signal voltage	Logic input	-0.3	V _{DD} +0.3	V	
Operation Temperature	T _{OP}	-30	85	°C	
Storage Temperature	T _{ST}	-30	85	°C	
LED Reverse Voltage	V _R	-	1.2	V	each LED Note 2
LED Forward Current	I _F	-	25	mA	each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 2: V_R Conditions: Zener Diode 20mA

3.1.1. Typical Operation Conditions

(GND=AV_{SS}=0V)

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	V _{DD}	2.3	2.5	2.7	V	Note 1
		3.1	3.3	3.5	V	
	AV _{DD}	4.8	5.0	5.2	V	
Input logic high voltage	V _{IH}	0.7V _{DD}	-	V _{DD}	V	
Input logic low voltage	V _{IL}	0	-	0.3V _{DD}	V	

Note 1: You should choose only one from the typical values of V_{DD}.

3.1.2. Current Consumption

(GND=AV_{SS}=0V)

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Current for Driver	I _{DD}	-	4.0	8.0	mA	V _{DD} =2.5V
	I _{AV_{DD}}	-	16.0	32.0	mA	AV _{DD} =5.0V

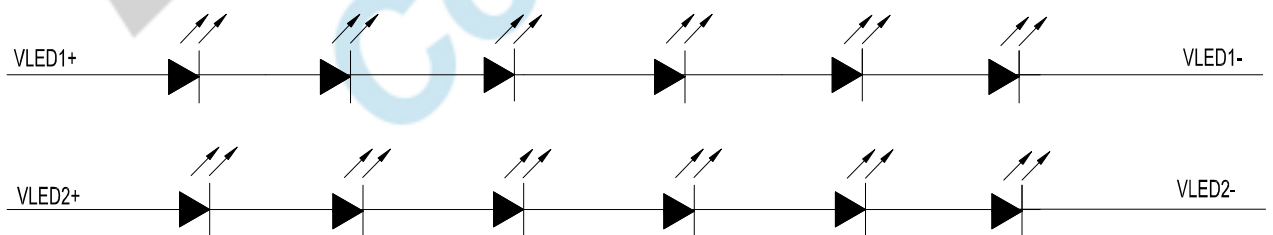
3.1.3. Backlight Driving Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
LED forward voltage	V _L	18.6	19.8	21.0	V	Note 2, 3
LED forward current	I _L	14	16	25	mA	Note 3
LED life time	-	20,000	-	-	Hr	Note 1

Note 1: The “LED life time” is defined as the module brightness decrease to 50% original brightness that the ambient temperature is 25°C and I_L =16mA. The LED lifetime could be decreased if operating I_L is larger than 16 mA.

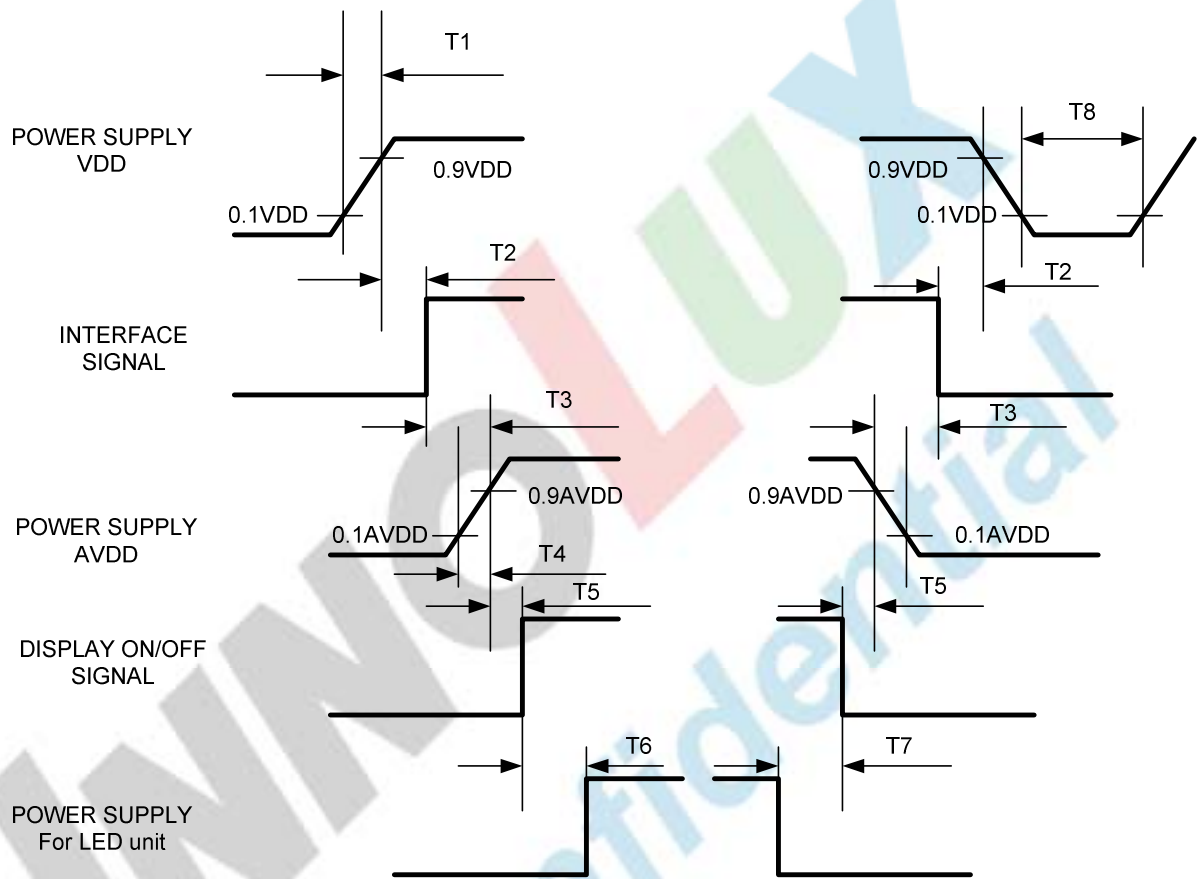
Note 2: The LED Supply Voltage is defined by the number of LED at Ta=25°C and I_L =16mA. In the case of 6pcs LED , V_L=3.3*6=19.8V

Note 3: The LED driving condition is defined for each LED module (6 LED Serial).



3.2. Power Sequence

To prevent a latch-up or DC operation of the LCD module, the power on/off sequence should be as the diagram below.



Symbol	Specification	Symbol	Specification
T1	$0 \leq T1 \leq 10 \text{ msec}$	T5	$0 \leq T5 \leq 160 \text{ msec}$
T2	$0 \leq T2 \leq 100 \text{ msec}$	T6	$160 \text{ msec} \leq T6$
T3	$0 \leq T3 \leq 50 \text{ msec}$	T7	$160 \text{ msec} \leq T7$
T4	$0 \leq T4 \leq 10 \text{ msec}$	T8	$1 \text{ msec} \leq T8$

3.3. Timing Characteristics

3.3.1. Timing Conditions

(TA = 25°C, VDD = 2.3V~3.5V, GND = 0V)

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Clock cycle	1/tc	-	9.00	15	MHz	
Hsync cycle	1/fH	-	17.14	-	KHz	
Vsync cycle	1/fv	-	59.94	-	Hz	
Horizontal signal	t _h	-	525	-	CLK	Note 1
Horizontal display period	t _{hd}	-	480	-	CLK	
Horizontal Front porch	t _{hf}	2	-	-	CLK	Note 2
Horizontal Pulse width	t _{hp}	2	41	-	CLK	Note 2
Horizontal Back porch	t _{hb}	2	-	-	CLK	Note 2
Vertical cycle	t _v	-	286	-	H	
Vertical display period	t _{vd}	-	272	-	H	
Vertical Front porch	t _{vf}	2	2	-	H	
Vertical Pulse width	t _{vp}	2	10	-	H	
Vertical Back porch	t _{vb}	2	2	-	H	
DISP Setup Time	t _{diss}	10	-	-	ns	
DISP Hold Time	t _{dish}	10	-	-	ns	
Clock Period	PW CLK	66.7	-	-	ns	
Clock Pulse High Period	PWH	26.7	-	-	ns	
Clock Pulse Low Period	PWL	26.7	-	-	ns	
Hsync Setup Time	t _{hs}	10	-	-	ns	
Hsync Hold Time	t _{hh}	10	-	-	ns	

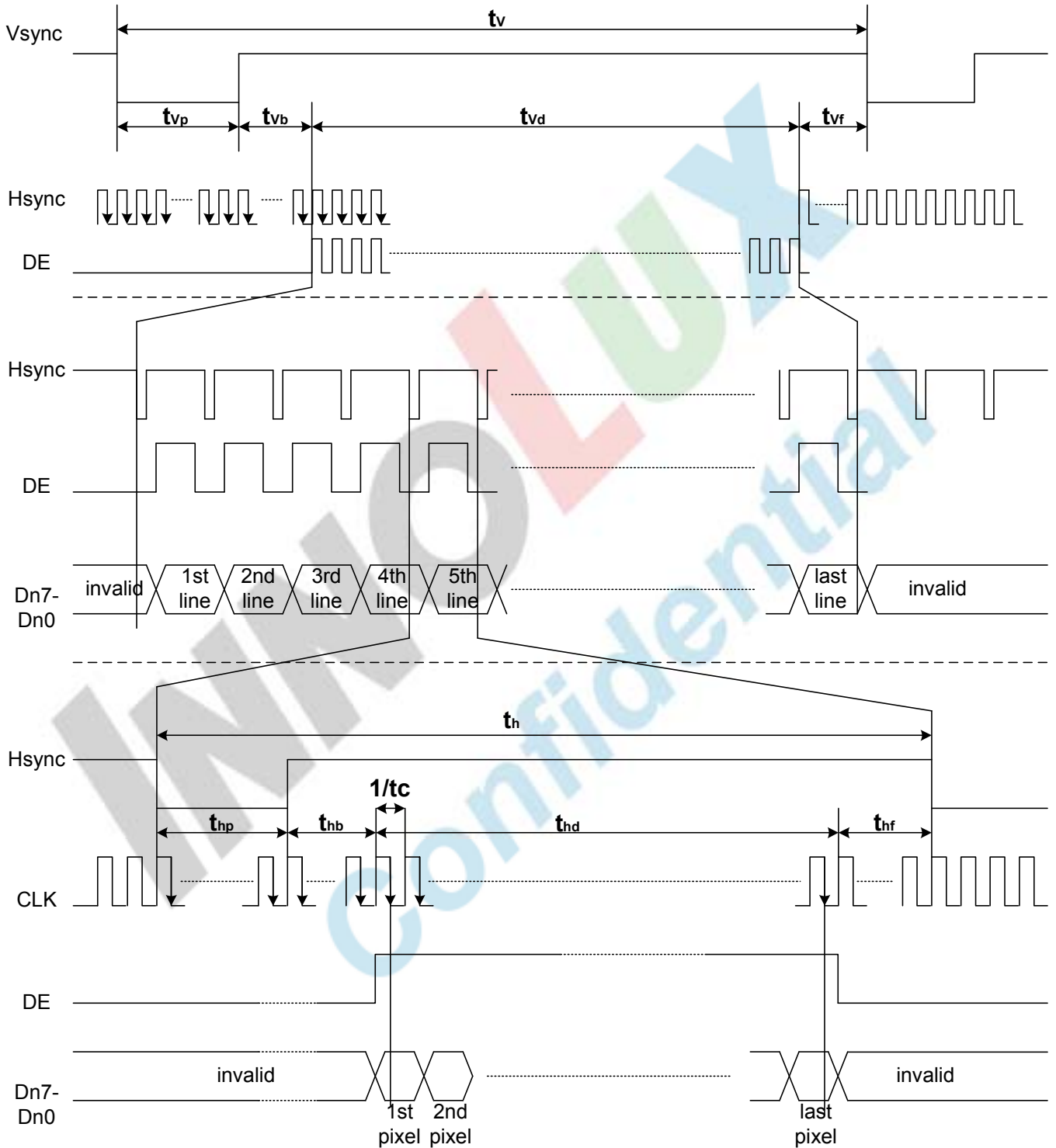
Data Setup Time	t _{ds}	10	-	-	ns	
Data Hold Time	t _{dh}	10	-	-	ns	
DE Setup Time	t _{des}	10	-	-	ns	
DE Hold Time	t _{desh}	10	-	-	ns	
Vsync Setup Time	t _{vhs}	10	-	-	ns	
Vsync Hold Time	t _{vhh}	10	-	-	ns	

Note 1: t_{hd}=480CLK, t_{hf}= 2CLK, t_{hp}= 41CLK, t_{hb}= 2CLK
 525CLK=480CLK + 2CLK + 41CLK + 2CLK

Note 2: t_{hf}+ t_{hp}+ t_{hb}> 44 CLK

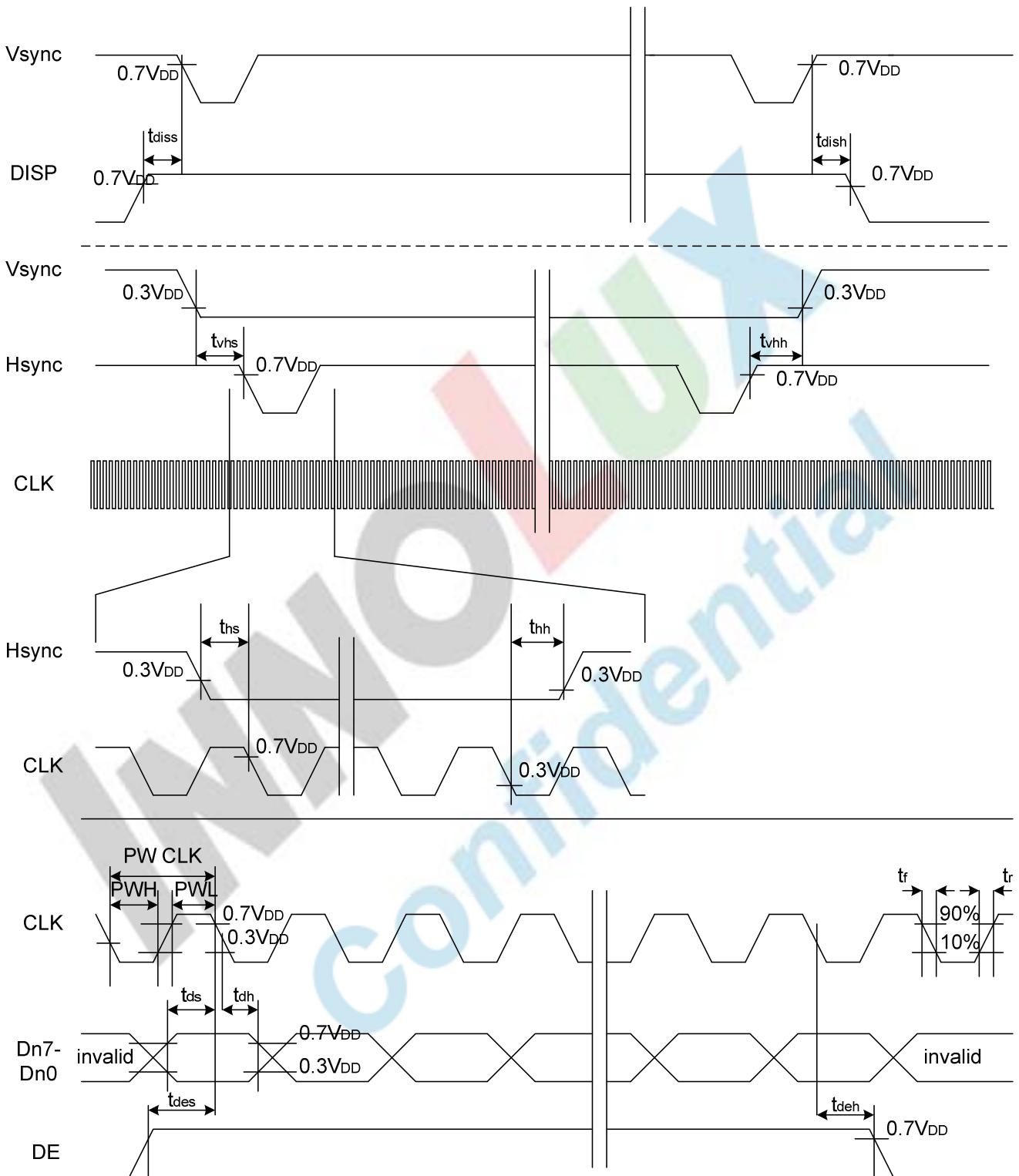


3.3.2. Timing Diagram



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4. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark
			Min.	Typ.	Max.		
Viewing angle (CR≥ 10)	θ_L	$\Phi=180^\circ$ (9 o'clock)	60	70	-	degree	Note 1
	θ_R	$\Phi=0^\circ$ (3 o'clock)	60	70	-		
	θ_T	$\Phi=90^\circ$ (12 o'clock)	40	50	-		
	θ_B	$\Phi=270^\circ$ (6 o'clock)	60	70	-		
Response time	T_{ON}	Normal $\theta=\Phi=0^\circ$	-	10	20	msec	Note 3
	T_{OFF}		-	15	30	msec	Note 3
Contrast ratio	CR		400	500	-	-	Note 4
Color chromaticity	W_X		0.26	0.31	0.36	-	Note 2 Note 5 Note 6
	W_Y		0.28	0.33	0.38	-	
Luminance	L		300	350	-	cd/m ²	Note 6
Luminance uniformity	Y_U		70	75	-	%	Note 7

Test Conditions:

1. $V_{DD}=3.3V$, $I_L=16mA$ (Backlight current), the ambient temperature is $25^\circ C$.
2. The test systems refer to Note 2.

Note 1: Definition of viewing angle range

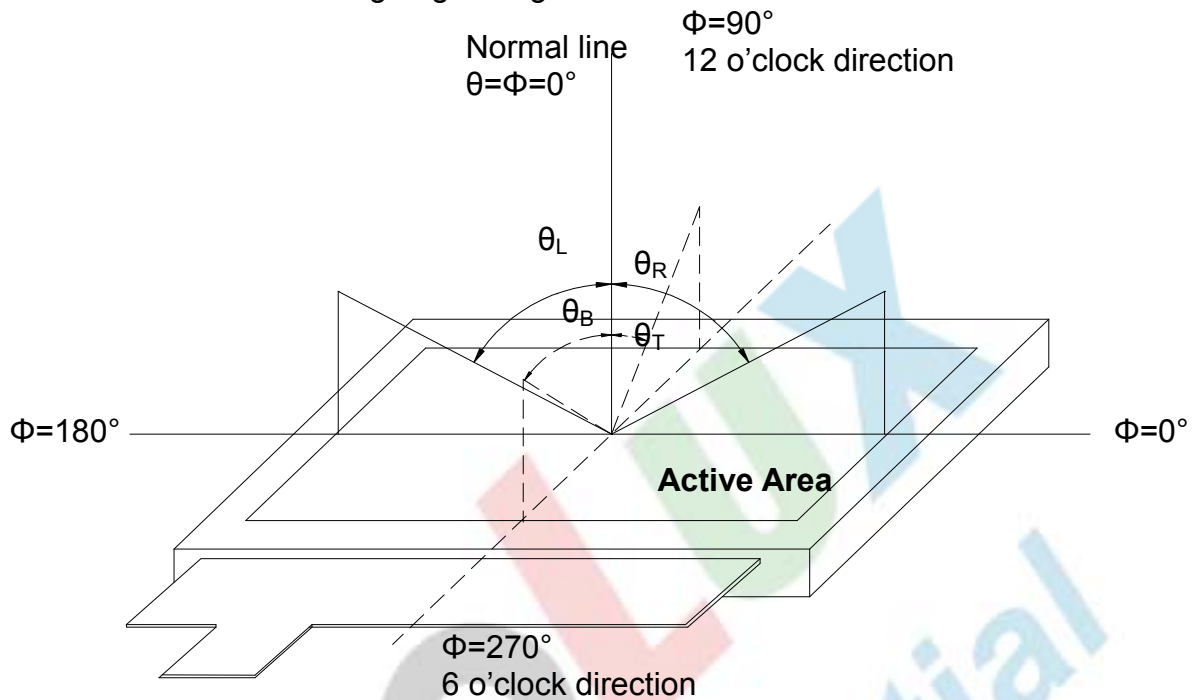


Fig. 4-1 Definition of viewing angle

Note 2: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

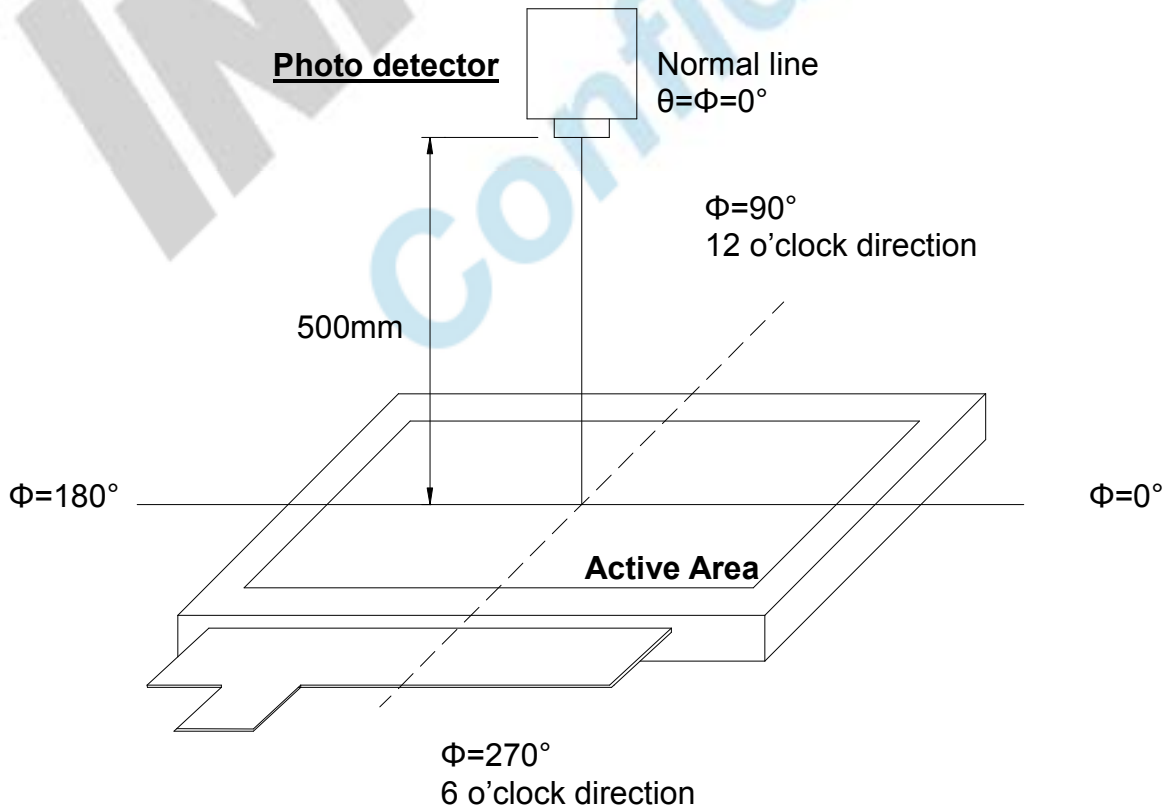


Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

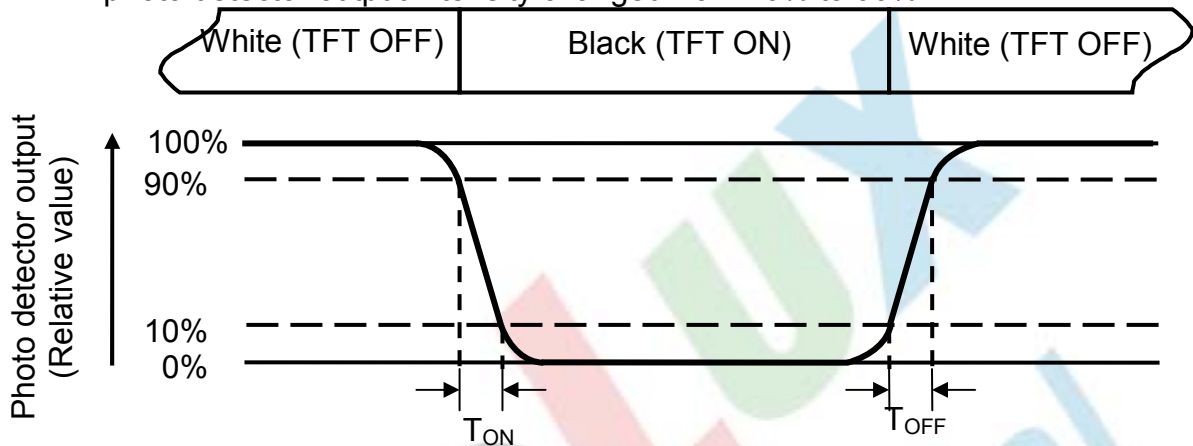


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is $I_L=16\text{mA}$ of which each LED module is 6 LED serial.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{B_{min}}{B_{max}}$$

L-----Active area length W----- Active area width

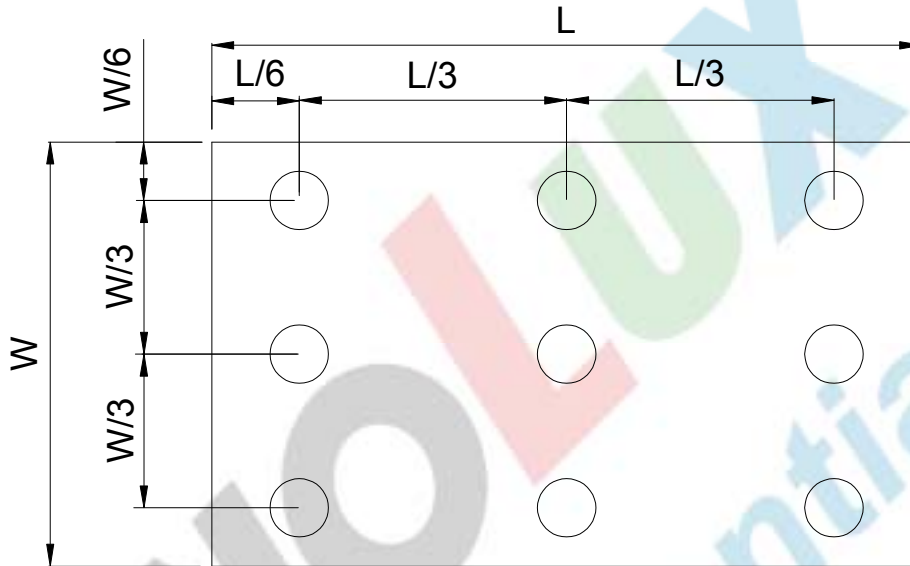


Fig. 4-4 Definition of measuring points

B_{max}: The measured maximum luminance of all measurement position.

B_{min}: The measured minimum luminance of all measurement position.

6. General Precautions

6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

6.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

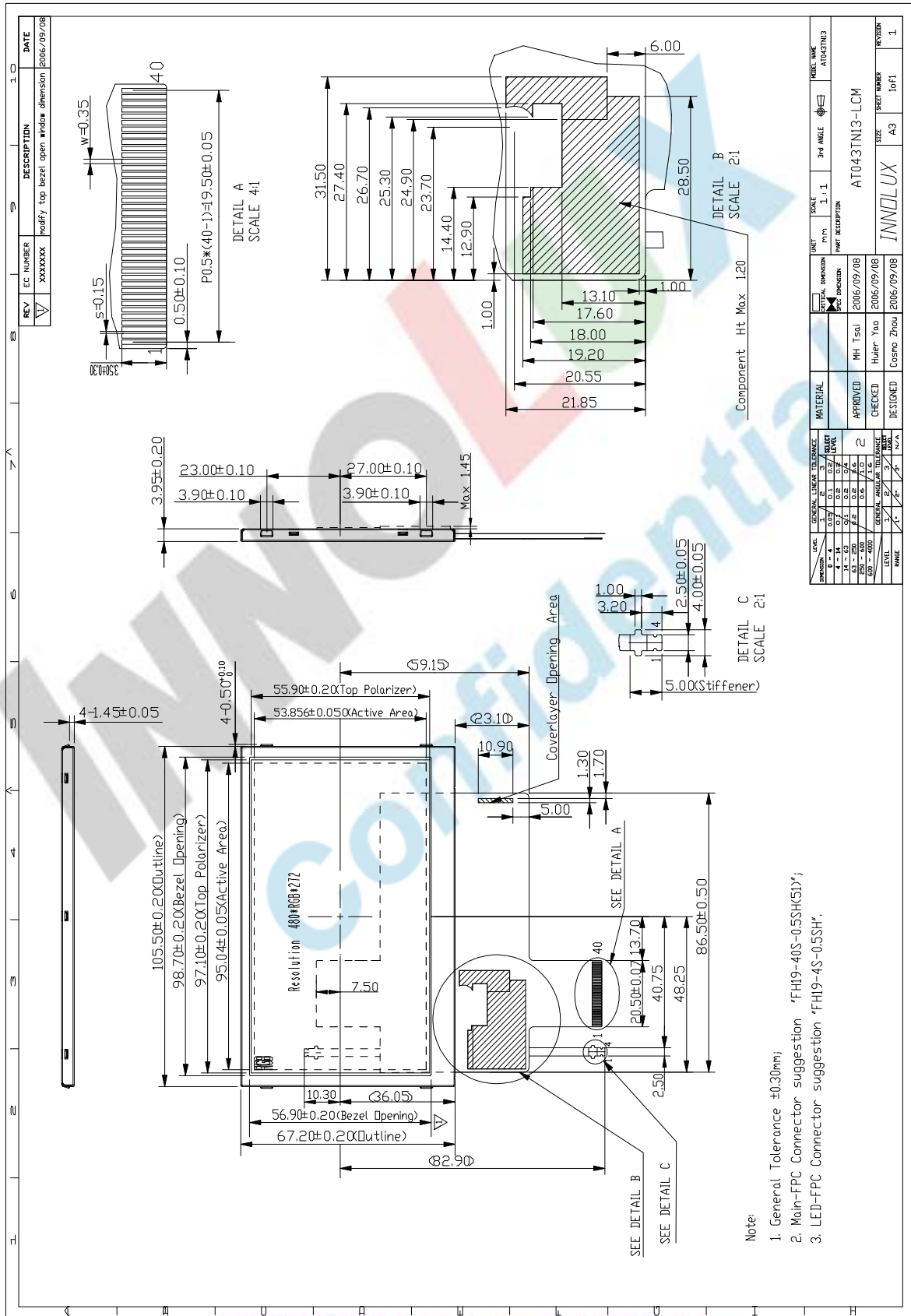
6.4. Storage

1. Store the module in a dark room where must keep at $25\pm 10^{\circ}\text{C}$ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

6.5. Cleaning

1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

7. Mechanical Drawing



8. Package Drawing

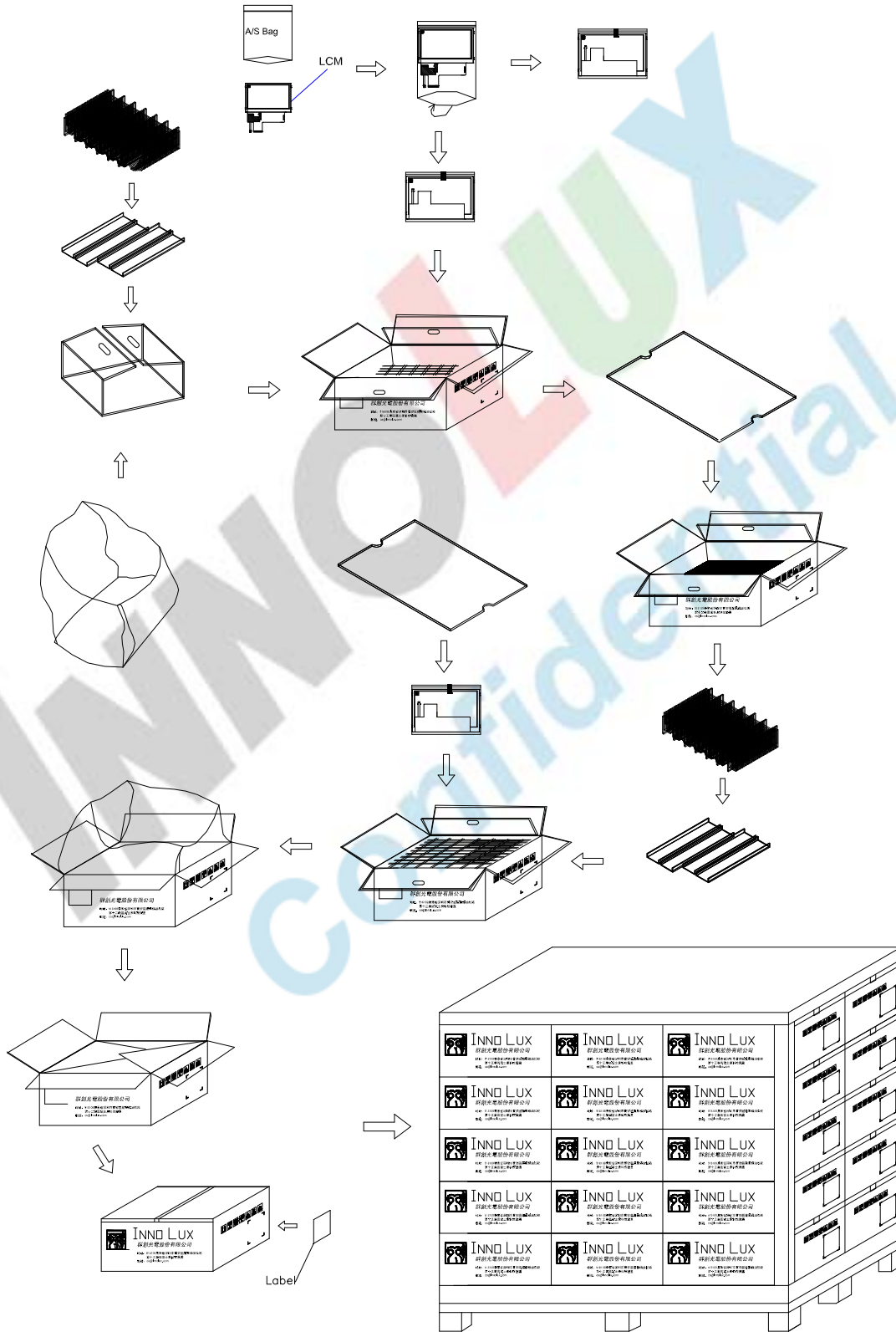
8.1. Packaging Material Table

No.	Item	Model (Material)	Dimensions(mm)	Unit Weight (kg)	Quantity	Remark
1	LCM Module	AT043TN13	105.5X67.2X3.95	0.052	160pcs	
2	Partition	BC Corrugated paper	512X349X106	1.102	2set	
3	Corrugated Bar	B Corrugated paper	349X173	0.030	8pcs	
4	Dust-Proof Bag	PE	700×530	0.060	1pcs	
5	Corrugated Board-1	BC Corrugated Paper	510×343	0.130	2pcs	
6	Corrugated Board-2	B Corrugated Paper	1152×512	0.260	1pcs	
7	A/S Bag	PE	132X117	0.002	160pcs	
8	Carton	Corrugated paper	530×355×255	1.100	1pcs	
9	Total weight	12.764± 5%Kg				

8.2. Packaging Quantity

(1) LCM quantity per Partition: 2Rows x 40quantity per Row = 80 pcs
(2) Total LCM quantity in Carton: 2 layer x 80 pcs per Partition = 160 pcs

8.3. Packaging Drawing



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